

Title (en)

METHOD AND DEVICE FOR AUTOMATICALLY ANALYSING A CIRCUIT DIAGRAM

Title (de)

VERFAHREN UND VORRICHTUNG ZUR AUTOMATISCHEN ANALYSE EINES SCHALTBILDES

Title (fr)

PROCÉDÉ ET DISPOSITIF D'ANALYSE AUTOMATIQUE D'UN SCHÉMA DE CÂBLAGE

Publication

**EP 4315280 A1 20240207 (DE)**

Application

**EP 22703881 A 20220110**

Priority

- DE 102021108281 A 20210331
- DE 2022100004 W 20220110

Abstract (en)

[origin: WO2022207027A1] A method for automatically analysing a circuit diagram, comprising: • - applying one or more filters to a circuit diagram in order to identify a circuit symbol defined by the respective filter in the circuit diagram (210); • - applying one or more detectors to the circuit diagram in order to identify connections between the circuit symbols (220); • - determining those identified circuit symbols that are coupled to one another by means of the identified connections (230, 310-340); and • - for each of the identified circuit symbols, using a data structure of the identified circuit symbol to store a reference to every further circuit symbol coupled to this identified circuit symbol (240). A corresponding device is also described.

IPC 8 full level

**G06V 30/422** (2022.01); **G06F 30/10** (2020.01); **G06F 30/12** (2020.01)

CPC (source: EP)

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