Title (en)
    LEVEL-AWARE CACHE REPLACEMENT

Title (de)
    EBENENBEWUSSTER CACHE-ERSATZ

Title (fr)
    REMPLACEMENT DE MÉMOIRE CACHE SENSIBLE AU NIVEAU

Publication
    EP 4371011 A1 20240522 (EN)

Application
    EP 22751611 A 20220711

Priority
- US 202163221875 P 20210714
- US 202217666429 A 20220207
- US 2022073591 W 20220711

Abstract (en)
    [origin: WO2023288192A1] An electronic device includes one or more processors and a cache that stores data entries. The electronic device transmits a request for translation of a first address to the cache. In accordance with a determination that the request is not satisfied by the data entries in the cache, the electronic device transmits the request to memory that is distinct from the cache, and receives data including a second address corresponding to the first address. In accordance with a determination that the data does not satisfy cache promotion criteria, the electronic device replaces an entry at a first priority level in the cache with the data. In accordance with a determination that the data satisfies the cache promotion criteria, the electronic device replaces an entry at a second priority level that is a higher priority level than the first priority level in the cache with the data including the second address.

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    G06F 12/1027 (2016.01); **G06F 12/123** (2016.01)

CPC (source: EP)
    G06F 12/1027 (2013.01); **G06F 12/123** (2013.01)

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Designated extension state (EPC)
    BA ME

Designated validation state (EPC)
    KH MA MD TN

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