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**EUROPEAN PATENT APPLICATION**

⑲ Application number: **78300250.4**

⑤① Int. Cl.2: **H 03 H 7/42**  
**// H04Q3/00**

⑳ Date of filing: **15.08.78**

③① Priority: **17.08.77 GB 34510/77**

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④③ Date of publication of application: **07.03.79**  
**Bulletin 79/5**

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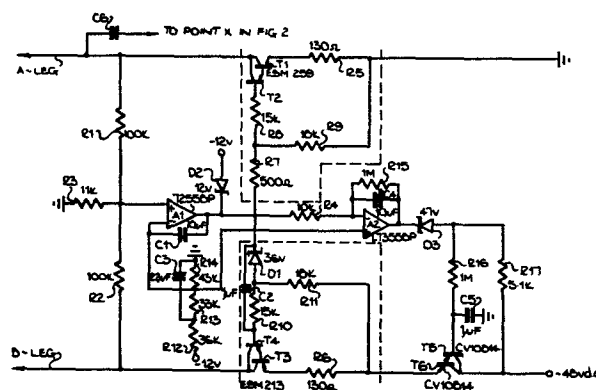
⑥④ Designated Contracting States: **DE FR NL SE**

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⑤④ **Unbalanced/balanced converter circuits.**

⑤⑦ Unbalanced/balanced converter circuit in which a first circuit (T1, T2) of controllable impedance is connected between a first supply terminal (earth) and a first balanced terminal (A-leg), a second circuit (T3, T4) of controllable impedance is connected between a second supply terminal (-48V d.c.) and a second balanced terminal (B-leg), and a control circuit (R, R2, A1) is connected to monitor any departure from balance of the potentials of the first and second balanced input/output terminals and to simultaneously change the impedances of the first and second circuits (T1, T2 and T3, T4 resp.) of controllable impedance in opposite directions to restore the potentials to balance. A first unbalanced terminal (X) is connected (via C6) to one balanced terminal (A-leg) and a second unbalanced terminal is connected to a common point (earth).

A principal use of the invention is balance/unbalanced conversion of a subscriber's telephone line.



This invention relates to circuits for connection between a balanced network and an unbalanced network to transfer signals back and forth between the balanced and unbalanced networks.

5 In the British Standards Institution "Glossary of Terms Used in Telecommunication and Electronics" B.S. 204: 1960 a balanced transmission line is defined as "A transmission line which is balanced electrically to earth and/or other conductors". The currently employed two-wire subscriber's telephone line is an example of a balanced transmission line although the potentials  
10 of the two wires are not symmetrically arranged with respect to earth potential since one pole of a central battery in an exchange to which the line is connected is earthed according to standard practice. It is not usual to include a power supply in the subscriber's telephone instrument and the central battery system  
15 is the standard way of supplying electrical power to telephone instruments. Thus, the standard two-wire subscriber's telephone line functions both as a means of feeding current to the associated telephone instrument to supply it with electrical power and as a balanced transmission line for transferring speech signals to and  
20 from the exchange.

In recent years, it has been proposed to transmit speech data between telephone exchanges in a pulse-coded digital form. For coding in a digital form, it is convenient to use speech signals which are not electrically balanced and therefore there is a need  
25 to have a circuit which can be used to derive both a balanced output signal from an unbalanced input signal and an unbalanced output signal from a balanced input signal. This function can be

achieved by a transformer but transformers have the disadvantage of being both bulky and expensive. Balanced active 2:4 wire converter circuits are known but generally require close matching of component values to maintain a properly balanced system.

5           It is an object of the invention to provide an improved circuit for connection between a balanced network and an unbalanced network.

          The present invention provides a circuit for connection between a network balanced to earth or other potential and an  
10           unbalanced network to transfer signals back and forth between the two networks, the circuit comprising:

          first and second supply terminals for connection to respective poles of an electrical supply,

          first and second balanced input/output terminals for connection  
15           to a balanced network,

          first and second unbalanced input/output terminals for connection to an unbalanced network,

          a first circuit of controllable impedance connected between the first supply terminal and the first balanced input/output  
20           terminal,

          a second circuit of controllable impedance connected between the second supply terminal and the second balanced input/output terminal, and

          a control circuit connected to monitor any departure from  
25           balance of the potentials of the first and second balanced input/output terminals and to simultaneously change the impedances of the first and second circuits of controllable impedance in opposite directions to restore the\_\_\_\_\_

potentials to balance, the first unbalanced input/output terminal being connected to one or other of the balanced input/output terminals and the second unbalanced input/output terminal being connected to a common point.

5           The control circuit and the first and second circuits of controllable impedance can be arranged to respond to frequencies embracing both dc. (direct current) and audio frequencies.

10           A further circuit of controllable impedance, but arranged to respond solely to d.c. and very low frequencies can be connected in series with one or other of the first and second circuits of controllable impedance and be arranged to regulate the direct current flowing from a d.c. power supply connected, in use, to the supply terminals and through a balanced network connected to the balanced input/output terminals. The further  
15           circuit of controllable impedance provides d.c. centralisation of the first and second circuits of controllable impedance and acts to attenuate noise on the supply terminals. This arrangement is of particular value in a telephone system as it can provide both regulated current line feed and balanced/unbalanced line conversion.

20           The control circuit can comprise a resistive network arranged to produce a potential representative of the sum of the potentials of the first and second balanced terminals and comparison means to compare the sum-representative potential with a reference potential.

25           The resistive network can comprise first and second resistors connected from one to the other of the first and second balanced input/output terminals and a third resistor connected from the junction of the first and second resistor to a point of fixed potential, the comparison means being connected to compare the potential at the said junction with the reference potential.

          The comparison means can comprise a differential amplifier

having one input connected to receive the sum-representative potential and its other input connected to a source of the reference potential.

5 Further comparison means can be connected to compare the output of the first-mentioned comparison means with a reference potential and can have an output connected to control the further circuit of controllable impedance. The source of reference potential can be common to both comparison means.

10 The further comparison means can comprise a differential amplifier having one input connected to the output of the first-mentioned comparison means and its other input connected to a or the source of the reference potential.

The first and second circuits of controllable impedance can comprise first and second transistors respectively, each  
15 transistor having its collector-emitter path connected between a respective one of the supply terminals and the associated balanced input/output terminal. The transistors can be of complementary conductivity type. Each transistor can have its base connected to the emitter of a respective further transistor and its collector  
20 connected to the collector of the respective further transistor to define a Darlington transistor pair.

The further circuit of controllable impedance can comprise a transistor having its collector-emitter path connected in series  
25 with one or other of the first and second circuits of controllable impedance.

The first and second balanced input/output terminals can be connected respectively to the A-leg and B-leg of a subscriber's

telephone line.

By way of example only, an illustrative embodiment of the invention will now be described with reference to the accompanying drawings, in which:

5           Figure 1 shows a line feed circuit embodying the invention, and

Figure 2 shows the circuit of a 2:4 wire converter for use with the line feed circuit of Figure 1.

10           The circuits shown are to be assumed to be located in a digital telephone exchange for example a digital PABX (private automatic branch exchange). Referring to Figure 1, the two wires connected to one telephone instrument served by the exchange are referenced A-leg and B-leg using the terminology common in telephone engineering. Two 1% 100 kilohm resistors R1 and R2 are connected  
15           in series from the A-leg to the B-leg. The junction of resistors R1 and R2 is connected to earth through a 1% kilohm resistor R3 and also to the non-inverting input of a type 72558P amplifier A1. The output of amplifier A1 is fed back to its inverting input through a 10 picofarad capacitor C1 and is connected to the  
20           inverting input of another type 72558P amplifier A2 through a 10 kilohm resistor R4.

25           The A-leg is also connected to the collector of a PNP transistor T1, the emitter of which is connected to earth through a 1% 130 ohm resistor R5. The collector of another PNP transistor T2 is connected to the collector of transistor T1 and the base of transistor T1 is connected to the emitter of transistor T2. Transistors T1 and T2 form a Darlington pair and are type ESM 259 available from

Thompson CSF.

The B-leg is also connected to the collector of an NPN transistor T3, the emitter of which is connected to one end of a 1% 130 ohm resistor R6. The collector of another NPN transistor T4 is connected to the collector of transistor T3 and the base of transistor T3 is connected to the emitter of transistor T4. Transistors T3 and T4 form a Darlington pair and are type ESM 213 available from Thompson CSF.

The output of amplifier A1 is connected to one end of a 500 ohm resistor R7, the other end of which is connected through a 15 kilohm resistor R8 to the base of transistor T2. An 18 kilohm resistor R9 is connected to earth from the junction of resistors R7 and R8.

The output of amplifier A1 is also connected to the cathode of a 36 volt  $\pm$  5% zener diode D1, the anode of which is connected to one end of another 15 kilohm resistor R10, and to the cathode of a 12 volt zener diode D2, the anode of which is connected to a - 12 volts supply. The other end of resistor R10 is connected to the base of transistor T4. A 1 microfarad capacitor C2 is connected from the cathode of zener diode D1 to the base of transistor T4. Another 18 kilohm resistor R11 is connected from the junction of zener diode D1 and resistor R10 to the end of resistor R6 remote from transistor T3.

A 36 kilohm resistor R12 has one end connected to a -12 volts supply and its other end connected to one end of a 33 kilohm resistor R13. The other end of resistor R13 is connected to earth through a 43 kilohm resistor R14. A 22 microfarad capacitor C3 is connected from the junction of resistors R12 and R13 to earth. The inverting input of amplifier A1 is connected to the junction of resistors R13 and R14.

The non-inverting input of amplifier A2 is also connected to the junction of resistors R13 and R14.

5 The output of amplifier A2 is fed back to its inverting input through the parallel combination of a 1 megohm resistor and a 10 microfarad capacitor C4. The output of amplifier A2 is also connected to the cathode of a 47 volt  $\pm$  5% zener diode D3, the anode of which is connected through a 1 megohm resistor R16 to the base of a PNP transistor T5. The emitter of transistor T5 is connected to the base of another PNP transistor T6, the collectors of transistors 10 T5 and T6 being commonly connected. The emitter of transistor T6 is connected to the end of resistor R6 remote from transistor T3. Both transistors T5 and T6 are type CV10814. 1 microfarad capacitor C5 is connected to earth from the base of transistor T5. A 5.1 kilohm resistor R 17 is connected from the anode of zener diode D3 to the 15 collectors of transistors T5 and T6. The collectors of transistors T5 and T6 are connected to a -48 volts supply.

One end of a capacitor C6 is connected to the A leg and its other end is connected to a point X in the circuit of Figure 2.

20 The circuit shown in Figure 2 is a 2:4 wire converter for providing an interface between a pair of input/output terminals (signal and earth, the "2 wires") and a pair of transmitting terminals (signal and earth, two of the "4 wires") and a pair of receiving terminals (signal and earth, the other two of the "4 wires"). The construction of 2: 4 wire converters is well known to those skilled in the art and 25 therefore the description of Figure 2 will be made brief.

The input/output terminals of the converter are referenced I/O, the transmitting terminals TX and the receiving terminals RX.



The receiving terminals RX are connected through an amplifier A3 to the junction of two equal exchange terminating impedances ZT. One of the impedances ZT has its other end connected to the input/output terminals connected to capacitor C6 and to one end of a resistor of resistance R. The other end of the other impedance ZT has its other end connected to another resistor of resistance R and to one end of a 2:4 wire balancing impedance ZB. The other end of impedance ZB is connected to earth. The other ends of the resistors of resistance R are connected through an amplifier A4 to the transmitting terminals TX. The impedance ZB is equal to one quarter the line impedance and R is high (typically 100 kilohm) compared with both ZT and ZB. The circuit of Figure 2 will not be described in further detail because it is a circuit of known type and included merely to show the use made of the circuit of Figure 1.

The operation and use of the circuit of Figure 1 will now be described.

The network consisting of resistors R12, R13 and R14 defines a reference potential applied to the inverting input of amplifier A1 and to the non-inverting input of amplifier A2. Amplifier A1 provides an output signal consisting of the amplified difference in potential between the potential at the junction of the resistors R1 and R2 and the reference potential. Amplifier A2 provides an output signal consisting of the amplified difference in potential between the reference potential and the output signal of amplifier A1. The output signal of amplifier A1 controls the Darlington pairs T1, T2 and T3, T4, a negative-going signal lowering the impedance of the collector - emitter path of transistor T1 and raising that of transistor T2 and

vice versa for a positive-going signal. The amplifier A1 will produce a negative-going output signal if the potential at the junction of the resistors R1 and R2 becomes more negative but the effect of the impedance of transistor T1 falling and that of T2 rising is to make the junction potential less negative. Thus, the potential at the junction of resistors R1 and R2 tends to a value equal to the reference potential.

The output of amplifier A2 controls the collector-emitter impedance of transistor T6, a positive-going output signal raising the impedance and a negative -going signal lowering it. Such a positive-going signal will arise from the potential at the junction of resistors R1 and R2 becoming more negative and thus the overall effect is again to cause the junction potential to tend to a value equal to the reference potential.

The resistors R1, R2 and R3 are chosen so that when the junction potential is equal to the reference potential, the mean of the potential of the A - and B- legs is equal to one half of the supply voltage, that is -24 volts. Furthermore, the component values and parameters are such that the loop current through the A- and B- legs is equal to 25 milliamps when the junction potential equals the reference potential. The voltage output from amplifier A1 applied via components R7, R8 and D1, R10 (acting as biasing chains) to the Darlington transistors T1, T2 and T3, T4 are primarily responsible for setting the current level in conjunction with the emitter resistors R5 and R6. Amplifier A1 and transistors T1, T2, T3 and T4 have a good response to audio frequencies and d.c. Amplifier A2 and transistors T5 and T6 driven via components D3 and R16 (acting as a biasing chain) centralise the first two circuits of controllable impedance (T1, T2; T3, T4) to ensure equal voltage drops from the A-leg output to earth and from the B-leg output to -48 volts. The components R15, C4, R16 and C5 severely curtail the frequency response of amplifier A2 and transistors T5 and T6 so that they respond solely to d.c. and very low frequencies.

It has already been explained that the circuit tends to maintain the potential at the junction of resistors R1 and R2 constant and it can be seen that a signal voltage applied between the A-leg (say) and earth will disturb the junction potential. The circuit will react to such a signal voltage by attempting to restore the junction potential to the value of the reference potential. If the signal voltage is a signal of audio frequency, the transistor T6 will not respond by virtue of the very limited frequency response associated with it but the impedance of one of the transistors T1, T3 will rise while that of the other falls to restore the junction potential to the reference potential. Thus, if a positive going potential is applied to the A-leg the circuit will react by making the A-leg less positive and the B-leg more positive until the mean potential of A - and B- legs is once more -24 volts. The effect is that a signal applied to the A-leg with respect to earth becomes, through the operation of the circuit, a differential signal on the A- and B- legs.

The circuits of Figure 1 provides a dual function of supplying a regulated direct current to the telephone line and also of transferring signals back and forth between the unbalanced converter circuit and the balanced subscriber's line. It has an impedance scaling property in that the A-leg to B-leg line impedance when seen from the point X appears to be multiplied by a factor  $\frac{1}{4}$ .

An advantage of regulating the current fed to subscriber's lines is that the peak power supply requirements of an exchange can be substantially reduced because the maximum current to be supplied is known with more certainty than is the case with unregulated line feed

circuits. A further advantage is that the loudness regulators in the telephone instruments are not brought into operation which helps to keep the apparent source and load impedances of the telephone instruments constant. Loudness regulators are attenuators  
5 contained in telephone instruments which are brought into operation when the energising current is above a threshold value. Even with the regulator disabled the telephone impedance is still dependent upon line feeding current but the present circuit is designed to supply a regulated 25 milliamps to a 1400 ohm loop. This current  
10 is below the threshold commonly used for loudness regulators. It is important to know the line and telephone instrument impedance accurately when using a 2:4 wire converter circuit in a digital switching system as otherwise side tone performance can be impaired. Line and telephone instrument impedance can be determined more  
15 accurately with the present circuit because the loudness regulators are prevented from operating and the telephone instrument is driven with a constant current. Where loudness regulation is still desired to compensate for different line lengths it can be accomplished in the four-wire part of the circuit in the exchange.  
20 It is possible to omit amplifier A2, transistors T5, T6 and associated components and the circuit of Figure 1 will continue to function as a converter between balanced and unbalanced signals but at a loss of some degree of control over the centralisation of circuit operation and rejection of noise on the -48 volt supply  
25 line.

1. A circuit for connection between a network balanced to earth or other potential and an unbalanced network to transfer signals back and forth between the two networks, the circuit comprising:

- 5 first and second supply terminals for connection to respective poles of an electrical supply,
- first and second balanced input/output terminals for connection to a balanced network,
- first and second unbalanced input/output terminals
- 10 for connection to an unbalanced network, characterised in that
- a first circuit (T1, T2) of controllable impedance is connected between the first supply terminal (earth) and the first balanced input/output terminal (A-leg),
- 15 a second circuit (T3, T4) of controllable impedance is connected between the second supply terminal (-48V) and the second balanced input/output terminal (B-leg) and
- a control circuit (R1, R2, A1) connected to monitor
- 20 any departure from balance of the potentials of the first and second balanced input/output terminals and to simultaneously change the impedances of the first and second circuits of controllable impedance in opposite directions to restore the potentials to balance, the
- 25 first unbalanced input/output terminal (X, C6) being

connected to one or other of the balanced input/output terminals and the second unbalanced input/output terminal being connected to a common point (earth).

2. A circuit as claimed in claim 1, further characterised in that the control circuit (R1, R2, A1) and the first and second circuits of controllable impedance (T1, T2; T3, T4) are arranged to respond to  
5 frequencies embracing both dc. (direct current) and audio frequencies.

3. A circuit as claimed in claim 2, further characterised in that a further circuit (T5, T6) of controllable impedance, but arranged to respond solely to d.c. and very low frequencies, is connected in  
5 series with one or other of the first and second circuits of controllable impedance (T1, T2; T3, T4) and is arranged to regulate the direct current flowing from a c.c. power supply (earth, -48v) connected to the supply terminals and through a balanced network  
10 (telephone line) connected to the balanced input/output terminals.

4. A circuit as claimed in any preceding claim, further characterised in that the control circuit comprises a resistive network (R1, R2, R3) arranged to produce a potential representative of the sum of the  
5 potentials of the first and second balanced terminals

(A-leg, B-leg) and comparison means (A1) to compare the sum-representative potential with a reference potential.

5. A circuit as claimed in claim 4, further characterised in that the resistive network comprises first and second resistors (R1, R2) connected from one to the other of the first and second balanced input/output terminals and a third resistor (R3) connected from the junction of the first and second resistor to a point of fixed potential, the comparison means (A1) being connected to compare the potential at the said junction with the reference potential.

6. A circuit as claimed in claim 5, further characterised in that the comparison means comprises a differential amplifier (A1) having one input (+) connected to receive the sum-representative potential and its other input (-) connected to a source (R12, R13, R14) of the reference potential.

7. A circuit as claimed in claim 4, 5 or 6 and including the features of claim 3, further characterised in that further comparison means (A2) is connected to compare the output of the first-mentioned comparison means (A1) with a reference potential and has an output connected to control the further circuit (T5, T6) of controllable impedance.

8. A circuit as claimed in claim 7, further characterised in that the <sup>source of reference</sup>potential (R12, R13, R14) is common to both comparison means.

9. A circuit as claimed in claim 7 or 8, further characterised in that the further comparison means comprises a differential amplifier (A2) having one input (-) connected to the output of the first-mentioned  
5 comparison means and its other input (+) connected to a or the source of the reference potential.

10. A circuit as claimed in any preceding claim, further characterised in that the first and second circuits of controllable impedance comprise first (T1) and second (T3) transistors respectively, each  
5 transistor having its collector-emitter path connected between a respective one of the supply terminals and the associated balanced input/output terminal.

11. A circuit as claimed in claim 10, further characterised in that the transistors (T1, T3) are of complementary conductivity type.

12. A circuit as claimed in claim 10 or 11, further characterised in that each transistor (T1, T2) has its base connected to the emitter of a respective further transistor (T2, T4) and its collector connected to the  
5 collector of the respective further transistor to define a Darlington transistor pair.

13. A circuit as claimed in claim 3 taken alone or in combination with any of claims 4 to 12, further characterised in that the further circuit of controllable impedance comprises a transistor (T6) having its  
5 collector-emitter path connected in series with one or



other of the first and second circuits of controllable impedance.

14. A circuit as claimed in any preceding claim, further characterised in that the first and second balanced input/output terminals are connected respectively to the A-leg and B-leg of a subscriber's
- 5 telephone line.



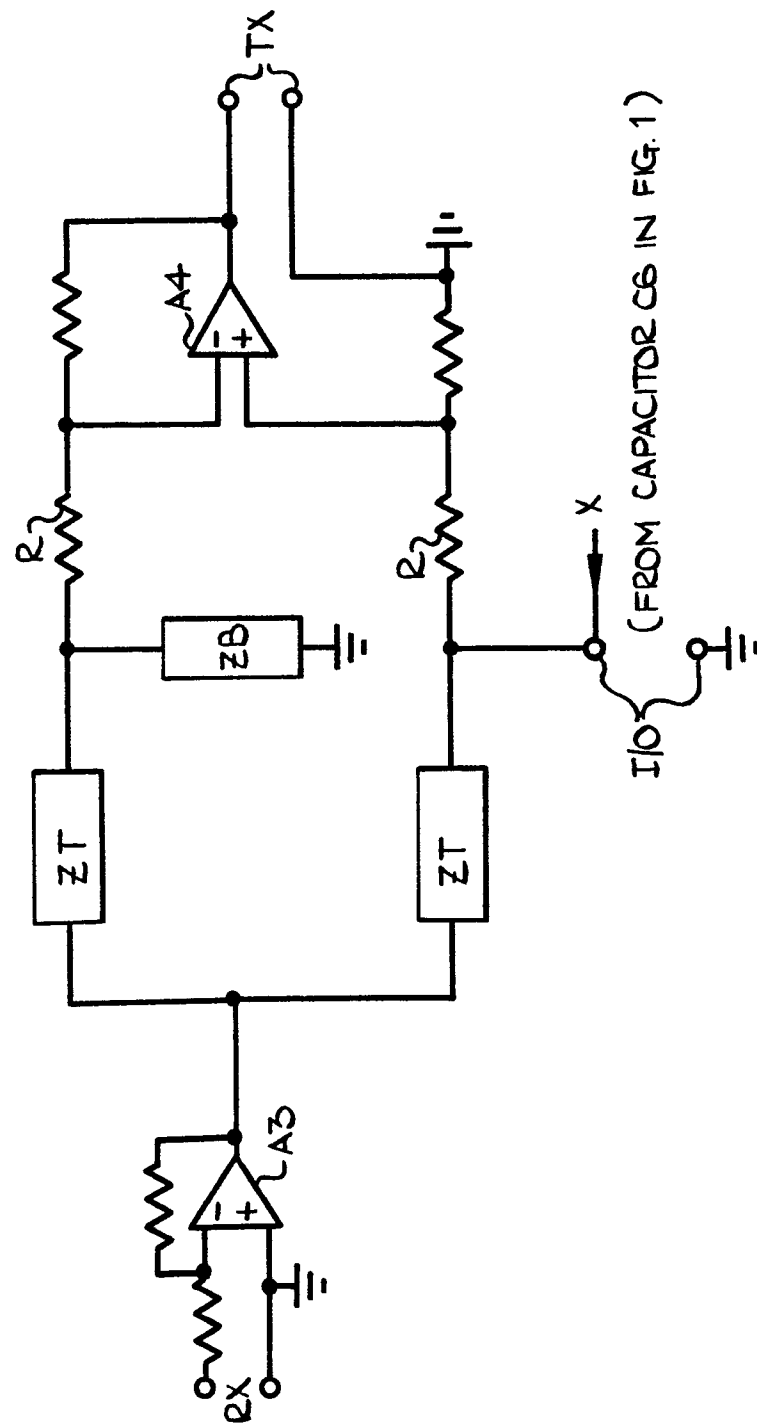


FIG. 2.



European Patent  
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# EUROPEAN SEARCH REPORT

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Application number

EP 78 30 0250

DOCUMENTS CONSIDERED TO BE RELEVANT			CLASSIFICATION OF THE APPLICATION (Int. Cl. <sup>2</sup> )
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	
P	US - A - 3 955 052 (SHELOMO ORBACH) * Figures 3,4; from column 4, line 45 to column 6, line 8 *	1,2,4,5,10,11	H 03 H 7/42 H 04 Q 3/00
	FR - A - 2 373 201 (DEUTSCHE TELEPHONWERKE UND KABELINDUSTRIE AKTIENGESELLSCHAFT) * Figures; from page 2, line 20 to page 3, line 3; from page 4, line 18 to page 5, line 11 *	1,2,4,5,10,11,14	
			TECHNICAL FIELDS SEARCHED (Int.Cl. <sup>2</sup> )
			H 03 H 7/42 H 04 Q 3/00 H 04 B 3/30 1/58 H 04 M 19/00
			CATEGORY OF CITED DOCUMENTS
			X: particularly relevant A: technological background O: non-written disclosure P: intermediate document T: theory or principle underlying the invention E: conflicting application D: document cited in the application L: citation for other reasons
			&: member of the same patent family, corresponding document
The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
The Hague	25-11-1978	COPPIETERS	