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(71) Applicant: **STELLAR SYSTEMS, INC.**
231 Charcot Avenue
San Jose California 95131(US)

(72) Inventor: **Mongeon, Ronald W.**
1034 Westwood Drive
San Jose California(US)

(74) Representative: **Horton, Andrew Robert Grant et al,**
BOULT, WADE & TENNANT 27 Furnival Street
London, EC4A 1PQ(GB)

(54) **E field balanced phase intrusion alarm.**

(57) An intrusion alarm is provided having one or more pairs of sense wires wherein each of a pair of sense wires (13,15) leads to a balanced circuit (19) whereby there is no alarm output if both of the sense wires are subjected to the same perturbation. Thus, false nuisance alarms are avoided.

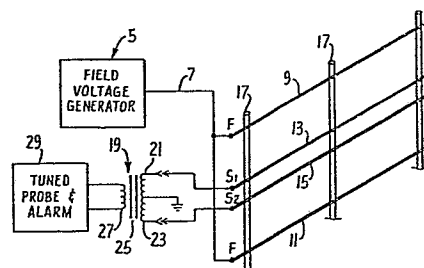


FIG. 1.

"E FIELD BALANCED PHASE INTRUSION ALARM"

The present invention relates to an improvement in intrusion alarms known as E field alarms and specifically
5 relates to improvements on the intrusion alarms of U.S. Patents 3,237,105 and 4,064,499, the subject-matter of which is incorporated herein by reference.

In the intrusion alarms described in those patents a field wire acts as a transmitting antenna at a
10 relatively low frequency to produce a quasi-stationary electric field. One or more sense antennas are placed in the vicinity of the field wire and connected to filtering and amplifying circuits leading to an alarm. In a practical embodiment of such an alarm, an AM detector
15 is connected to the output of a preamplifier and the output is connected to a low-pass filter and to high-gain amplifying means, including a band pass filter arrangement whereby the output of the high gain amplifier passes only signals within the relatively low frequency range
20 associated with the movement of an intruder in the electric field created between the field wire and the sensing antenna.

Although this is a very satisfactory system, in some applications it may be subject to false triggering.
25 For instance, such alarm systems are ordinarily placed in the vicinity of a fence and fence movements which might be

caused by wind or other natural phenomena as well as lightning or other forms of electrical interference may cause a false alarm. Large "friendly" objects such as trucks can also cause a false alarm.

5 In accordance with the present invention, a system is provided which is much less susceptible to false alarms by providing at least one pair of field wires and at least one pair of sense wires. Each pair of sense wires is connected to opposite terminals of an input circuit for
10 the alarm device in such a manner that if equal voltages of equal phase are developed on the two sense wires, there will be no output. Since both of the sense wires would normally be subject to the same electrical disturbances such as movements of an adjacent fence,
15 trucks or lightning, such disturbances would not cause an alarm. On the other hand, in the case of an intruder, invariably a greater voltage would be developed on one of the sense wires than on the other setting off the alarm. Thus, the system would still remain sensitive to set off
20 an alarm in the case of an intrusion but would be less susceptible to nuisance alarms.

In the accompanying drawings:

Figure 1 is a simplified diagram showing how the present invention might be used as a free-standing
25 perimeter security system;

Figure 2 is a fragmentary diagram, similar to Figure 1, showing how the system of the present invention could be applied to an existing fence;

Figure 3 is a fragmentary diagram of a curtain
30 array system; and

Figure 4 is a schematic diagram showing a practical filtering and phasing circuit for carrying out the present invention.

In practical embodiments of the invention circuits would be included for sensing whether or not wires were open or short-circuited. The alarm would include circuits to detect amplitude changes in the field, the rate of change and the time that the intruder is in the field pattern to fit the typical intruder profile. However, these various features of a practical circuit are well-known to those skilled in the art and form no part of the present invention and therefore are not herein described.

Referring now to the drawings by reference characters, there is shown a field voltage generator 5 which may be identical with those parts identified as items 11, 12 and 23 in U.S. Patent 4,064,499. The field voltage generator has an output line 7 which feeds an upper field wire 9 and a lower field wire 11. Running parallel to the field wires 9 and 11 are the sense wires 13 and 15. All of these wires are supported on the posts designated 17 and would be insulated from the posts.

The sense wire 13 is parallel to the field wire 9 and similarly, the field wire 11 is parallel to the sense wire 15. Sense wires 13 and 15 have output terminals designated S1 and S2 connected to opposite ends 21 and 23 of the primary transformer 19 having a grounded centre tap. The transformer has effectively two primaries wound in the same direction on a common core 25. Although the two primary windings are shown for illustrative purposes as being a single centre tapped winding, in a practical embodiment of the invention, the two primary windings may be separate. The secondary 27 is connected to the input terminals of a tuned probe and alarm circuit 29. Item 29 can be substantially identical with the items designated 14 to 22 of U.S. Patent 4,064,499.

It is apparent that if equal voltages are induced on the sense lines 13 and 15 that there will be no output on the secondary 27; equal voltages of the same polarity and phase applied at opposite ends of the windings would
5 oppose each other so that there would be no output. Thus, with a system such as this if the two sense wires are subjected to the same perturbation such as a large vehicle, lightning or electrical disturbance or the movement of an adjacent fence, no false alarm would be produced by the
10 circuit. On the other hand, an intruder trying to climb over, under or through the security system would cause a greater disturbance on one sense wire than the other, causing an alarm to be set off.

In Figure 2, a somewhat similar installation is
15 shown except here the installation is designed for use with an existing fence. Thus, with the existing fence posts 31 supporting a fence, not shown, one provides a plurality of standoff arms 33. These arms can advantageously be made of an insulating material such as
20 fibreglass or they can be made of a conductive material with insulators at the ends. Standoffs 33 support the wires 9, 13, 15 and 11 in exactly the manner previously described.

Figure 4 shows how several field and sense wires
25 can be combined to form a curtain array. Note that in the centre of the array, field wire F2 serves two sense wires, namely S4 and S5.

In Figure 4 a practical filter and probe circuit is shown. Here a transformer T1 is employed, having equal
30 primary windings 35 and 37 and a secondary winding 39. In a practical embodiment of the invention, an ordinary telephone hybrid transformer was employed but it will be understood that the transformer is not critical so long as

it is capable of handling the frequencies of the alarm circuit and also matching the impedances of the sensing circuit to the alarm circuit. The sensing wire S1 is attached to one side of the primary 35 while the sensing wire S2 is connected to the opposite terminal of the primary 37 so that voltages developed on S1 and S2 oppose each other. Both of the input lines are advantageously provided with lightning arresters LA1 and LA2. The opposite sides of the primary windings are grounded for AC through the capacitors C5 and C6. The output winding 39 of the transformer is connected to a filter C1, C3 and L1 tuned to the frequency of the field voltage generator. The output of the filter is by-passed to ground for stray RF fields through the capacitor C4. The output signal from the circuit is taken through line 41. R3 forms part of a DC supervisory circuit for detecting an open or short on sense wire S2. In a similar manner, line 42 serves to monitor for an open or short on sense wire S1. The circuit shown in Figure 4 is substituted for the tuned probe circuit 14 of Patent 4,064,499 and the output is passed to the preamplifier 15 of said patent so that the further processing of the signal is not herein described.

Although certain specific circuitry has been shown, it will be understood that this is for purposes of illustration only. For instance, instead of employing the transformer with the split primary, one can merely pass the output from the two sensing wires to the inverting and non-inverting inputs of a differential amplifier. Further, many other configurations can be employed rather than simple configurations shown.

CLAIMS:

1. In an intruder alarm system wherein a field wire creates an electric field and a sense wire is provided parallel to said field wire and wherein a sensitive amplifying and filtering circuit detects perturbations on said sense wire resulting from the presence of an intruder intersecting the field between the field wire and the sense wire, the improvement comprising:
- a. at least two field wires, said field wires being spaced from each other and running generally parallel to each other;
 - b. sense wires between said field wires for sensing the field created between each sense wire and each field wire, there being at least two sense wires, said sense wires being spaced from corresponding field wires whereby a common disturbance creates equal voltages on said sense wires, said sense wires being connected to a balanced circuit having an input leading to a filtering and detection system whereby equal voltages on said sense wires will counteract each other and said detection system is actuated only by unequal voltages developed in said two sense wires and;
 - c. said field and sense wires being arranged in a substantially vertical array.

2. The alarm system of claim 1 having one pair of spaced field wires and one pair of sense wires, said sense wires being located between said field wires.

3. The alarm system of claim 1 having at least one field wire common to two sense wires.

4. The alarm system of claim 1 in the form of a curtain array with at least three spaced field wires and with sense wires located between said field wires.

5 5. The alarm system of claim 1 wherein the balanced circuit includes a transformer having two effective primaries and a secondary wherein sense wires are connected to said primaries in such a manner that voltages developed on said sense wires oppose
10 each other whereby equal input voltages cause no output voltage on said secondary.

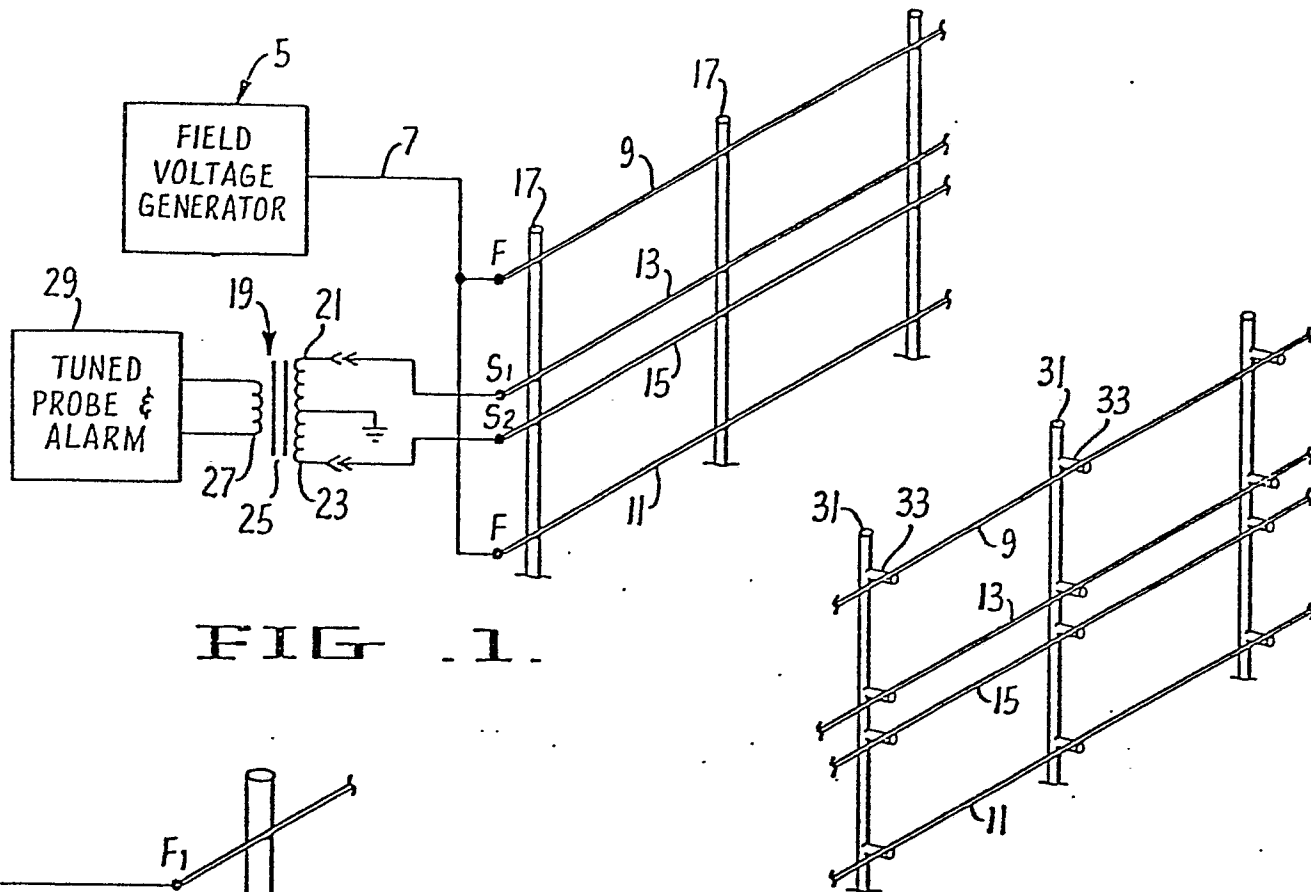


FIG. 1.

FIG. 2.

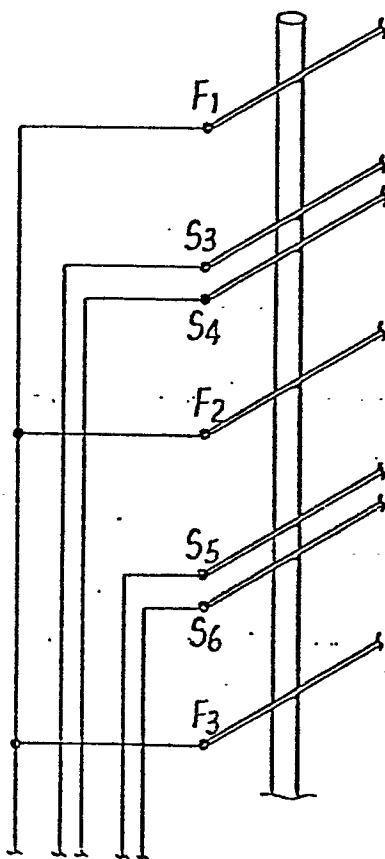


FIG. 3.

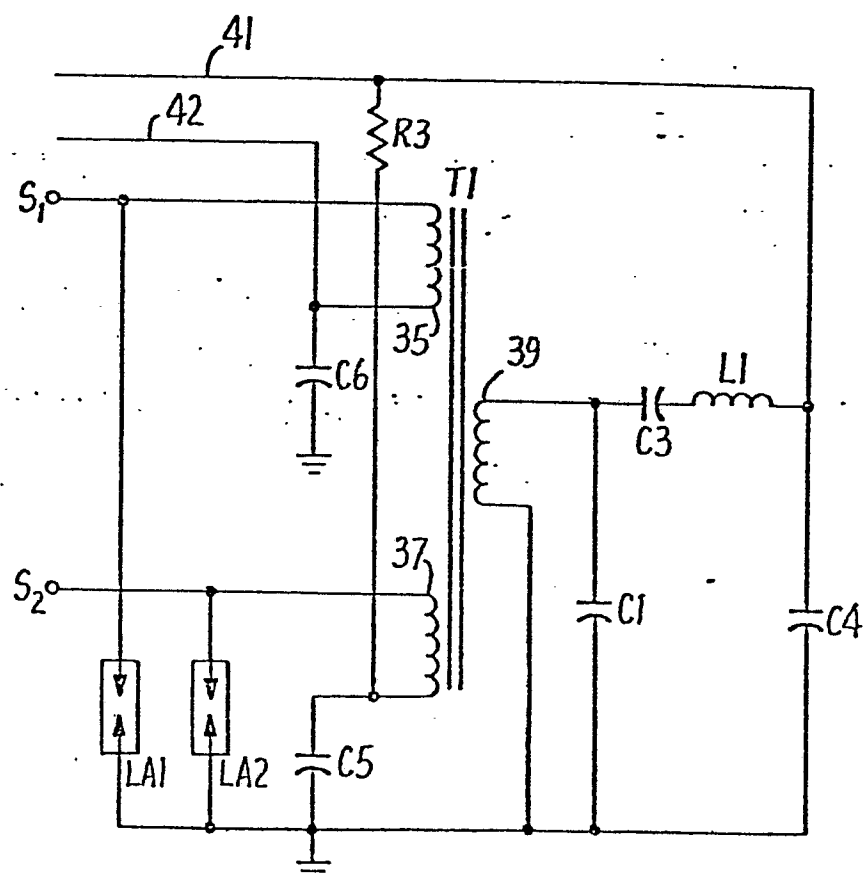
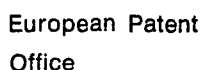


FIG. 4.



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