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(12) # EUROPEAN PATENT APPLICATION

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(54) **Microprocessor systems for electronic postage meters.**

(57) An electronic postage meter has an accounting unit with redundant nonvolatile random access memories (20, 21) controlled by a microprocessor (10). The redundant random access memories (20, 21) have separate groups of address (22, 24) and data lines (23, 25) to minimize identical errors in data stored therein. Data transfer may occur at different times to and from the memories, with respect to any given byte of data. The system may incorporate redundant microprocessors, and critical parameters may be checked at periodic intervals in the main program oif the accounting microprocessor system.

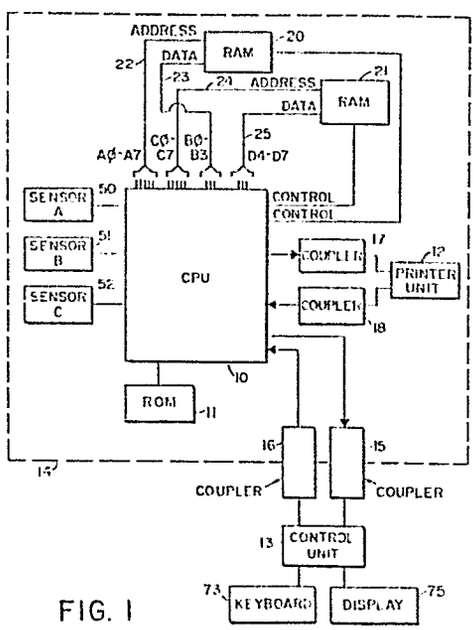

FIG. 1

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EUROPEAN SEARCH REPORT

0085385
Application number

EP 83 10 0639

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. ³)
Y	EP-A-0 019 515 (FRIDEN MAILING) * Page 3, line 5 - page 5, line 27; page 6, line 27 - page 9, line 6; page 10, lines 10-20; page 18, line 12 - page 19, line 6; page 19, line 28 - page 20, line 31; page 33, line 31 - page 35, line 16; page 40, line 15 - page 42, line 21; claims 1-9; figures 3,11a-11c,18 *	1,4,5, 9	G 07 B 17/02 G 06 F 11/16
X		8	
A		2,3,5, 10,11, 13-16	
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Y	DE-A-3 024 370 (SIEMENS) * Page 5, line 32 - page 6, line 12; figure 1 *	1,4,5, 9	G 07 B G 06 F

Y	IBM TECHNICAL DISCLOSURE BULLETIN, vol. 10, no. 10, March 1968, pages 1484,1485, New York, USA; F.J. HACKL et al.: "Dynamic allocation of high availability storage" * Whole document *	1,4,9	
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The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
THE HAGUE	06-08-1984	MEYL D.

CATEGORY OF CITED DOCUMENTS

X : particularly relevant if taken alone
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A	IBM TECHNICAL DISCLOSURE BULLETIN, vol. 10, no. 10, March 1978, pages 4071-4072, New York, USA; M.E. CHAMOFF et al.: "Nonvolatile totals implementation" * Whole document *	1,3,4, 8,9	
A	GB-A-2 079 223 (RONEO ALCATEL) * Abstract; claims 20-25; figures 10,11 *	1,2,4, 8-11, 13-16	
A	EP-A-0 026 734 (LICENTIA) * Page 5, line 6 - page 9, line 7; claims 1,4-6; figures *	1,5,9	
A,D	US-A-4 301 507 (SODERBERG et al.) * Abstract; column 1, line 26 - column 2, line 48; column 14, line 55 - column 15, line 65; figures *	1,2,4, 5,8-10 ,13-16	TECHNICAL FIELDS SEARCHED (Int. Cl. ³)

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DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int Cl. ³)
A	PROCEEDINGS OF THE IEEE, vol. 66, no. 10, October 1978, pages 1146-1158, New York, USA; D. KATSUKI et al.: "Pluribus - An operational fault-tolerant multiprocessor" * Page 1148, column 1, line 1 - column 2, line 16; page 1151, column 2, line 21 - page 1152, column 1, line 15; figures *	1,4,5, 9	
A	IEEE TRANSACTIONS ON RELIABILITY, vol. R-22, no. 1, April 1973, pages 2-12, New York; USA; S.A. SZYGENDA et al.: "Self-diagnosis and self-repair in memory: An integrated system approach"		
A	IEE PROCEEDINSG-A, vol. 128, no. 4, May 1981, pages 257-272, Old Woking, Surrey, GB; P.G. DEPLEDGE: "Fault-tolerant computer systems"		TECHNICAL FIELDS SEARCHED (Int. Cl. ³)

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