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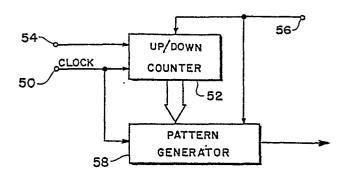
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Filtering and frequency detection circuits for delta modulated signals.

57 High and low pass filters and frequency detection circuitry responsive to the pulse train output of a linear delta modulator which generates a stream of positive and negative going output pulses, the average of which correspond to an analog input signal to said delta modulator. The filter circuits (52, 58) determine the slope of the output pulse average by counting the number of positive (1) and negative (0) going pulses during a predetermined interval and alters the sign of certain output pulses if the slope is higher than a predetermined slope value in the case of a low pass filter but lower than a predetermined value in the case of a high pass filter. The frequency detector circuitry employs an up/down counter to detect the zero crossover point of the output pulse waveform by sensing the point when the num-N ber of one pulses exceeds the number of zero pulses, or vice versa, and measures the time between those crossover points.





EUROPEAN SEARCH REPORT

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EP 83 40 0201

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Place of search Date of comple THE HAGUE 18-12		Date of completion o	f the search 984	LUBER	Examiner RICHS A.
Y: pa do A: teo O: no	CATEGORY OF CITED DOCL articularly relevant if taken alone articularly relevant if combined we accument of the same category chnological background an-written disclosure termediate document	vith another D:	earlier paten after the filin document ci document ci	it document, ig date ited in the ap ited for other	lying the invention but published on, or plication reasons ent family, corresponding



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