(11) Publication number:

0 086 650

A3

EUROPEAN PATENT APPLICATION

(21) Application number: 83300715.6

(22) Date of filing: 11.02.83

(5) Int. Cl.³: **H 02 P 7/62** H 02 P 13/18

(30) Priority: 12.02.82 US 348319

(43) Date of publication of application: 24.08.83 Bulletin 83/34

88) Date of deferred publication of search report: 28.03.84

(84) Designated Contracting States: CH DE FR GB IT LI

(71) Applicant: Black & Decker Inc. Drummond Plaza Office Park 1423 Kirkwood Highway Newark Delaware 19711(US)

(72) Inventor: Saar, David Alan 48 Belfast Road Timonium Maryland 21093(US)

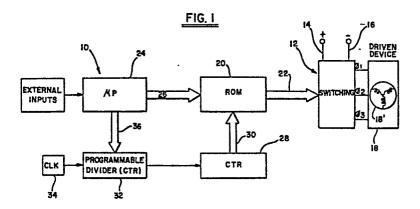
(72) Inventor: Bhagwat, Pradeep Madhav 13011 G Airlie Way Baltimore Maryland 21239(US)

(74) Representative: Lucas, Brian Ronald et al, Lucas, George & Co. 135 Westhall Road Warlingham Surrey CR3 9HJ(GB)

(54) Apparatus for providing a controlled power waveform.

(57) A waveform synthesizer (10) for generating single or poly-phase waveforms of selected wave shape and frequency for driving AC machinery (18) including, for example, single or poly-phase induction, synchronous or other type motors or the like, includes a plurality of controllable powerswitching device pairs (12) for selectively switching power from an oppositely poled DC source (14,16) to the corresponding power utilizing circuits of the driven device. The switching device pairs are controlled to provide switched pulses of selected widths, spacings, and repetition rates to generate the desired current waveform. The powerswitching device pairs are selectively switched by a control circuit that includes a microprocessor-controlled memory (20) that is divided into P addressable pages with each page including S sequentially accessible memory locations containing the switching device pairs control information necessary to synthesize the desired waveform. The microprocessor (24) is adapted to select one of the P available pages in response to prestored instructions, user input information, or condition-responsive transducer or similar information and, after selecting the desired page P, to control a presettable address counter (28) to sequentially access the S memory locations of the selected page P at a selected access

rate and to gate the so-accessed switching information to the power-switching device pairs to thereby synthesize the desired waveform.







EUROPEAN SEARCH REPORT

EP 83 30 0715

		DERED TO BE RELEVANT indication, where appropriate,	Relevant	CLASSIFICATION OF	THE
Category	of relevant passages		to claim	APPLICATION (Int. Cl. 3)	
Α	DE-A-2 829 793 * Page 8, parag paragraph 3 *	(SIEMENS) raph 2 - page 13,	1	H 02 P 7	7/62 3/18
A	DE-B-2 831 589	- (SIEMENS)			
	* Column 2, li line 2 *	ne 11 - column 3,			
P,A	Patent Abstracts 6, no. 176, 10 S JP-A-57-91691	- of Japan Vol. eptember 1982 &			
				{ 	
				TECHNICAL FIELDS SEARCHED (Int. Cl. ³)	
				H 02 M H 02 P H 02 P 1	3/00 7/00 7/00 3/00 3/00
!					
	The present search report has b	een drawn up for all claims			
	Place of search BERLIN	Date of completion of the search 20-12-1983	GESS	Examiner NER E A F	
Y: F	CATEGORY OF CITED DOCL particularly relevant if taken alone particularly relevant if combined we document of the same category technological background non-written disclosure	after the f ith another D: documen L: documen	iling date t cited in the a t cited for oth	erlying the invention t, but published on, or application er reasons atent family, correspond	ina