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54 **Code confirmation circuit.**

57 A code confirmation circuit includes input means comprising a number of switches (K1 to Kn) each representing a different code value. A counter (CT) has a number of outputs energisable in sequence from a datum state and each representing a different code value. Circuit means interconnect the switches and the outputs of the counter such that the operation of a switch having the same code value as the energised counter output causes an amplifier (A1) to clock the counter (CT) so as to energise the next counter output in sequence. Resetting means comprising a second amplifier (A2) are responsive to the operation of an incorrect switch to cause the counter to be reset. The energisation of the last counter output in the sequence indicates the correct sequence of operation of the switches.

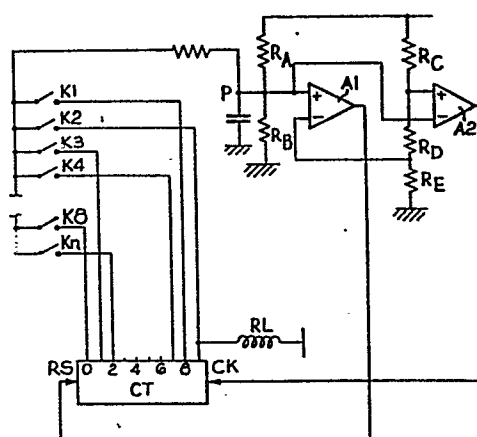


Fig. 1



DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. <sup>3</sup> )
X	ELECTRONIC DESIGN, vol. 26, no. 18, September 1, 1978 Rochelle Park, New York (US) "Ten-digit keyboard and CMOS circuits give low-cost electronic lock 151,000 codes", page 140.	1-3, 8, 10	E 05 B 49/00
A	--- GB-A-1 323 542 (SHORROCK DEVELOPMENTS LTD.) * figures 1-3; page 1, line 34 - page 2, line 24; page 2, line 59 - page 3, line 34 *	1, 9	
A	--- FR-A-2 234 714 (WESTINGHOUSE)	1, 8, 9, 10	
A	--- FR-A-2 224 629 (INTEGRATED CONVERSION TECHNOLOGY) -----	1, 8	TECHNICAL FIELDS SEARCHED (Int. Cl. <sup>3</sup> )  E 05 B
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 21-09-1984	Examiner HERBELET J.C.
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document but published on, or after the filing date D : document cited in the application L : document cited for other reasons &amp; : member of the same patent family, corresponding document</p>			