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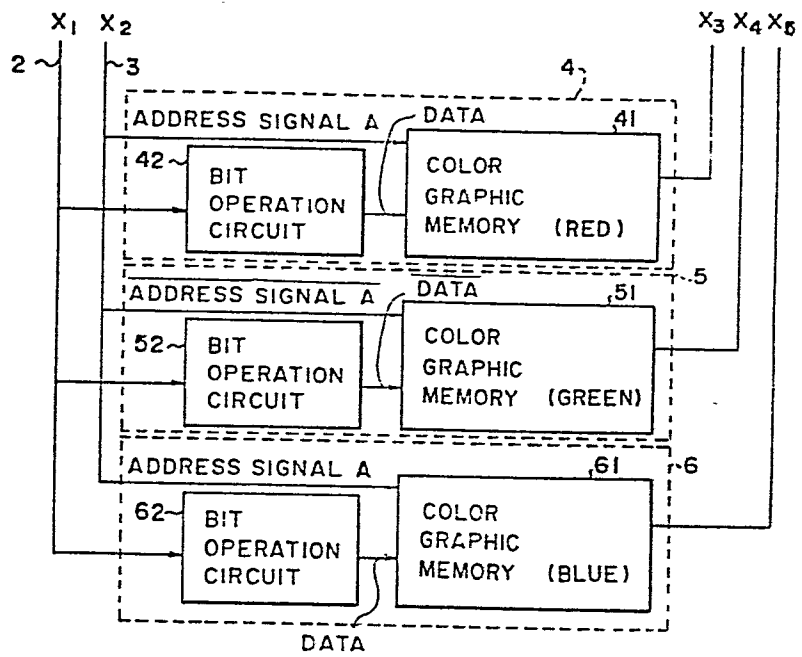
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(54) **Data write arrangement for color graphic display unit.**

(57) A plurality of color graphic memories (41, 51, 61) are assigned the same address space, and are connected to a common data bus (X₁). It is determined by a separately output memory select data which one of the color graphic memories (41, 51, 61) is to be written into. In the case of writing the same data into two or more of the color graphic memories (41, 51, 61) at the same address, the color graphic memories to be written into are selected by the memory select data first and then the same data is written into the selected color graphic memories in the same write cycle.

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FIG. 1 B





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EUROPEAN SEARCH REPORT

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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 3)
X	US-A-4 016 544 (MORITA et al.) * Column 2, line 41 - column 3, line 6; column 3, lines 27-56 * -----	1	G 09 G 1/28
			TECHNICAL FIELDS SEARCHED (Int. Cl. 3)
			G 09 G 1/28
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 07.11.1985	Examiner SIX G.E.E.
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	