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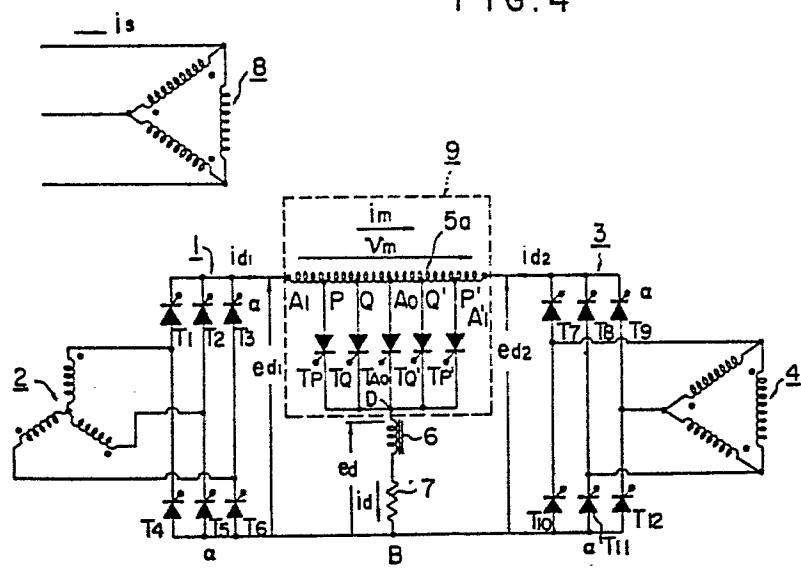
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(54) **Circuit for reducing harmonics and pulsation in multiplex polyphase rectifying circuits or inverters, respectively provided with interphase reactors.**

(57) A circuit for reducing harmonics in the current on AC side and the pulsation in the voltage on DC side in a multiplex polyphase rectifying circuit or an inverter, each having an interphase reactor (9) and plural rectifying circuits (1,3), wherein a plural number of taps, (A1,Bq,Ao,Q1,P1) is provided either on said interphase reactor or on the secondary windings thereof which is directly connected with the central point (A0) of the interphase reactor on the central point thereof, and the taps are connected to the DC circuit respectively via rectifying elements (TP,TQ, TA0,TQ1,TP1).

FIG. 4





DOCUMENTS CONSIDERED TO BE RELEVANT			CLASSIFICATION OF THE APPLICATION (Int. Cl. 3)
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	
E	US-A-4 488 211 (SHOTA MIYAIRI) * column 2, lines 49-66; figure 4 *	1-6	H 02 M 5/45 H 02 M 1/12
A	US-A-3 308 368 (A. SCHMIDT) * column 2, lines 5-54; figure 1 *	1,2	
A	FR-A-2 479 600 (JEUMONT-SCHNEIDER) * page 1, lines 21-34; figure 1 *	1	
			TECHNICAL FIELDS SEARCHED (Int. Cl. 3)
			H 02 M 5/00 H 02 M 1/00 H 02 M 7/00 H 02 P 13/00
The present search report has been drawn up for all claims			
Place of search BERLIN	Date of completion of the search 24-10-1985	Examiner WEIHS J.A.	
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			

