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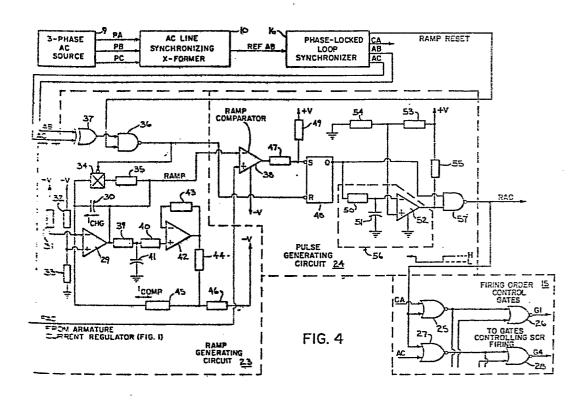
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(54) Compensated ramp generating circuit for controlling SCR firing.

(57) Ramp generating circuits (23), for synchronizing SCR firing control signals to the phase-varying signals derived from a three-phase AC power source (9), each have a compensating circuit (39 to 46) connected across an operational amplifier (29) and a capacitor (30) to control the peak value of the ramp signal generated at the output of the operational amplifier (29). The compensating circuit (39 to 46) maintains the peak value of the ramp signal close to its calculated value despite variations in the actual value of the capacitor (30) from its normal or rated value and despite changes in power line frequency between 50 hertz and 60 hertz. The compensating circuit (39 to 46) includes a T-section passive network (39, 40, 41), a voltage following amplifier (42) and several feedback resistors (44, 45) connected from the output of the operational amplifier (29) to its inverting (-) input. This compensating circuit (39 to 46) increases or decreases charging current to the capacitor (30) according to the type of compensation that is necessary.





## **EUROPEAN SEARCH REPORT**

EP 84 10 9974

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Category		th indication, where appropriate, vant passages		levant claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)	
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A	SOVIET INVENTIONS Week C 13, 7th M abstract no. R55 Publications Ltd SU - A - 674 206 19-07-1979 * abstract, figure	ay 1980, C8747, Derwent ., London, GB; & (SEREDIN AA)	1-	3		
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