

(19)



Europäisches Patentamt
European Patent Office
Office européen des brevets

(11) Publication number:

0 159 111
A1

(12)

EUROPEAN PATENT APPLICATION

(21) Application number: 85301096.5

(51) Int. Cl.⁴: **H 04 N 5/78**
H 04 N 5/94, G 11 B 5/09

(22) Date of filing: 19.02.85

(30) Priority: 13.03.84 US 589181

(43) Date of publication of application:
23.10.85 Bulletin 85/43

(84) Designated Contracting States:
DE GB

(71) Applicant: **HAZELTINE CORPORATION**
500 Commack Road
Commack New York 11725(US)

(72) Inventor: **Kerbel, Sheldon J.**
1676 Buckingham Road
Merrick New York 11566(US)

(74) Representative: **Wood, Anthony Charles et al,**
c/o **MICHAEL BURNSIDE & PARTNERS** 2 Serjeants' Inn
Fleet Street
London EC4Y 1HL(GB)

(84) Method and apparatus for storing/retrieving digital data on video tape.

(57) An input signal (Figure 1) provided to a video cassette recorder has the following format: a vertical synchronization pulse (101) followed by digital data (data periods 1-256) included in 256 data periods. Each data period (Figure 2) has 32 bytes of data followed by a horizontal sync pulse (horizontal 1-255) which includes a resynchronization pulse. The first

half of the digital data is the same as the last half of the digital data (Figure 2). The last half of the digital data is inverted so that the signal average is constant (Figure 3 and 4). During data recovery, the last half is stored over the first half of data except during medial dropout (Figure 6).

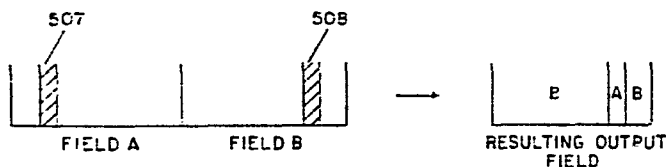


FIG. 6

DOCKET R4333.07
FRA:mfk

1 METHOD AND APPARATUS FOR
2 STORING/RETRIEVING DIGITAL DATA ON VIDEO TAPE

3 The invention relates to systems for
4 storing images and, in particular, to a method and
5 apparatus for storing and retrieving digital data on a
6 video cassette tape using a video cassette recorder.

7 Image storage systems employing video
8 cassette recorders are well known in the prior art
9 such as described in U.S. Patent Application Serial
10 No. 06/431,542, filed September 30, 1982, incorporated
11 herein by reference. However, such systems generally
12 store frequency-modulated analog data at a very slow
13 data rate and do not include the type of error
14 correction which is necessary for high digital data
15 rate transfer.

1 It is an object of this invention to
2 provide a method and system for accurately and quickly
3 storing or retrieving digital data on a video cassette
4 tape using a video cassette recorder.

5 The invention relates to a method of
6 storing digital information on a video cassette tape
7 using a video cassette recorder. An input signal is
8 provided to the recorder for recording onto the tape,
9 the input signal being representative of the video
10 information. A first vertical synchronization pulse
11 is provided to the recorder as part of the input
12 signal. Digital data encoded in "A" data periods
13 between each vertical synchronization pulse are
14 provided to the recorder as part of the input signal.
15 Each data period has "B" bytes of data wherein A and B
16 are positive integers. A resynchronization pulse is
17 provided as part of the input signal for recording on
18 the cassette before the beginning of each data period.

19 The invention also relates to a method for
20 recovering digital information on a video cassette
21 tape using a video cassette recorder. An output signal
22 is detected from the recorder as the recorder reads
23 the tape, the output signal being representative of
24 the digital information. A first vertical

1 synchronization pulse is detected as part of the
2 output signal. Digital data encoded in "A" data
3 periods between each first vertical synchronization
4 pulse are detected as part of the output signal. A
5 resynchronization pulse is detected before the
6 beginning of each said data period as part of the
7 output signal.

8 The invention also relates to an apparatus
9 for recapturing and decoding at least one period of
10 digital data stored on a video cassette tape as a
11 signal. The apparatus includes a video cassette
12 recorder for reading the tape, detecting the signal
13 and providing an output signal corresponding to the
14 digital data. The recorder includes a media dropout
15 detector for identifying parts of the output signal
16 which are the result of media dropouts and means for
17 detecting the digital data represented by the output
18 signal. Means are provided for storing the first half
19 of each period of digital data in a buffer except said
20 parts resulting from identified media dropouts
21 detected by the media dropout detector. Means are
22 also provided for storing the second half of each
23 period of digital data in the buffer at the same
24 buffer locations corresponding to the first half of
25 each period except said parts resulting from
26 identified media dropouts detected by the media
27 dropout detector.

1 For a better understanding of the present
2 invention, together with other and further objects,
3 reference is made to the following description, taken
4 in conjunction with the accompanying drawings, and its
5 scope will be pointed out in the appended claims.

6 Figure 1 is a diagram showing the signal
7 format of a digital data signal which is recorded on a
8 videc cassette tape according to the invention.

9 Figure 2 is a diagram showing the 32 bytes
10 of each data period.

11 Figure 3 is a diagram illustrating various
12 8-bit bytes.

13 Figures 4a and 4b are illustrations of the
14 maximum period of digital data without transitions.

15 Figure 5 is a functional block diagram
16 illustrating an error correction apparatus according
17 to the invention.

18 Figure 6 is a diagram illustrating error
19 correction storage according to the invention.

20 Figure 1 illustrates the signal format for
21 a signal used in accordance with the invention. The

1 lower portion of Figure 1 illustrates the field
2 organization of the signal. Generally, the signal
3 format must take the form of a standard
4 black-and-white video signal so that it can be
5 recorded on a video cassette tape by a video cassette
6 recorder. A signal such as illustrated in Figure 1
7 would be provided to the input of a video cassette
8 recorder. Because the signal format is similar to an
9 NTSC black-and-white format, the recorder would be
10 able to record the signal onto a video cassette tape.
11 Furthermore, the recorder would be able to read the
12 signal in the playback mode from a video cassette tape
13 recorded with such a signal and would provide the
14 signal at its output.

15 The basic format of the signal is a
16 vertical synchronization pulse followed by 256 data
17 periods. Each of the data periods is preceded by a
18 resynchronization pulse. In particular, as
19 illustrated in Figure 1, the preferred signal format
20 includes a first vertical synchronization pulse V1
21 followed by a second vertical synchronization pulse V2
22 which is then followed by 256 data periods. First
23 vertical sync pulse V1 and second vertical sync pulse
24 V2 are separated by a resynchronization pulse 100.
25 The purpose and function of resync pulses will be
26 discussed in greater detail below. The data periods
27 illustrated in Figure 1 are numbered from 1 to 256.

1 However, the actual number of data periods which can
2 be stored between vertical sync pulses depends on the
3 length of the data period and the tolerances of the
4 video cassette recorder. In order to closely
5 approximate a format which is similar to the NTSC
6 format and to be compatible with a video cassette
7 recorder, each data period approximates one TV line.
8 There are 256 TV lines in a standard 512-line system.
9 Each data period and horizontal synchronization period
10 should approximate the standard TV timing to insure
11 that the recorder can synchronize to the waveform.

12 According to the invention, each data
13 period is 64 microseconds and comprises 256 bits or 32
14 bytes of information as illustrated in Figure 2
15 followed by a 1.25 microsecond horizontal
16 synchronization pulse. In order to obtain the highest
17 data storage rate and the maximum volume of storage of
18 digital information, the invention employs a recovery
19 system configuration which resynchronizes itself from
20 the actual data being recovered. In particular, the
21 invention contemplates that the data recovery system
22 for detecting the digital data at each data period
23 will use data transitions from 1 to 0 or 0 to 1 to
24 resynchronize the data recovery clock which times the
25 detection of the digital information. As a result, it
26 is necessary to force data transitions to provide a
27 basis for which the data clock may be resynchronized.

1 In order to force such data transitions,
2 the following rule applies to the signal format: an
3 all zero (or all one) byte code is not permitted and
4 every other byte is inverted or multiplied by a minus
5 1. Therefore, as shown in Figure 2, odd bytes within
6 each data period are multiplied by a plus one (+1) and
7 even bytes are multiplied by a minus one (-1).

8 Figures 3 and 4 illustrate various 8-bit
9 byte configurations. Reference character 301 refers
10 to an odd byte of digital value equal to 170 and
11 reference character 302 refers to an even byte of
12 digital value equal to 170. As is apparent, even byte
13 302 is inverted or multiplied by minus one (-1)
14 thereby being the opposite of odd byte 301.
15 Similarly, reference characters 303 and 304 refer to
16 odd and even bytes having a digital value equal to 85;
17 reference characters 305 and 306 refer to odd and even
18 bytes having a digital value equal to 1; and reference
19 characters 307 and 308 refer to odd and even bytes
20 having a digital value equal to 255. Because the
21 value zero is not permitted for a byte, it is readily
22 apparent from figures 4a and 4b that the maximum
23 number of contiguous bits of information having the
24 same voltage level value is 22 bits. In particular,
25 as shown in Figure 4a if an odd byte of digital value
26 equal to 128 is followed by an even byte of digital
27 value equal to 255 which is then followed by an odd

1 byte of digital value equal to 1, the last seven bits
2 of the first odd byte, all eight bits of the even byte
3 and the first seven bits of the last odd byte all have
4 a zero value resulting in 22 contiguous bits of the
5 same value. Similarly, as illustrated in Figure 4b,
6 if a first even byte has a digital value equal to 128
7 and the next odd byte has a digital value equal to 255
8 and the last odd byte has a digital value equal to 1,
9 22 contiguous bits having the same voltage level value
10 will result. Therefore, by not allowing a zero value
11 byte and by multiplying every other byte by a minus
12 one (-1), at least one data transition for every 22
13 bits of information is guaranteed.

14 This means that the data clock will be
15 resynchronized by a transition at least every 22
16 bits. As long as the data clock can remain in
17 synchronization with the actual data rate for 22 bits,
18 no data loss will result. This is because the timing
19 between the data clock and the actual data will never
20 be out of synchronization.

21 There are various reason why the reading
22 rate and the writing (actual data) rate will differ.
23 First of all, most video cassette recorders use
24 phase-locked loops to control reading and writing head
25 speeds. As a result, the instantaneous reading and
26 writing speed may vary and the instantaneous writing
27 speed can be different from the instantaneous reading

1 speed. Furthermore, the data pulses which are
2 actually stored are not square pulses as illustrated
3 but usually have steep slopes. These steep slopes, in
4 addition to the FM modulation detection, result in
5 variations in determining the edges of the pulses
6 representing the digital data resulting in variations
7 in the recovered pulse widths.

8 In addition to resynchronizing the
9 recovery system clock with data transitions, the
10 signal format also contemplates a resynchronization
11 pulse 101 before the beginning of each data period to
12 reset and clear the counter used in recapturing the
13 data. In the signal format, resync pulse 101 is part
14 of the horizontal synchronization pulses 102.
15 Horizontal sync pulse 102 is provided as part of the
16 signal format so that the format is compatible with
17 the video cassette recorder. Preferably, horizontal
18 sync pulse 102 is 1.25 microseconds in length the 0.25
19 microsecond resync pulse 101 at the end thereof.

20 Referring to the field organization
21 illustrated in Figure 1, the data periods between
22 vertical sync pulses can be considered to define a
23 field of two sections A and B. For error correction
24 reasons which will be explained below, section B is
25 the same data as section A. Therefore, sections A and
26 B can be referred to as a single period of digital
27 data wherein the first half of the digital data is the

1 same as the last half of digital data. In order to be
2 compatible with the video cassette recorder circuitry
3 and, particularly, its clamping circuits, the average
4 value of data over a vertical period (16 milliseconds)
5 should be approximately constant to prevent the
6 recorder AC-couple circuits from going in to a
7 limiting condition. The limiting condition is
8 unacceptable for data recovery because the recorder
9 clamps recover too slowly for the data rates which are
10 contemplated by this invention. Since the data in
11 section A is equal to the data in section B, a
12 constant average value can always be guaranteed by
13 multiplying section A by plus one (+1) and multiplying
14 section B by minus one (-1) or inverting section B.

15 There are other minor modifications which
16 may be required to permit data storage and retrieval
17 by using a particular video cassette recorder. For
18 example, in most recorders, the automatic gain control
19 and pre-emphasis circuits may not operate properly and
20 should be disconnected.

21 Figures 5 and 6 illustrate in block
22 diagram form the error correction apparatus according
23 to the invention. Video cassette recorder 500
24 includes video heads 501 which detect the signal
25 recorded on the video cassette tape. The video signal
26 is then provided as an output 502 from recorder 500 in
27 the signal format as illustrated in Figure 1.

1 Included within recorder 500 is a media dropout
2 detector 503. Such detectors are well known in the
3 art and are used to detect data transfer problems
4 which occur between the tape and the video heads when
5 the recorder is in the playback mode. Such data
6 transfer problems may be the result of impurities in
7 the video tape media, damaged media or dust or other
8 foreign matter between the media and the head. The
9 output signal provided by output 502 is applied to
10 circuit 503 for detecting the digital data encoded in
11 each of the data periods in section A and section B.
12 The detected data is delayed by delay 504 and passes
13 through a write/don't write switch 505 before being
14 stored in buffer 506. Buffer 506 is configured to
15 hold one section of digital data so that it is first
16 loaded with section A. However, buffer 506 is not
17 loaded during identified periods of media dropout as
18 detector 503 enables switch 504 to prevent writing.
19 Section B is then written over section A in buffer 506
20 except during identified periods of media dropout.
21 The result is a significant decrease in the error rate
22 of the recaptured data.

23 As shown in Figure 6, section A may suffer
24 from media dropout 507 and section B may suffer from
25 media dropout 508. The result is that section A would
26 be stored in buffer 506 except for the data occurring

1 during media dropout 507 in which case no data would
2 be stored in buffer 506 at that equivalent address
3 location. Subsequently, section B would be written
4 over section A as stored in buffer 506. Since section
5 B does not have a media dropout at the same equivalent
6 location to section A (and the likelihood of such is
7 statistically very remote), section B will overwrite
8 the data stored in buffer 506 including the missing
9 data due to media dropout 507. However, when it is
10 time to write the data corresponding to media dropout
11 508 in buffer 506, switch 504 will be enabled to
12 prevent the writing of such data. The resulting
13 section output from buffer 506 would be complete and
14 would be provided by section B except for the
15 identified dropouts occurring in section B (i.e.
16 dropout 508) which have been previously stored in
17 buffer 505 during the writing of section A.

1 WHAT IS CLAIMED IS:

1 Claim 1. A method of storing digital
2 information on a video cassette tape using a video
3 cassette recorder by providing an input signal to the
4 recorder for recording on to the tape, the input
5 signal representing the digital information, said
6 method comprising the steps of:

- 7 a. providing to the recorder as part of the
8 input signal a first vertical synchronization
9 pulse (VI);
10 b. providing to the recorder as part of the
11 input signal digital data encoded in "A" data
12 periods (data periods 1-256) between each first
13 vertical synchronization pulse, each said data
14 period having "B" bytes of data (32 bytes,
15 Figure 2), wherein A and B are positive
16 integers; and
17 c. providing to the recorder as part of the
18 input signal a resynchronization pulse (101)
19 before the beginning of each said data period.

1 Claim 2. The method of claim 1 wherein the
2 average value of the magnitude of data between each
3 first vertical pulse is substantially constant
4 (Figure 3).

1 Claim 3. The method of claim 2 wherein the
2 first A/2 data periods include data which is equal and
3 opposite in magnitude to the last A/2 data periods
4 between each successive vertical synchronization pulse
5 (Figure 6).

1 Claim 4. The method of claim 3 further
2 comprising the steps of:
3 a. providing to the recorder as part of the
4 input signal a second vertical synchronization
5 pulse (V2) between the first vertical
6 synchronization pulse (V1) and the first data
7 period (data period 1); and
8 b. providing to the recorder as part of the
9 input signal a horizontal synchronization
10 pulse (H2) at the end of each data period.

1 Claim 5. The method of claim 4 wherein A
2 equals 256 and B equals 32.

1 Claim 6. The method of claim 5 wherein every
2 other byte is inverted (Figures 2 and 3).

-15-

1 Claim 7. The method of claim 6 wherein the
2 period between resynchronization pulses is
3 substantially 64 microseconds and wherein each
4 resynchronization pulse has a width substantially
5 equal to or greater than 0.25 microseconds (Figure 1).

1 Claim 8. The video cassette tape resulting
2 from the method of claims 1 - 7.

1 Claim 9. A method for recovering digital
2 information on a video cassette tape using a video
3 cassette recorder by detecting an output signal from
4 the recorder as the recorder reads the tape, the
5 output signal representing the digital information,
6 said method comprising the steps of:
7 a. detecting as part of the output signal a
8 first vertical synchronization pulse (VI);
9 b. detecting as part of the output signal
10 digital data encoded in "A" data periods
11 (data periods 1-256) between each first
12 vertical synchronization pulse, each said data
13 period having "B" bytes of data (32 bytes,
14 Figure 2), wherein A and B are positive
15 integers; and

-16-

16 c. detecting as part of the output signal a
17 resynchronization pulse (101) before the
18 beginning of each said data period.

1 Claim 10. The method of claim 9 further
2 including the step of synchronizing the encoded data
3 detection to transitions in the encoded data
4 (Figures 1 and 4).

1 Claim 11. The method of claim 10 further
2 including the steps of:
3 a. identifying (by 503) parts of the output
4 signal which are the result of media dropouts
5 (507, 508);
6 b. writing, except during said parts resulting
7 from identified media dropouts, the digital
8 data encoded in the first A/2 data periods into
9 a buffer (Figure 6, FIELD A); and
10 c. writing, except during said parts resulting
11 from identified media dropouts, the data
12 encoded in the last A/2 data periods in to the
13 buffer at the same buffer locations
14 corresponding the the data in the first A/2
15 data periods (Figure 6, FIELD B).

1 Claim 12. An apparatus (Figure 5) for
2 recapturing and decoding at least one period of
3 digital data stored on a video cassette tape as a
4 signal, said apparatus comprising:
5 a. a video cassette recorder (500) for reading
6 the tape, detecting the signal and providing an
7 output signal corresponding to the digital data;
8 b. a media dropout detector (503) associated
9 with said recorder for identifying parts of the
10 output signal which are the result of media
11 dropouts;
12 c. means (503) for detecting the digital data
13 represented by the output signal;
14 d. means (504, 505, 506, FIELD A) for storing
15 the first half of each period of digital data
16 in a buffer except said parts resulting from
17 identified media dropouts detected by said
18 media dropout detector; and
19 e. means (504, 505, 506, FIELD B) for storing
20 the second half of each period of digital data
21 in the buffer at the same buffer locations
22 corresponding to the first half of each period
23 except said parts resulting from identified
24 media dropouts detected by said media dropout
25 detector.

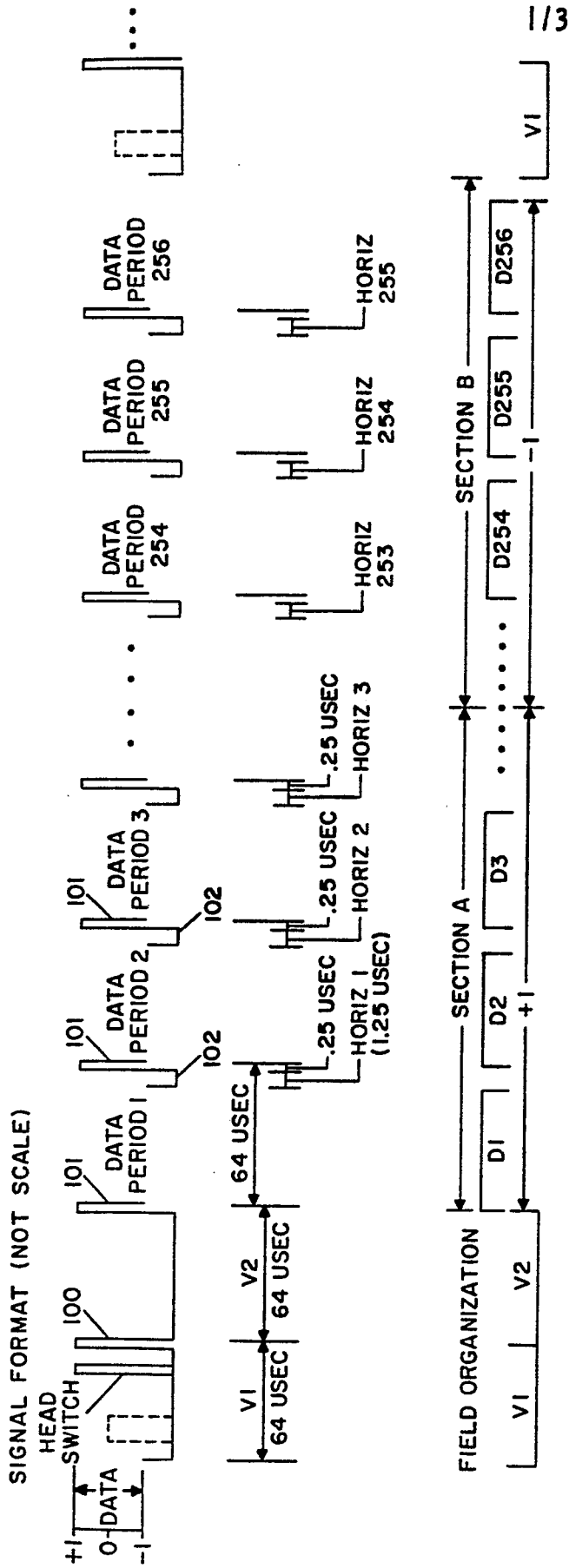


FIG. 1

EACH DATA PERIOD - 256 BITS = 32 BYTES = 64 USEC

BYTE:	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
MULTIPLIER	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1	+1	-1

FIG. 2

3/3

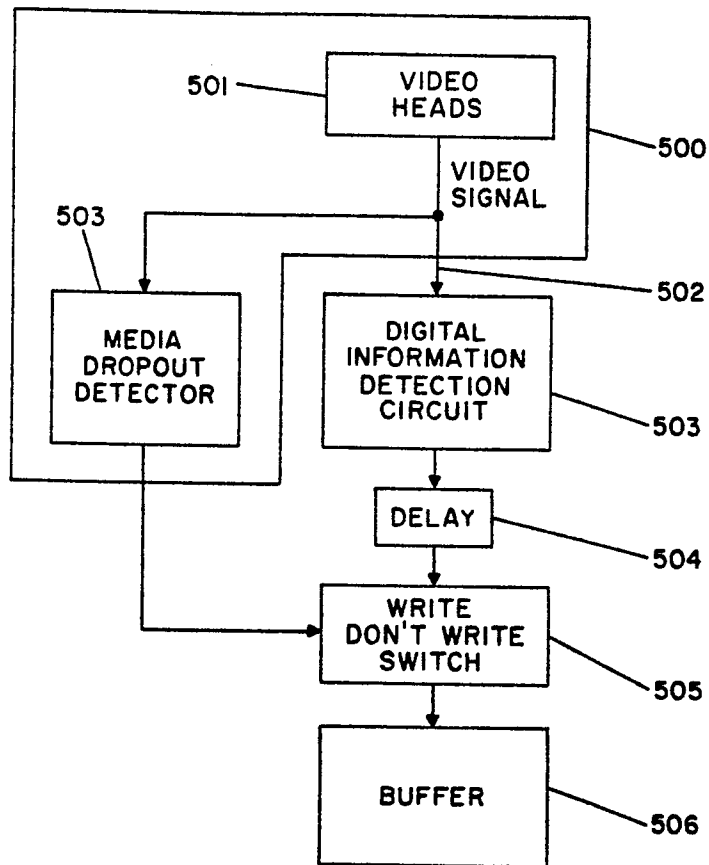


FIG. 5

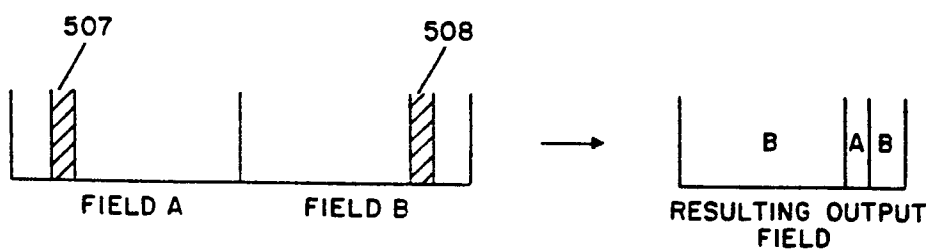


FIG. 6



European Patent
Office

EUROPEAN SEARCH REPORT

01501111
Application number

DOCUMENTS CONSIDERED TO BE RELEVANT			EP 85301096.5
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 4)
X	EP - A1 - 0 074 039 (MATSUSHITA)	1,8,9,10	H 04 N 5/78
A	* Fig. 1A,2A,3A to 3C; page 3, line 17 - page 12, line 20 * --	4	H 04 N 5/94 G 11 B 5/09
X	DE - A1 - 3 144 191 (SONY)	1,8	
A	* Fig. 2,3; page 9, line 24 - page 10, line 7 * * Page 8, lines 2-15 * --	4,11,12	
A	DE - A1 - 2 846 939 (QUANTEL)	11,12	
A,T	* Fig. 1,7; page 7, line 26 - page 8, line 17; page 17, line 15 - page 19, line 9 * -- US - A - 4 393 414 (REITMEIER)	11,12	TECHNICAL FIELDS SEARCHED (Int. Cl. 4) H 04 N 5/00 G 11 B 5/00
	* Column 1, lines 48-60; column 2, line 16 - column 3, line 44 * -----		
The present search report has been drawn up for all claims			
Place of search VIENNA		Date of completion of the search 17-06-1985	Examiner DIMITROW
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			