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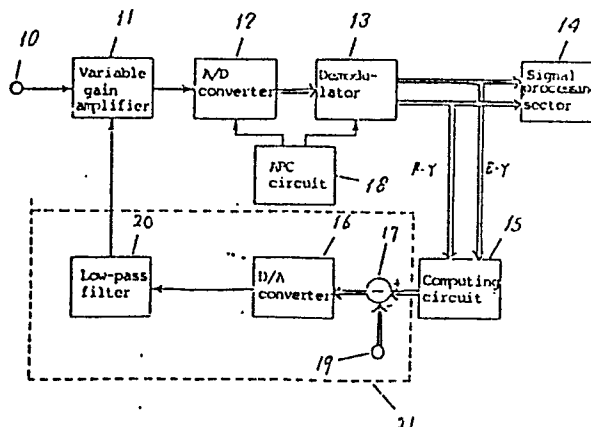
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(54) **Chrominance signal processing system.**

(57) A digital chrominance signal processing system comprises a variable gain amplifier (11) for gain control of a carrier chrominance signal; an A/D converter (12) for effecting analogue/digital conversion of the output of said variable gain amplifier; a demodulator (13) for demodulating the output of said A/D converter into two chrominance difference signals (R-Y, B-Y); a computing circuit (15) for computing the amplitude of the burst signal of said carrier chrominance signal from said two chrominance signals; and a control circuit (21) for producing a signal for controlling the gain of said variable gain amplifier by the output of said computing circuit, whereby the gain control is prevented from erroneous operation even when the demodulating axis of the demodulator is not in agreement with the burst phase.

FIG. 2





EP 85304060.8

DOCUMENTS CONSIDERED TO BE RELEVANT		
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim
A	<u>GB - A - 2 128 442 (RCA)</u> * Fig. 1; page 2, line 32 - page 7, line 10 * --	1,2
A	<u>US - A - 4 434 439 (STECKLER)</u> * Fig. 1; column 3, lines 34- 54 * --	1
A	<u>DE - A1 - 2 644 465 (SONY)</u> --	
A	<u>US - A - 4 400 742 (YAMAMITSU)</u> ----	
The present search report has been drawn up for all claims		
Place of search VIENNA		Date of completion of the search 12-11-1986
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CATEGORY OF CITED DOCUMENTS		
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		
T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document		

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