

12

EUROPEAN PATENT APPLICATION

21 Application number: 85303436.1

51 Int. Cl.⁴: **H 05 K 3/12**
H 05 K 3/46

22 Date of filing: 16.05.85

30 Priority: 17.05.84 GB 8412606

43 Date of publication of application:
18.12.85 Bulletin 85/51

88 Date of deferred publication of search report: 03.06.87

84 Designated Contracting States:
AT BE CH DE FR GB IT LI LU NL SE

71 Applicant: **Murray, John**
55 Granville Park
Aughton Lancashire(GB)

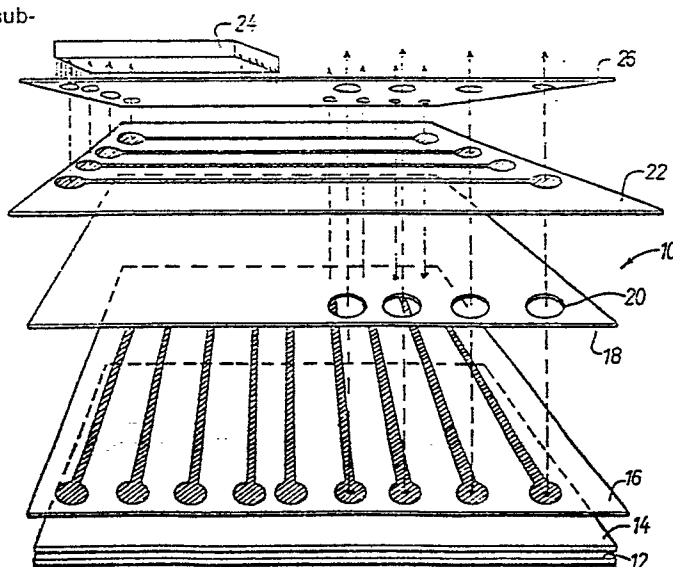
72 Inventor: **Murray, John**
55 Granville Park
Aughton Lancashire(GB)

74 Representative: **Huntingford, David Ian et al,**
W.P. THOMPSON & CO. Coopers Building Church Street
Liverpool L1 3AB(GB)

54 Improvements in and relating to printed circuit boards.

57 A single or multi-layer circuit board (10) is formed by printing directly onto an electrically non-conductive medium (16, 22) using a metal-based fluid which, upon drying, leaves behind a solid metallic residue defining a predetermined circuit configuration (16). For multi-layer boards a plurality of conductive and non-conductive layers are printed alternately onto a non-conductive substrate or onto a conductive substrate (12) provided with a non-conductive surface (14).

Fig 1





DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 4)
P,X	EP-A-0 140 585 (ELECTRO MATERIALS) * claims 1-4,8 *	1-4,6, 8,9	H 05 K 3/12 H 05 K 3/46
X	DE-A-2 935 421 (PHILIPS') * claims 1-4; page 2, lines 11-16 *	1,4,6, 8	
X	EP-A-0 091 075 (CIT-ALCATEL) * figure 1; claims 1-4 *	1,3,4, 6	
X	DE-A-3 225 483 (BOSCH) * figure 1; claims 1-3 *	1,3,6, 7	
			TECHNICAL FIELDS SEARCHED (Int. Cl. 4)
			H 05 K 3/12 H 05 K 3/46 B 41 M 1/40
The present search report has been drawn up for all claims			
Place of search BERLIN		Date of completion of the search 20-02-1987	Examiner HAHN G
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			

