

EUROPEAN PATENT APPLICATION

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DE FR GB NL

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25.04.90 Bulletin 90/17

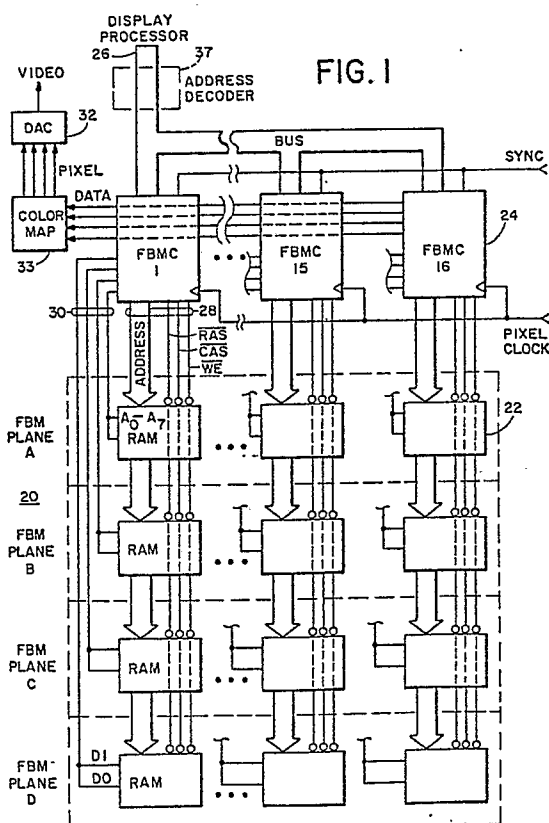
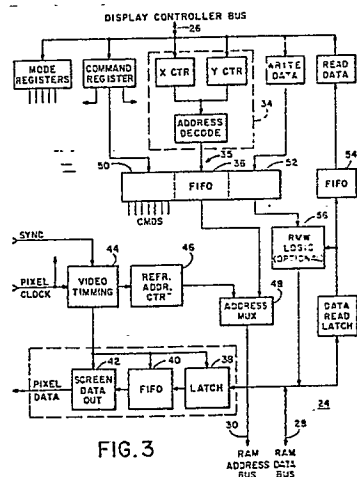
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⑤4 Frame buffer memory controller.

57 A frame buffer memory controller (24) allows rapid image updating while maintaining screen refresh data flow rate. One frame buffer memory controller (24) controls one or more pixel depth columns comprising one or more frame buffer memory chips (22) per pixel. Each frame buffer memory controller (24) listens on a display processor bus (26) for read, write or read-modify-write commands addressed to a pixel, or memory chip, under its control. Such commands, along with the associated addresses and data, are stored in a first-in, first-out (FIFO) buffer (35) for execution during the first free memory cycle.





DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)
X	WO-A-8 303 036 (TELEFONAKTIEBOLAGET LM ERICSSON) * Figure 2; page 4, line 1 - page 5, line 13; claim *	5	G 09 G 1/16
A	---	1	
A	ELECTRONIC DESIGN, vol. 32, no. 14, July 1984, pages 135-142, Waseca, MN, Denville, NJ, US; H. ASSARPOUR: "Graphics controller chip raises video data rate, is simpler to program" * Figure 1 *	1,5	
A	---		
A	US-A-4 092 728 (BALTZER) * Column 1, lines 24-31; figure 1; claims 1,3,4 *	1	
A	---		
A	US-A-4 150 364 (BALTZER) * Claim 1 *	1	

			TECHNICAL FIELDS SEARCHED (Int. Cl.4)
			G 09 G
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 30-01-1990	Examiner TIBAUX M.J.P.G.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			