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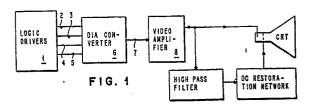
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- Representative: Tubiana, Max Compagnie IBM France Département de Propriété Industrielle F-06610 La Gaude(FR)
- Glitch energy reduction in digital gray scale CRT diplay.
- (57) A circuit addition is described for a digital, gray scale CRT display circuit to substantially reduce glitch distortion during CRT beam intensity changes. A high pass filter and DC restoration circuit couples the control grid of the CRT to the video amplifier such that the cathode and control grid substantially track each other during a glitch. This results in no substantial change in beam intensity as would otherwise be perceived during a glitch. This is accomplished without signal conditioning prior to the output of the video amplifier circuit by means of either delaying the higher order bits of the binary value corresponding to the desired intensity level or sampling and holding the analog output of the D/A converter prior to application of that signal to the video amplifier.





EUROPEAN SEARCH REPORT

EP 86 10 0110

	DOCUMENTS CONSII	DERED TO BE RELEVA	NT	
Category	Citation of document with inc of relevant pas		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 4)
A	US-A-4 017 681 (SME * Claim 1; column 1,	ULERS et al.) lines 21-46 *	1,8	G 09 G 1/00
A	FR-A-2 550 899 (BBC BROWN, BOVERI ET CIE * Page 2, lines 10-1 - page 4, line 19 *	:)	1,8	
Α	US-A-3 965 389 (ENG * Claim 1 * 	GLISH et al.)	1,8	
			,	TECHNICAL FIELDS SEARCHED (Int. Cl.4) G 09 G H 05 K H 04 N
	The present search report has be	en drawn up for all claims		
	Place of search	Date of completion of the search	1	Examiner
THE HAGUE 19-04		19-04-1990	TIBAUX M.J.P.G.	
X: par Y: par doo A: teo O: no	CATEGORY OF CITED DOCUMENticularly relevant if taken alone ricularly relevant if combined with anocument of the same category chnological background n-written disclosure ermediate document	E : earlier paten after the fili ther D : document ci L : document ci	T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document	