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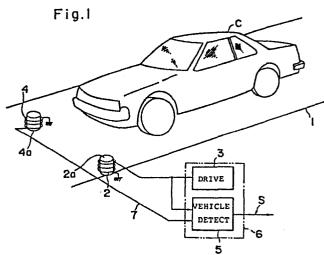
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- 71) Applicant: OMRON TATEISI ELECTRONICS CO. 10, Tsuchido-cho Hanazono Ukyo-ku Kyoto 616(JP)
- 122 Inventor: Mizuno, Masao Omron Tateisi Electronics Co. Patent Center 20, Igadera Shimo-Kalinji Nagaokakyo-shi Kyoto 617(JP)

- (72) Inventor: Maruo, Tosihiko Omron Tateisi Electronics Co. Patent Center 20, Igadera Shimo-Kalinji Nagaokakyo-shi Kyoto 617(JP)
- (72) Inventor: Fujimoto, Takuya Omron Tateisi Electronics
  Co.
  Patent Center 20, Igadera Shimo-Kalinji
  Nagaokakyo-shi Kyoto 617(JP)
- (72) Inventor: Sawada, Seiichi Omron Tateisi Electronics Co. Patent Center 20, Igadera Shimo-Kaiinji Nagaokakyo-shi Kyoto 617(JP)
- (72) Inventor: Kanayama, Kenji Omron Tatelsi Electronics
  Co.
  Patent Center 20, Igadera Shimo-Kaiinji
  Nagaokakyo-shi Kyoto 617(JP)
- Representative: WILHELMS, KILIAN & PARTNER Patentanwälte
  Eduard-Schmid-Strasse 2
  D-8000 München 90(DE)

- (54) Vehicle detecting system.
- (57) There is a vehicle detecting system for detecting the number of vehicles which pass the roadway, thereby realizing the smooth flow of vehicles. This system comprises: a transmitting coil (2) and a receiving coil (4) which are arranged on both sides of the vehicle detection area set over the roadway; a driving circuit (3) to supply a high frequency exciting current to the transmitting coil; and vehicle detecting circuit (5) to output a vehicle detection signal in response to a change in level or phase of the electrical signal which is induced in the receiving coil. The detection signal is outputted when the signal level exceeds a predetermined value or when the signal phase exceeds a predetermined angle. One or a plurality of pairs of transmitting and receiving coils may be buried under the roadway surface or on or above the roadway at regular intervals. With this system, the installing construction of the coils is simplified and the possibility of the occurrence of the accident such as disconnection of the coil is reduced. The vehicle on the roadway can be certainly detected.



# WILHELMS · KILIAN & PARTNER 0199329

PATENTANWÄLTE

**EUROPEAN PATENT ATTORNEYS** 

EUROPÄISCHE PATENTVERTRETER

MANDATAIRES EN BREVETS EUROPÉENS

DR.RER.NAT. ROLF E. WILHELMS DR. RER. NAT. HELMUT KILIAN DR.ING, JÜRGEN SCHMIDT-BOGATZKY\*

> EDUARD-SCHMID-STRASSE 2 8000 MÜNCHEN 90

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OMRON TATEISI ELECTRONICS CO.

Kyoto / Japan

Vehicle Detecting System

Priority: April 19, 1985 - JAPAN - No. 60-84855

#### BACKGROUND OF THE INVENTION

Field of the Invention

system to detect the existence, passage, and the like of a vehicle. This system is used in a traffic control system to smooth the traffic flow by, for example, counting the number of vehicles which pass a predetermined place on the road and controlling the signals on the basis of the count number obtained.

Description of the Prior Art

As typical one of conventional vehicle detecting systems, there has been known a system including an almost square loop coil buried under the road surface. A high frequency exciting current flows through the loop coil. When

the vehicle passes above the loop coil, the inductance of the coil changes, so that the value of the current also varies. The passage of the vehicle can be recognized by detecting the change in this current.

However, such a conventional vehicle detecting system has the following problems.

In general, the loop coil has the size of about 2m x 2m and the road must be dug up over a wide range to bury such a large loop coil into the road. Such a burying construction is a large-scaled, so that the construction expenses increase and much labors are needed for the construction.

The loop coil buried under the road surface is frequently subjected to the loads in association with the passages of vehicles, so that the accident of disconnection of the coil is likely to occur. The occurrence of the accident of the disconnection disenables the detection of vehicles.

## SUMMARY OF THE INVENTION

It is an object of the present invention to simplify the construction for installing the vehicle detecting system represented by the burying construction and also suppress the occurrence of the accident of the disconnection while keeping the vehicle detecting sensitivity to a relatively high level.

A vehicle detecting system according to the present invention comprises: a transmitting coil arranged on one side of a predetermined detection area set on the roadway of vehicles and a receiving coil arranged on the opposite side of THERE'S GO

running direction of the vehicle. Further, those coils may be obliquely arranged with respect to those directions.

In the vehicle detecting system according to the invention, in the case of detecting the vehicle on the basis of the variation in level of the signal which is induced in the receiving coil, the following actions are obtained. Namely, when the high frequency exciting current flows through the transmitting coil, the high frequency magnetic field is developed between the transmitting and receiving coils. When the vehicle passes in the magnetic field, the mutual inductance of both coils changes and the level of the electrical signal which is induced in the receiving coil changes. This reception signal is inputted to the vehicle detecting circuit (vehicle detecting means). When the change in the level of the reception signal is a predetermined amount or more, the vehicle detection signal is outputted from the vehicle detecting circuit.

extremely smaller than the conventional loop coil. Therefore, even when these coils are buried in the roadway as well, the burying construction can be simplified as compared with the conventional one. In addition, it is not always necessary to bury the transmitting and receiving coils to detect the vehicle but these coils may be also installed on the roadway. In this case, the installing construction can be further simplified.

In the case where the driving circuit and vehicle detecting circuit are enclosed in a box and this box is

the transmitting coil; a driving circuit to supply a high frequency exciting current to the transmitting coil; and vehicle detecting means for outputting a vehicle detection signal in response to a change over a predetermined amount in the characteristic of an electrical signal which is induced in the receiving coil. The characteristic of the electrical signal includes the level of the signal, phase of the signal, and the like.

The term "roadway" mentioned above denotes all of the locations where vehicles run and has the concept which apparently includes not only the ordinary road but also the road, floor and the like in the factories or precincts. terms "vehicle" and "vehicles" also have the wide meaning including not only what are called four-wheeled automobiles but also tricycle type automobiles, two-wheeled type vehicles, bicycles, unmanned automobiles, travelling robot, and the like. The transmitting and receiving coils may be buried under the roadway surface or may be set at positions of predetermind heights above the roadway. The detection area is the virtual area and is actually determined by the positions where the transmitting and receiving coils are arranged. One side and the other side of the detection area do not necessarily coincide with one side and the other side of the roadway. transmitting and receiving coils may be provided at positions along the running direction of the vehicle or may be provided at two positions which are away from each other at a predetermined distance in the direction perpendicular to the

arranged on one side of the roadway, it is sufficient to bury one of the transmitting and receiving coils, e.g., the transmitting coil on this side and to bury the other coil, e.g., the receiving coil on the other side of the roadway or in the central portion thereof, or the like. In this case, although the signal line connecting the receiving coil and vehicle detecting circuit must be buried so as to cross the roadway, it is sufficient to dig up the roadway along only a signal line. In the case where the driving circuit and transmitting coil are arranged on one side of the roadway and the receiving coil and vehicle detecting circuit are arranged on the other side, respectively, there is no need to arrange the signal line so as to cross the roadway.

Further, in the case of providing the transmiting and receiving coils above the roadway, it is unnecessary to dig up the roadway.

Consequently, the possibility of the occurrence of the accident of the disconnection decreases as compared with the conventional system in which the whole large loop coil is burid in the roadway.

Moreover, the space between the transmitting and receiving coils becomes the vehicle detection area and this detection area can be set to a wide region. Therefore, the deterioration of the sensitivity as compared with that of the conventional loop coil is not caused.

Fig. 1 shows an arrangement of a transmitting coil and a receiving coil;

Figs. 2a and 2b are diagrams for explaining the vehicle detecting operation;

Fig. 3 is a block diagram showing an electrical arrangement of an embodiment of the present invention;

Fig. 4 shows output signal waveforms in the block diagram of Fig. 3;

Fig. 5 is a block diagram showing an electrical arrangement of another embodiment of the present invention;

Fig. 6 is a flowchart showing the operation of the system shown in Fig. 5, particularly, the processing procedure by a CPU;

Fig. 7 is a time chart showing the time-dependent changes of the signals and values in the system shown in Fig. 5;

Fig. 8 shows another example of the arrangement of the transmitting and receiving coils;

Figs. 9a and 9b show still other examples of the arrangement of the transmitting and receiving coils;

Fig. 10 is a block diagram showing an electrical arrangement of still another embodiment of the present invention;

Fig. 11 is a flowchart showing the operation of the system shown in Fig. 10, particularly, the processing procedure by a CPU;

Fig. 12 is a time chart showing the time-dependent

changes of the signals, data, and values in the system shown in Fig. 10;

Fig. 13 shows an embodiment in which a plurality of transmitting and receiving coils are alternately arranged;

Fig. 14 is a block diagram showing an electrical arrangement of a vehicle detecting system which is applied to the arrangement of the transmitting and receiving coils shown in Fig. 13;

Figs. 15 to 20 show further another embodiment, in which:

Fig. 15 shows the state in which a plurality of pairs of transmitters and receivers are arranged along the vehicle roadway;

Fig. 16 is a block diagram showing an arrangement of the transmitter;

Fig. 17 is a block diagram showing an arrangement of the receiver;

Fig. 18 is a block diagram showing an electrical arrangement of the vehicle detecting system;

Fig. 19 is a waveform diagram showing the relations among the designation signal, the driving of the transmitters, and the reception in the receivers; and

Fig. 20 is a waveform diagram showing the relations among the driving of the transmitters, the reception in the receivers, and the vehicle detection signals.

## DESCRIPTION OF THE PREFERRED EMBODIMENTS

Fig. 1 shows an example of an arrangement of a transmitting coil 2 and a receiving coil 4. The coils 2 and 4 are wound around magnetic cores 2a and 4a, respectively, and buried on both sides in the lateral direction of a roadway (e.g., one lane of the road). The sizes of cores 2a and 4a are relatively small; for example, the length is about 70 mm and the diameter is about 15mm. The cores 2a and 4a are arranged that their longitudinal directions are the vertical direction. The distance between the transmitting coil 2 and the receiving coil 4 may be set to an arbitrary value and it will be ordinarily about one to a several meters. The area between the coils 2 and 4 substantially serves as the vehicle detection area. Therefore, the vehicle detection area may be set to an arbitrary range and at an arbitrary location in accordance with the position where the coils 2 and 4 are arranged.

Either one of or both of the transmitting coil 2 and receiving coil 4 may be arranged on the roadway surface or may be also installed above the roadway surface (for example, at the position of height of about five meters) by a pole brace or the like which is vertically set on the roadway. Both coils 2 and 4 may be provided on one side of the roadway 1.

A high frequency exciting current is supplied from a driving circuit 3 to the transmitting coil 2. The frequency of this exciting current may be set to a value in about a range from tens to hundreds of kHz. This frequency assumes  $\mathbf{f}_0$ .

The voltage or current which is induced in the

receiving coil 4 by the high frequency electromagnetic field (mainly, the magnetic field) which is generated from the transmitting coil 2 is given to a vehicle detecting circuit 5 through a line 7 buried under the surface of the roadway 1. The detecting circuit 5 detects in principle a change over a predetermined level in a reception signal of the receiving coil 4 and outputs a vehicle detection signal S. In the arrangement shown in Fig. 1, the detecting circuit 5 always compares the level (or phase) of the signal from the driving circuit 3, namely, the level (or phase) of the current which is outputted to the transmitting coil 2 with the level (or phase) of the current which is inputted from the receiving coil 4. When the difference between those levels (or phases) is over or below a predetermined value, the detecting circuit 5 outputs the vehicle detection signal S. The driving circuit 3 and vehicle detecting circuit 5 are enclosed in a box 6 and this box is installed on the side or above the roadway 1. The vehicle detection signal S is transmitted to a central controlling unit (not shown) through a telehpone line or another communication line or a transmitting line which is particularly installed. The central controlling unit controls the flow of all vehicles in the whole range covered by the unit on the basis of the vehicle detection signals transmitted.

When the high frequency exciting current flows through the transmitting coil 2 from the driving circuit 3, the high frequency magnetic field H is developed between the transmitting coil 2 and the receiving coil 4. As shown in Fig.

2a, when a vehicle C doesn't exist in the magnetic field H, there is no change in the level difference (or phase difference) between the transmitting current (exciting current) which is inputted to the detecting circuit 5 and the received current which is obtained in the receiving coil 4, so that the vehicle detection signal S is not outputted.

As shown in Fig. 2b, on the contrary, when the vehicle C passes in the magnetic field H, the magnetic fluxes are concentrated to the metallic portions of the vehicle C, so that the mutual inductance of the transmitting coil 2 and receiving coils 4 changes.

between the coils 2 and 4 decreases in dependence on the material of the vehicle body or the height of vehicle, so that the received current increases (in the case where the vehicle body is made of iron); and where the received current decreases due to the eddy-current loss (in the case where the vehicle body is made of aluminum). In any of these cases, this current change is detected by the detecting circuit 5. Namely, since the received current changes but the transmitting current is constant, the vehicle detection signal S is outputted from the detecting circuit 5.

Fig. 3 shows an example of a more practical arrangemnt of the driving circuit 3 and vehicle detecting circuit 5 shown in Fig. 1. In this diagram, the same parts and components as those shown in Fig. 1 are designated by the same reference numerals. Fig. 4 shows typical output signal

waveforms in the circuit block shown in Fig. 3.

In Fig. 3, the driving circuit 3 of the transmitting coil 2 comprises a high frequency oscillating circuit 8 and a power amplifier 9 to amplify an oscillating output of the oscillator 8 and gives it to the transmitting coil 2. In this example, the output current of the driving circuit 3 is not supplied to the detecting circuit 5.

The signal induced in the receiving coil 4 is amplified by an amplifier 11 and thereafter its noise component is removed by a band pass filter 12 having a center frequency of  $f_0$ . An output signal a of the filter 12 is detected by a detecting circuit 13 and becomes a signal b.

The signal b is sent to an analog switching circuit 14. The first output side of the switching circuit 14 is grounded through a capacitor 16 and the second output side is grounded through a capacitor 17. Electrostatic capacitances of the capacitors 16 and 17 are equal. Positive terminals of the capacitors 16 and 17 are connected to the input terminals of a differential amplifier 18, respectively. An output terminal of the differential amplifier 18 is connected to one input terminal of a comparator 19. A constant voltage power supply 20 to generate a threshold voltage Vref is connected to the other input terminal of the comparator 19. An output signal of the comparator 19 becomes the vehicle detection signal S. mentioned above, the detection signal S is transmitted to an external apparatus, e.g., the central controlling unit (not shown) and also supplied to a timing curcuit 15 (as a signal

g). When the ouput signal g of the comparator 19 is at a low level "L", the timing circuit 15 outputs a pulse signal (a train of pulses) c to the switching circuit 14. When the output signal g of the comparator 19 is at a high lvel "H" (i.e., when the vehicle is detected), the timing circuit 15 stops the generation of the pulse signal c.

The switching circuit 14 connects the detecting circuit 13 to the capacitor 16 when the pulse signal c from the timing circuit 15 is at an "H" level. The switching circuit 14 connects the detecting circuit 13 to the capacitor 17 when the pulse signal c is at an "L" level.

The switching circuit 14, differential amplifier 18, capacitors 16 and 17 connected therebetween, and timing circuit 15 serve to detect the time-dependent change in the signal which is induced in the receiving coil 4 and operate in the following manner.

Since the vehicle detection signal S is at an "L" level until time  $t_1$  shown in Fig. 4, a series of pulses c are generated from the timing circuit 15. Therefore, the switching circuit 14 repeatedly performs the switching operation. Since the signal b of the same level is alternately supplied to the capacitors 16 and 17, charged voltages  $V_{16}$  and  $V_{17}$  of the capacitors 16 and 17 are equal and an output signal f of the differential amplifier 18 is at the zero level. The output signal g of the comparator 19 is held at an "L" level.

When the vehicle C enters the magnetic field H at time  $t_1$ , the level of the output signal b of the detecting

circuit 13 starts increasing. Assuming that the pulse signal c rise at time  $t_1$ , the detecting circuit 13 is connected to the capacitor 16. The level of the input signal d of the capacitor 16 increases by only an amount commensurate with the increased amount of the signal b. Thus, the output signal f of the differential amplifier 18 is slightly reduced.

When the pulse signal c trails at time  $t_2$ , the detecting circuit 13 is connected to the capacitor 17. Since the level of the output signal b of the detecting circuit 13 increases for the period of time between times t, and t, the level of the input signal e to the capacitor 17 largely Thus, the level of the output signal f of the increases. differential amplifier 18 exceeds the threshold voltage Vref and the output signal g of the comparator 19 becomes an "H" level. Due to this, the vehicle detection signal S at an "H" level is outputted and at the same time, the output signal g is inputted to the timing circuit 15. The generation of the pulse signal c from the timing circuit 15 is stopped. Thus, the switching operation of the switching circuit 14 is stopped at time t2. The connecting state of the detecting circuit 13 with the capacitor 17 is held.

In other words, the charged voltage  $V_{16}$  of the capacitor 16 is held constant and at the same time, the level of the input signal e to the capacitor 17, namely, the charged voltage  $V_{17}$  of the capacitor 17 changes in accordance with the level change of the output signal b of the detecting circuit 13. In addition, the level of the output signal f of

the differential amplifier 18 also similarly changes. Such a state continues for the period of time until the vehicle C has passed through the magnetic field H.

When the vehicle C has passed through the magnetic field H and the level of the output signal b of the detecting circuit 13 decreases and at the same time, when the level of the output signal f of the differential amplifier 18 becomes lower than the threshold voltage Vref at time t<sub>3</sub> in response to the reduction of the level of the signal b, the generation of the vehicle detection signal S stops (i.e., the detection signal S becomes an "L" level) and the output signal g of the comparator 19 also becomes an "L" level. Thus, the pulse signal c is sent from the timing circuit 15 to the switching circuit 14. The switching operation is restarted and the system is returned to the inherent switching state.

The above embodiment can be applied to only the case where the signal level of the receiving coil 4 increases due to the existence of the vehicle C. However, if an window type comparator is substituted for the comparator 19, this system can be also applied to the case where the received signal level decreases due to the existence of the vehicle C.

Figs. 5 to 7 show another embodiment in which the vehicle detecting process is executed by use of a microprocessor.

Fig. 5 shows an electrical arrangement of a vehicle detecting system, in which the same parts and components as those shown in Fig. 3 are designated by the same reference

numerals.

The driving circuit 3 is additionally provided with an attenuator 31 and a band pass filter 32. The oscillating output of the oscillator 8 is attenuated by the attenuator 31. Thereafter, the higher harmonic wave component is removed by the band pass filter 32. Thus, only the component of the frequency  $f_0$  is sent to the power amplifier 9. An amount of attenuation of the attenuator 31 is controlled by microprocessor 21 as well be explained hereinafter in a manner such that the level of the received signal which is induced in the receiving coil 4 when no vehicle exists is always predetermined level.

After the received signal of the receiving coil 4 was detected by the detecting circuit 13, its level is converted into a digital value by an analog-to-digital (A/D) converter 29 and supplied to the microprocessor 21. The level of the received signal converted into the digital value is referred to as the reception data hereinafter.

The microprocessor 21 comprises: a central processing unit (CPU) 22 to perform the control of the attenuator 31 and the like as well as the vehicle detecting process; a read only memory (ROM) 23 in which the programs which are executed by the CPU 22 are stored; a random access memory (RAM) 24 to store a reference value Va of the received signal level (i.e., reception data), attenuation amount of the attenuator 31, level of the received signal, threshold value Vth for detection of the vehicle, etc.; a timer 25; an interface 26 to take in the

received signal; an interface 27 to control the attenuator; and an interface 28 to output the vehicle detection signal S.

Fig. 6 shows a flowchart for the processing procedure by the CPU 22. Fig. 7 shows time-dependent changes of various kinds of signals and values, respectively.

Referring now to Figs. 5 to 7, when the power supply is turned on at time  $\mathbf{T}_1$ , the attenuation amount of the attenuator 31 is set to the maximum value. Namely, the value of the exciting current flowing through the transmitting coil 2 (i.e., transmission level) is minimized (step 101). attenuation amount is stored into the RAM 24. Next, the reception data as the output value of the A/D converter 29 is supplied to the microprocessor 21 (step 102). This reception data is checked to see if it has reached the reference value Va corresponsing to a predetermined voltage which is required to detect the vehicle C or not (step 103). If NO, the attenuation amount of the attenuator 31 is set to the value which is lower by only one unit (step 104) and then step 102 follows again. By repeating the process in steps 102 to 104, the attenuation amount of the attenuator 31 is reduced in a stepwise manner, the reception data increases step by step. attenuation amount stored in the RAM 24 is updated each time the above process is repeated.

When the reception data becomes the reference value Va in time  $T_2$  (namely, if YES in step 103), the reception data is again supplied from the A/D converter 29 (step 105) and stored into a predetermined reception data area of the RAM 24

(step 106). Next, the idling is carried out for a predetermined period of time in step 107. Namely, the system waits for a sampling period which is determined by the timer 25.

Thereafter, the reception data is further supplied (step 108) and the difference between this reception data and the precedent reception data which has been obtained one sampling before and which has been stored in the RAM 24 is calculated (step 109). A check is then made to see if the difference exceeds the vehicle detection threshold value Vth or not (step 110).

When the difference is less than the value Vth, the reception data supplied in step 108 is stored into the reception data area of the RAM 24 and the reception data is updated (step 111). This process is executed to cope with the time-dependent change in the level of the received signal. It is desirable to execute this updating process only in the case where the difference between the reception data supplied in step 105 and the present reception data supplied in step 108 is less than a predetermined value. Or, if the process in steps 101 to 106 is periodically executed when no vehicle exists, it is not always necessary to execute the updating process in step 111. Thereafter, the idling is performed (step 112) and the processing routine is returned to step 108. In this manner, the process in step 108 to 112 is repeated.

When the vehicle C enters the detection area and the difference between the present reception data and the precedent

reception data exceeds the threshold value Vth at time  $T_3$  (i.e., if YES in step 110), the vehicle detection signal S is outputted through the interface 28 (step 113).

After completion of the idling (step 114), the reception data is taken in (step 115). The difference between this reception data and the reception data stored in the RAM 24 in step 106 or 111 is calculated and this difference is checked to see if it is below the threshold value Vth or not (step 116). If the difference still exceeds Vth, the process in steps 114 to 116 is repeated.

When the vehicle C has passed the detection area where the magnetic field H exists and the difference becomes smaller than the value Vth at time  $\mathbf{T_4}$ , the generation of the vehicle detection signal S is stopped (step 117). Thereafter, step 108 follows again.

As shown in Fig. 8, in the case where two adjacent roadways are formed, the transmitting coil 2 is installed at the boundary portion of both roadways 1A and 1B and two receiving coils 4 are arranged on the outsides of the roadways 1A and 1B. In this way, the vehicle detecting system can be also constituted such that one transmitting coil 2 is commonly used to detect vehicles which pass two roadways 1A and 1B.

In the foregoing embodiment, particularly, as shown in Figs. 2a and 2b, the longitudinal directions, i.e., the axes of the cores 2a and 4a of the transmitting and receiving coils 2 and 4 are vertically arranged. In such an arrangement, even if the vehicle C dosn't exist as well, the number of magnetic

fluxes which interlink the receiving coil 4 due to the magnetic field H which is formed between the coils 2 and 4 is relatively large. When the vehicle C passes in the detection area, the number of magnetic fluxes which interlink the receiving coil 4 increases. However, since this increase amount is not so large, the sensitivity for detection of vehicles is not so high.

As shown in Figs. 9a and 9b, by vertically arranging an axis  $P_1$  of the transmitting coil 2 and horizontally arranging an axis  $P_2$  of the receiving coil 4, the vehicle detecting sensitivity can be raised.

As shown in Fig. 9a, if no vehicle exists in the detection area, the number of magnetic fluxes of the magnetic field H which interlink the receiving coil 4 is extremely small.

When the vehicle C passes through the magnetic field H, on the other hand, the magnetic field H is bent as shown in Fig. 9b and the density of magnetic fluxes interlinking the receiving coil 4 increases and at the same time, the number of interlinking magnetic fluxes remarkably increases. Thus, the level of voltage which is induced in proportion to the change in number of interlinking magnetic fluxes also fairly increases as compared with the case of the arrangement shown in Figs. 2a and 2b (namely, it is increased at least about ten to hundred times) and the vehicle detecting sensitivity is sufficiently improved.

To increase the vehicle detecing sensitivity, the

is preferable. Namely, briefly following arrangement explaining, in the plane which is formed by the axis of the core of the transmitting coil 2 and the axis of the core of the receiving coil 4, it is sufficient to arrange the axis of the core 4a of the receiving coil 4 substantially perpendicularly with respect to the axis of the core 2a of the transmitting coil 2 or at an angle near it. Therefore, in the case where the axis of the core of the transmitting coil is vertically arranged and the axis of the core of the receiving coil is horizontally arranged, the sensitivity is improved. Also, in the case where the axis of the core of the transmitting coil is horizontally arranged and the axis of the core of the receiving coil is vertically arranged, or where both axes of the cores of the transmitting and receiving coils are obliquely arranged, or the like, the detecting sensitivity is improved.

rig. 10 shows an electrical arrangement of the vehicle detecting system in the case where the transmitting coil 2 and receiving coil 4 arranged as explained above are used. Fig. 11 is a flowchart showing the processing procedure by the CPU. Fig. 12 shows the states of changes of signals, data, and values.

According to the experiments performed by the inventors of this application, it has been found that when the vehicle C passes the area between the transmitting coil 2 and the receiving coil 4, the phase of the received signal is delayed than the phase of the transmitting signal, and when the vehicle passes the roadway adjacent to the roadway 1, the phase

of the received signal is advanced than the phase of the transmitting signal.

Therefore, as well be explained hereinafter, a phase comparator (a phase detector) 36 is provided and the shifting direction of the phase of the received signal is discriminated. Therefore, it is possible to clearly distinguish whether the vehicle has passed the roadway having the vehicle detection area or it has passed the roadway adjacent thereto. Thus, only the vehicles which have passed the detection area can be accurately detected.

In Fig. 10, the same parts and components as those shown in Fig. 5 are designated by the same reference numerals. The vehicle detecting circuit 5 is newly provided with the phase comparator 36. The transmitting signal which is supplied to the transmitting coil 2 and the received signal which is obtained from the receiving coil 4 are inputted to the phase comparaotr 36. A signal indicative of the phase difference between those signals, more accurately speaking, a signal representative of an amount of phase shift of the received signal using the transmitting signal as the reference is outputted from the comparator 36. The signal indicative of the phase difference is converted into a digital value by analog-to-digital (A/D) converter 37 and thereafter it is supplied to the microprocessor 21. A threshold value Vp of the phase difference to decide the vehicle detection is set in the RAM 24.

In Fig. 11, the same processes as those shown in Fig.

6 are designated by the same reference numerals. Although the process in steps 101 to 104 in Fig. 6 is omitted from Fig. 11, this process is also similarly executed in the flowchart of Fig. 11. In addition, the updating process of the reception level data (the data which is obtained from the A/D converter 29) in step 111 is omitted in Fig. 11.

Referring now to Figs. 10 to 12, after completion of the reading and storing operations of the reception level data (step 105 and 106), the phase data of the receiving signal which is obtained from the A/D converter 37 is taken into the microprocessor 21 and stored into a predetermined area of the RAM 24 (steps 121 and 122).

When it is confirmed that the change amount of the reception level data has exceeded the threshold value Vth (step 110), an amount of change in the phase data is likewise derived (steps 123 and 124). A check is then mae to see if the change amount (difference) has exceeded the threshold value Vp in the negative direction or not (step 125). If YES, the vehicle detection signal S is outputted (step 113).

In the case of stopping the generation of the detection signal S as well (step 117), the AND logic is got between the signal indicating that the change amount of the level of the received signal became below the threshold value Vth (steps 115 and 116) and the signal representing that the change amount of the phase difference became below the threshold value Vp (in steps 126 to 128).

The detecting circuit 13, A/D converter 29, and

interface 26 may be omitted and the vehicle detection may be executed on the basis of only the phase difference of the received signal to the transmitting signal as well.

Figs. 13 and 14 relate to a developed system of the form shown in Fig. 8 and show a system to detect the vehicles which run a plurality of adjacent roadways, for example, a plurality of lanes.

In this embodiment, the example of four lanes is shown. Receiving coils 4A, 4B, and 4C and transmitting coils 2A and 2B are alternately arranged at the boundary portions of the respective lanes in the lateral directon of the lanes.

A high frequency signal which is generated from the oscillator 8 of the driving circuit 3 is sent to a change-over switch 42. Amplifiers 9A and 9B to drive the transmitting coils 2A and 2B are connected to two output sides a and b of the switch 42, respectively. Although the contact type change-over switch 42 has been shown, a contactless type switch composed of transistors and the like may be generally used. The switch 42 is controlled by the CPU 22.

The receiving coils 4A, 4B, 4C are provided with amplifiers 11A, 11B, 11C and band pass filters 12A, 12B, and 12C, respectively. An output of the filter 12A is supplied to a detecting circuit 13A and a phase comparator 36A. Similarly, outputs of the filters 12B and 12C are supplied to detecting circuits 13B and 13C and to phase comparators 36B and 36C, respectively. Outputs of the detecting circuits 13A to 13C and outputs of phase comparators 36A to 36C are all supplied to a

multiplexer 41 which is controlled by the CPU 22. These analog outputs are sequentially switched and converted into the digital signals by the A/D converter 29 and thereafter inputted to the CPU 22. Vehicle detection signals  $S_1$  to  $S_4$  of the first to fourth lanes are individually outputted from the CPU 22.

The above-mentioned system operates in a manner as follows. First, the switch 42 is connected to the side a and the transmitting coil 2A is driven. In this case, the vehicles which pass the first and second lanes can be detected, so that the received signals of the receiving coils 4A and 4B are checked. The output of the detecting circuit 13A is first taken into the CPU 22 through the multiplexer 4l and subsequently the output of the phase comparator 36A is taken into the CPU 22. The process shown in Fig. 1l, particularly, the process in steps 108 to 117 is executed by the CPU 22. If it is determined that the passage of the vehicle was detected, the vehicle detecting signal S<sub>1</sub> is outputted.

Next, the outputs of the detecting circuit 13B and phase comparator 36B are sequentially taken into the CPU 22 and the presence or absence of the vehicle in the second lane is decided by the similar process.

Thereafter, the switch 42 is connected to the side be and the transmitting coil 2B is driven. In this case, the vehicle detecting processes for the third and fourth lanes are sequentially performed by checking the signals of the receiving coils 4B and 4C.

By repeatedly executing the above-mentioned processes at a short period, the vehicles in four lanes can be always detected.

Such an alternate arrangement of a plurality of transmitting and receiving coils may be installed in the running direction of the vehicle. With this arrangement, the running velocity of the vehicle can be measured using the difference of times of the vehicle is detected at respective positions. This arrangement can be also applied to detect the jam of vehicles.

Figs. 15 to 20 show further other embodiments. These embodiments relate to the systems which are useful to detect the jam of vehicles on the road, waiting states of vehicles at the toll station, running velocities of vehicles, and the like.

Referring now to Fig. 15, a plurality of transmitters 50 are arranged at regular intervals on one side along the road. A plurality of receivers 60 are arranged at regular intervals on the other side along the road in corresopondence to the transmitters 50, respectively. For convenience of explanation, reference numbers one to n (Nos. 1 to n) are added to the transmitters 50. The same reference numbers (Nos. 1 to n) are slso added to the receivers 60 corresponding to the transmitters 50, respectively. These plurality of transmitters 50 are connected in a multidrop manner through one transmitting signal line, a plurality of (for example, in the case of four bits, four) designation signal lines and power supply lines to the box 6 equipped with the vehicle detecting system. These

plurality of receivers 60 are also likewise connected to the box 6 in a multidrop manner through one received signal line, a plurality of designation signal lines and power supply lines.

The interval of the transmitters 50 (and receivers 60) is set to 30 to 150 m in the case where, for example, the jam of vehicles is detected at the entrance and exit of an express highway or on the other ordinary road. This interval is set to a small value of 0.5 to 1.0 m in the case of detecting the waiting state of vehicles at the toll station or parking lot. Namely, it may be set to a proper desired interval in accordance with a use object.

Fig. 16 shows an example of a constitution of the transmitter 50. Apparentlym, the transmitter 50 includes the transmitting coil 2. The coil 2 is constituted by a resonance circuit on the secondary side and a primary coil to excite the resonance circuit. The high frequency signal transmitted through the transmitting signal line is inputted to an amplifier 53 through a switch 52 consisting of a semiconductor switching device or the like and the transmitting coil 2 is driven by the amplifier 53. For instance, the designation signal of four bits is decoded by a decoder 51 and when this constituted is designated, the switch 52 is turned on.

In Fig. 17, the receiver 60 similarly comprises: the receiving coil 4; an amplifier 63 to amplify the received signal of the coil 4; a switch 62 to connect an output of the amplifier 63 to the received signal line; and a decoder 61 to decode the designation signal and turn on the switch 62 when

this receiver is designated.

Fig. 18 shows an electrical arrangement of the system built in the box 6. In Fig. 18, the same parts and components as those shown in the block diagram already described in the foregoing embodiments are disignated by the same reference numerals. The output of the driving circuit 3 is sent to the transmitting signal line. The received signal line connected to the amplifier 11. The designation signal lines of four bits extend from the CPU 22 through an interface (not The designation signals on these lines are amplified by an amplifier 65 and thereafter supplied to all of the transmitters 50 and receivers 60. When a pair of transmitter and receiver are designated, the multiplexer 41 sequentially switched the level detection signal and phase difference signal of the received signal from the receiver and supplies them to the CPU 22.

As shown in Fig. 19, each pair of transmitter and receiver is sequentially designated from No. 1 by the designation signal at every constant period of time (e.g., 8 msec). In the designated transmitter 50, the switch 52 is turned on by the decoder 51, so that the transmitting coil 2 is driven for only the designated time period. In the designated receiver 60, on one hand, the received signal of the receiving coil 4 is likewise sent to the vehicle detecting system through the received signal line for only the designated time period. In the system shown in Fig. 18, particularly, the CPU 22 processes the received signals which are sequentially inputted,

due to the foregoing method shown in Fig. 11 and the like, thereby determining the detection of the vehicle.

In this manner, a plurality of pairs of transmitters and receivers are time-sharingly driven and the detection of the vehicle in the detection area of each pair of transmitter and receiver is executed.

Fig. 20 shows an example of the vehicle detection signals in the respective detection areas. The CPU determines the velocity of the vehicle C and the stop state of the vehicle C on the basis of the time-dependent changes of the vehicle detection signals. The difference of times when the detection of the existence of the vehicle C is started between the adjacent receivers is obtained. For example, the time difference between time t, when the detection of existence of the vehicle C by the No. 1 receiver is started and time  $t_2$  when the detection of the existence of the vehicle C by the No. 2 receiver is started, namely,  $(t_2 - t_1)$  is calculated. Or, the time difference of the vehicle detection between the Nos. 2 and 3 receivers, namely,  $(t_3 - t_2)$  is calculated. The running velocity of the vehicle C can be calculated by dividing the resultant time difference by the interval between the adjacent receivers installed. The running velocity obtained is compared with a set value. When it is larger than the set value, it is decided that the vehicle C smoothly runs. If the velocity is smaller than the set value, on the contrary, it is determined that the vehicle C is in the jam state.

In the above embodiment, each of the transmitters and receivers is provided with the switch and decoder. However, if the transmitters (or receivers) are installed at large regular intervals in the running direction of the vehicle and the transmitters (or receivers) are not adversely influenced by the adjacent transmitting coils or receiving coils, the switch and decoder may be provided for only either one of the transmitter and receiver.

#### WHAT IS CLAIMED IS:

- 1. A vehicle detecting system comprising:
- a transmitting coil (2) arranged on one side of a predetermined detection area which is set over a roadway of vehicles and a receiving coil (4) arranged on the other side of said detection area;
- a driving circuit (3) for supplying a high frequency exciting current to said transmitting coil; and

vehicle detecting means (5) for outputting a vehicle detection signal in response to a change over a predetermined amount of the characteristic in an electrical signal which is induced in the receiving coil.

- 2. A vehicle detecting system according to claim 1, wherein the characteristic of the electrical signal is a level of the signal and said vehicle detecting means outputs the vehicle detection signal when a change in the level of the signal induced in the receiving coil exceeds a predetermined value.
- A vehicle detecting system according to claim 1, wherein the characteristic of the electrical signal is a phase of the signal and said vehicle detecting means outputs the vehicle detection signal when a change in the phase of the signal induced in the receiving coil exceeds a predetermined angle.

- 4. A vehicle detecting system according to claim 1, wherein the transmitting coil and the receiving coil are buried under the surface of the roadway.
- 5. A vehicle detecting system according to claim 1, wherein the transmitting coil and the receiving coil are disposed on the roadway.
- 6. A vehicle detecting system according to claim 1, wherein the transmitting coil and the receiving coil are installed above the roadway.
- 7. A vehicle detecting system according to claim 1, wherein said vehicle detecting means includes a comparator for comparing the level of the electrical signal which is induced in the receiving coil with a predetermined reference level.
- 8. A vehicle detecting system according to claim 1, further comprising means for controlling a level of the exciting current which is supplied from said driving circuit to the transmitting coil in order to keep constant the level of the electrical signal which is induced in the receiving coil when no vehicle exists in said detection area.
- 9. A vehicle detecting system according to claim 1, wherein said vehicle detecting means samples at every constant period of time the level of the electrical signal which is

induced in the receiving coil, thereby obtaining a change amount in the level of the electrical signal and then comparing this change amount with a predetermined reference value.

- A vehicle detecting system according to claim 1, wherein in the plane which is formed by the axis of the transmitting coil and the axis of the receiving coil, said transmitting coil and said receiving coil are arranged such that the axis of the receiving coil is substantially perpendicular to the axis of the transmitting coil.
- 11. A vehicle detecting system according to claim 1, wherein two receiving coils are arranged on both sides of one transmitting coil so as to away from each other at a constant distance.
- 12. A vehicle detecting system according to claim 1, further comprising:
- a plurality of transmitting coils (2A, 2B) and receiving coils (4A, 4B, 4C) which are alternately arranged substantially in a line;

first switching means (42) for time-sharingly supplying an output signal of said driving circuit to said plurality of transmitting coils; and

second switching means (41) for time-sharingly switching the received signals of said plurality of receiving coils in correspondence to the switching operation by said

first switching means and then supplying said received signals to said vehicle detecting means.

13. A vehicle detecting system according to claim 1, further comprising:

a plurality of transmitters (50) including the transmitting coils which are arranged at regular intervals on one side along the vehicle roadway;

a plurality of receivers (60) including the receiving coils which are arranged on the other side along the vehicle roadway in correspondence to said plurality of transmitters;

generating means (22) for generating designation signals to sequentially designate a pair of transmitter and receiver for every constant period of time; and

switching means (51, 52, 61, 62), provided for either one of said transmitter and said receiver, for supplying the output of said driving circuit to the transmitting coil or supplying the received signal of the receiving coil to said vehicle detecting means when a pair of transmitter and receiver are designated by said designation signal.

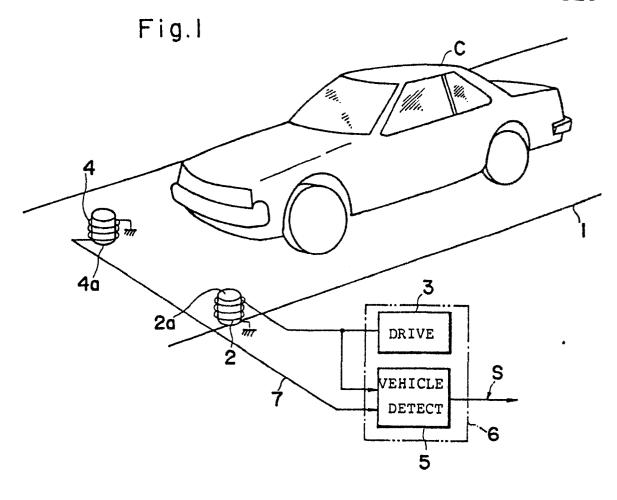
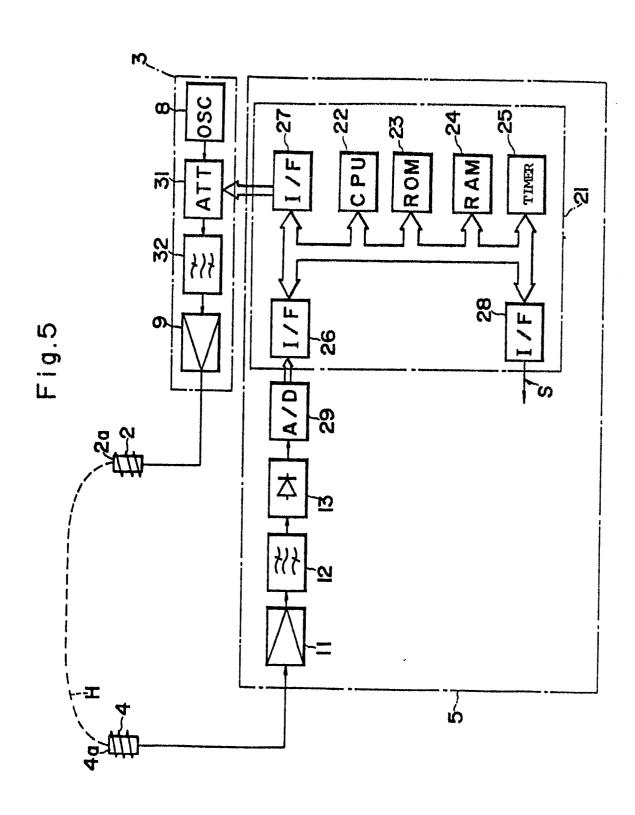


Fig.2a Fig.2b

7 TIMING 4 <u>M</u> 本 ⋈,



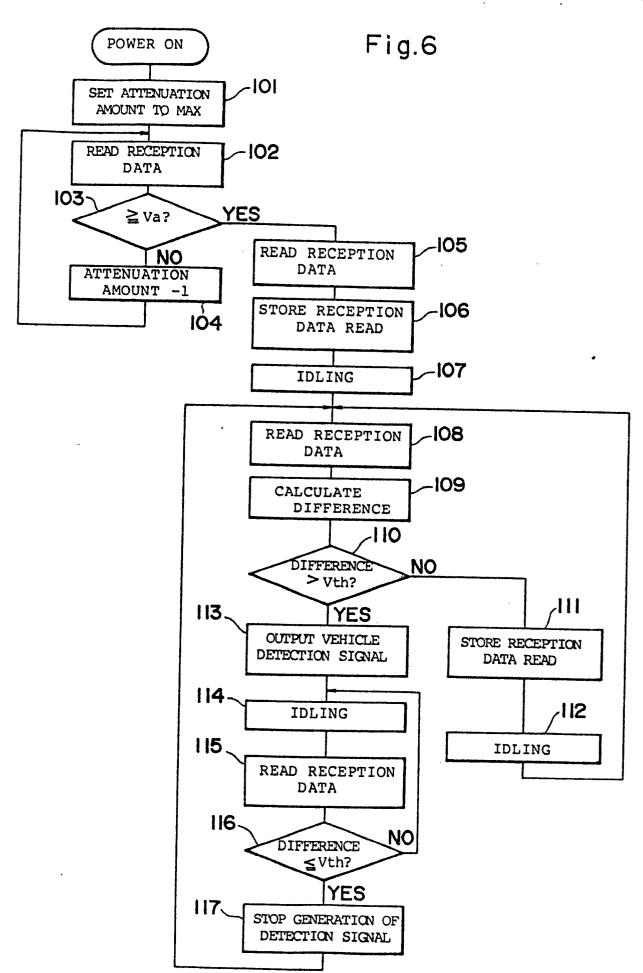


Fig.7

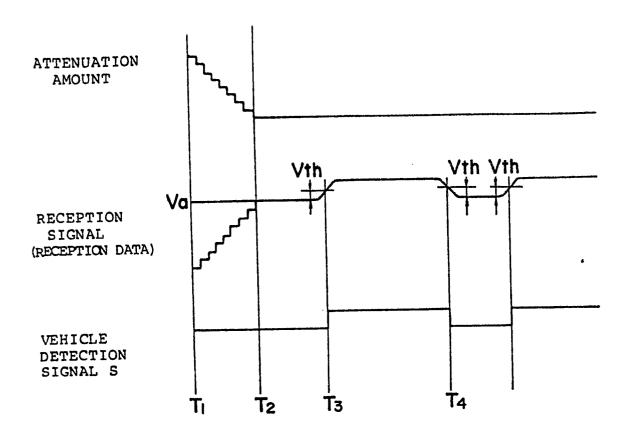
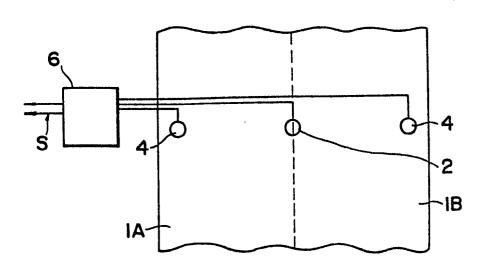
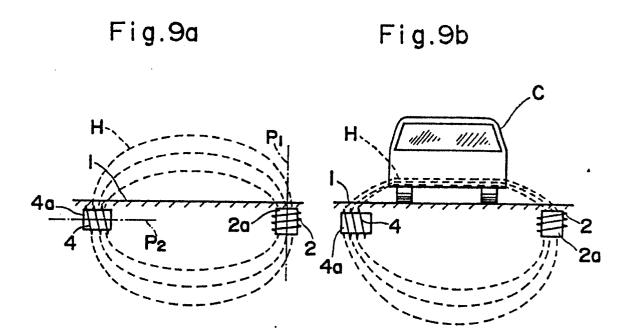
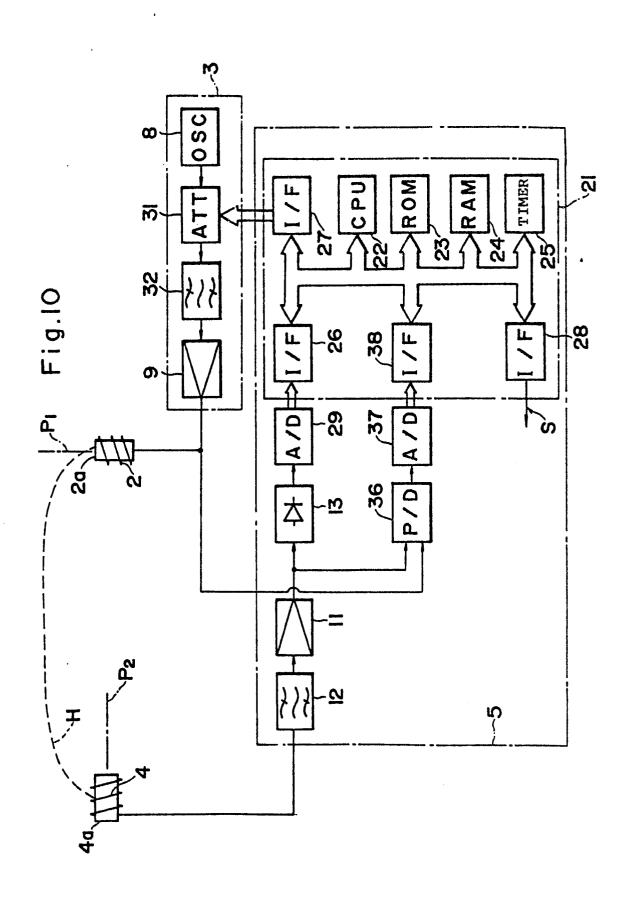
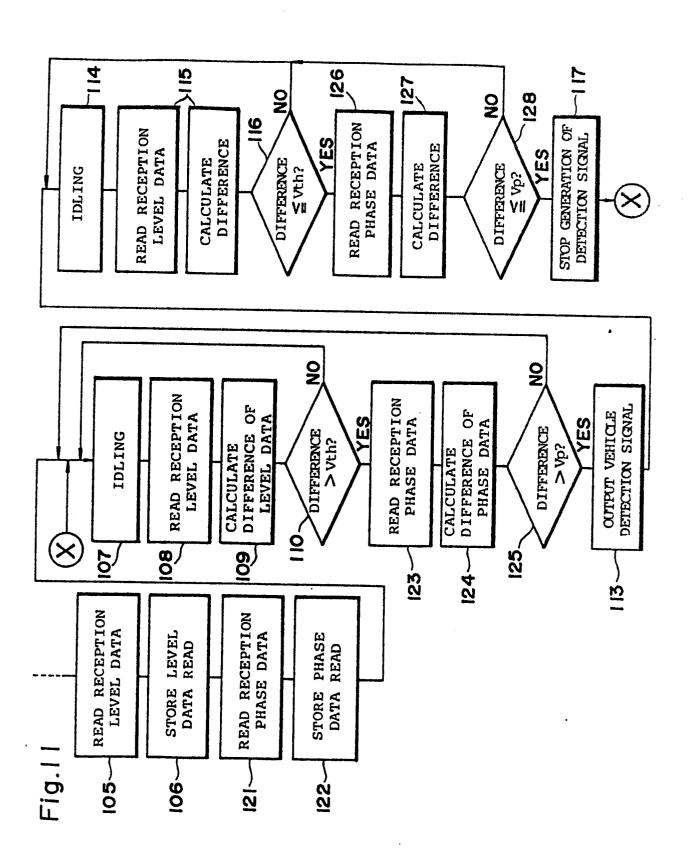


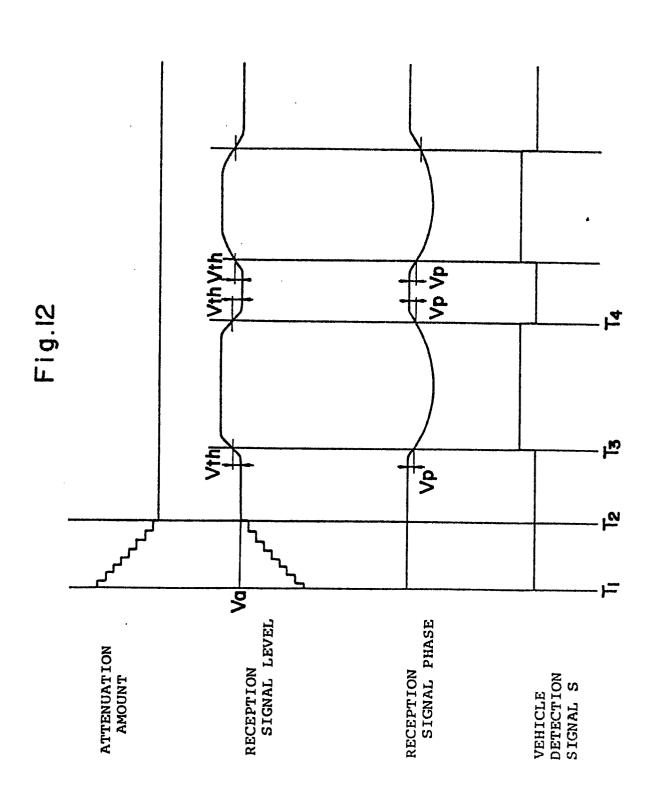
Fig.8

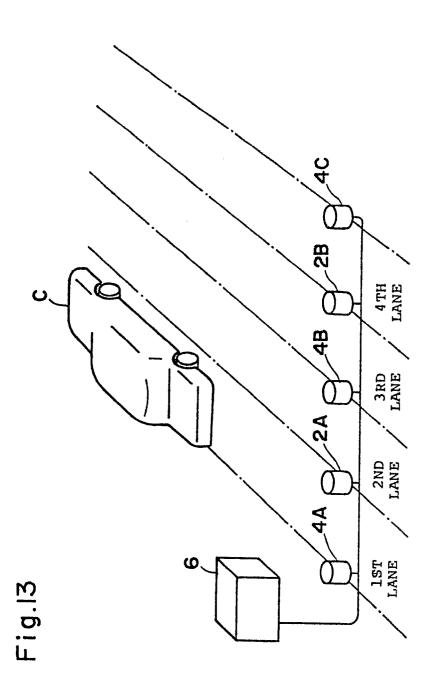












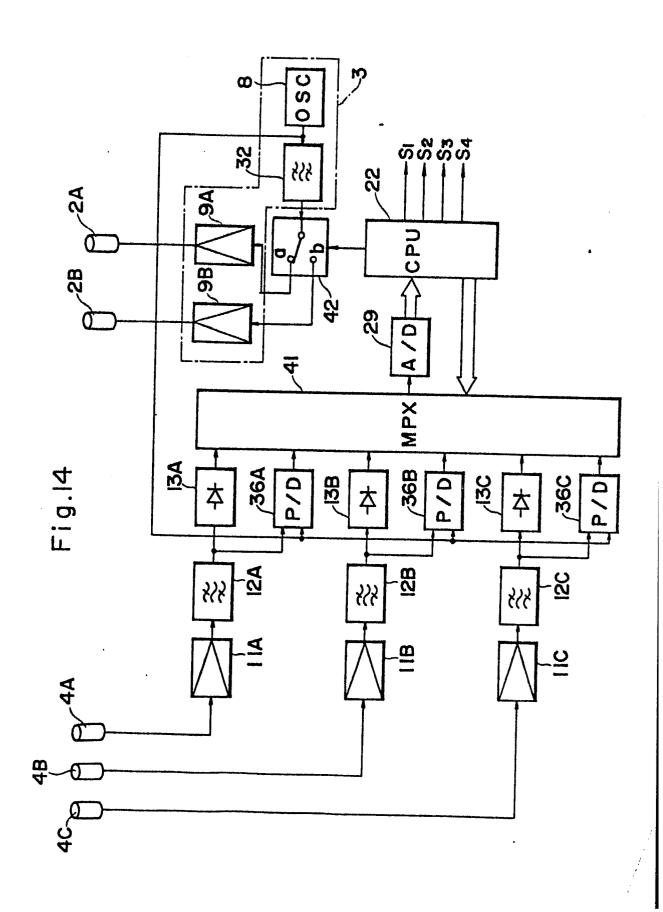


Fig.15

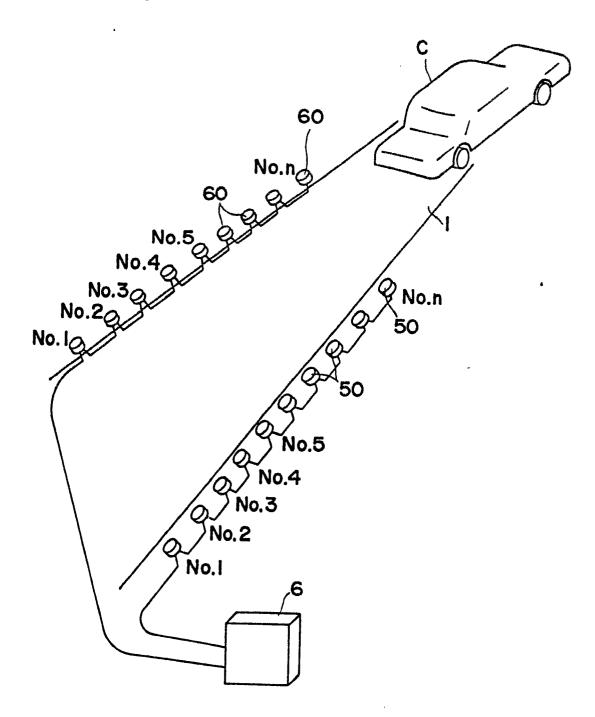


Fig.16

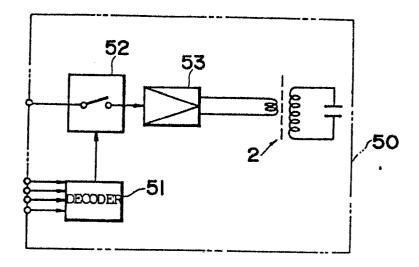


Fig.17

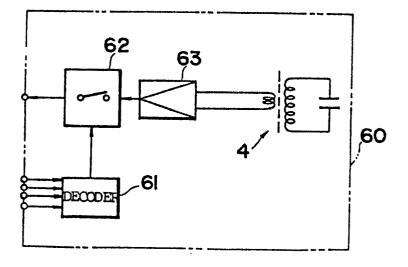


Fig.18

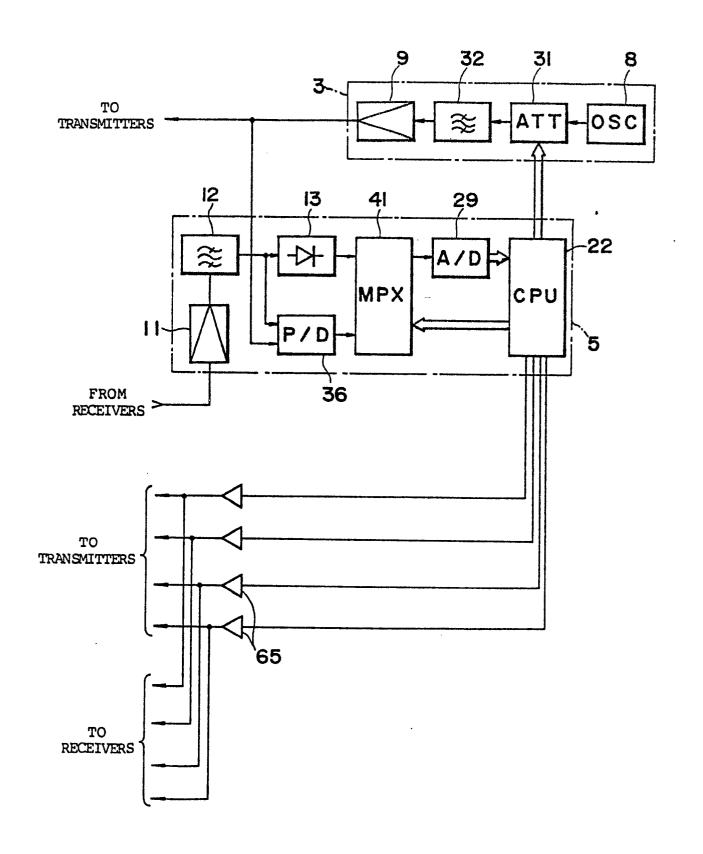


Fig.19

lst bit DESIGNATION SIGNALS 2ND BIT 3RD BIT. 4TH BIT -No.1 DRIVING OF TRANSMITTERS No.n No.I RECEPTION IN No.2 -RECEIVERS No.3 No.n

Fig.20

