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- © Cursor circuit for a dual port memory.
- system that includes a storing circuit 1 for storing image data. The storing circuit includes a first port 8 to provide access to the image data to the display system and a second port 12 to provide access to the image data to a display device 10 for displaying the image data. A combining circuit 22 is further provided to combine the image data with cursor data in the storing circuit when the storing circuit is being accessed by the display device. However, the combining circuit removes the cursor data from the image data when the image data is being accessed by the display system through the first port.

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EUROPEAN SEARCH REPORT

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	DOCUMENTS CONSID	DERED TO BE RELEVA	NT		
Category	Citation of document with ind of relevant pass		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 4)	
A	IBM TECHNICAL DISCLO 26, no. 10B, March 1 5622-5623, New York, "Cursor display on r devices" * Whole document *	984, pages US; A.C. GAY:	1-5,7	G 09 G 1/00	
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A	IEEE COMPCON SPRING PAPERS, San Francisc 25th-28th February 1 IEEE, New York, US; "New silicon to solv graphics problems"	o, California, 1985, pages 276-279, K. GUTTAG et al.:			
				TECHNICAL FIELDS	
				SEARCHED (Int. Cl.4)	
				G 06 F G 09 G	
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	The present search report has b	een drawn up for all claims			
71	Place of search	Date of completion of the search	1	Examiner ABRAM R	
	HE HAGUE	29-09-1989			
THE HAGUE CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document		E : earliér pater after the fill other D : document c L : document c	T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document		