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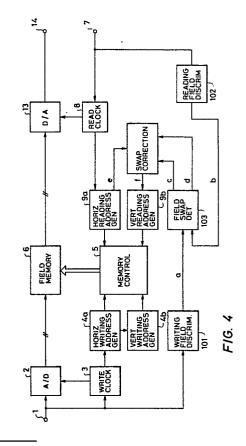
EUROPEAN PATENT APPLICATION

- 21 Application number: 87301866.7
- (51) Int. Cl.4: HO4N 5/04 , HO4N 5/93

- 2 Date of filing: 04.03.87
- 3 Priority: 06.03.86 JP 49253/86
- (43) Date of publication of application: 23.09.87 Bulletin 87/39
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- Applicant: PIONEER ELECTRONIC CORPORATION
 No. 4-1, Meguro 1-chome
 Meguro-ku Tokyo 153(JP)
- Inventor: Miyazaki, Haruhito Pioneer Electronic Corporation Tokorozawa Works No. 2610, Hanazono 4-chome Tokorozawa-shi Saitama(JP)
- Representative: Jackson, Peter Arthur et al GILL JENNINGS & EVERY 53-64 Chancery Lane
 London WC2A 1HN(GB)

- 54 Video memory control device.
- (57) A video memory control device for a two-field video signal that is buffered in a memory (6) having capacity for one field. The phase of the writing to and reading from the memory (6) may be such that the fields are being swapped on output. To display the correct sequence without losing any fields, the reading address for one field is delayed by IH (one horizontal scanning line period) or the reading address for the other field is advanced by IH dependent on the detected phase. A similar technique may alternatively be applied to the writing side.



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EUROPEAN SEARCH REPORT

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