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(7) Applicant: SCHLUMBERGER ELECTRONICS (U.K.)
LIMITED
124 Victoria Road
Farnborough Hampshire GU14 7PW (GB)

18 Lochlea Road
Newlands Glasgow Scotland (GB)

Representative: Pears, David Ashley et al REDDIE & GROSE 16 Theobalds Road London WC1X 8PL (GB)

54) Programmable time switch.

(a) A microprocessor controlled time switch has a liquid crystal display device (13) with provision for displaying day of the week and time. Below the display a row of four keys (21 to 24) are used to select programming functions and a key (20) can be pressed at either end to increment and decrement numerical values. The keys (21 to 24) are multi-function and, above each key, the display device (13) includes a column of three legends, of which only one is ever displayed at a time. The microprocessor controls the legends (27) so that, in all phases of operation, the functions performed by the keys (21 to 24) are apparent.

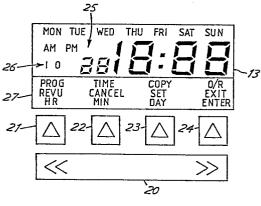


Fig.2



EUROPEAN SEARCH REPORT

EP 87 30 6424

		NSIDERED TO BE RELEV		
Category	Citation of document v	vith indication, where appropriate, nt passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)
Y	GB-A-2 056 730 HORLOGER SA) * Figures *	(CENTRE ELECTRONIQUE	1-4	G 04 G 15/00
Y	GB-A-2 081 477 * Figures *	(SEIKO KOKI K.K.)	1-4	
Y	GB-A-2 028 547 * Figures *	(BBC LTD)	1-4	
				TECHNICAL FIELDS SEARCHED (Int. Cl.4)
				G 04 G G 04 C
	The present search report	has been drawn up for all claims		
	Place of search	Date of completion of the se	arch	Examiner
TH	E HAGUE	22-12-1987		LMANS U.G.J.R.

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