

11 Publication number:

0 333 353 A3

(12)

EUROPEAN PATENT APPLICATION

(21) Application number: 89302113.9

(51) Int. Cl.5: G05F 1/56

2 Date of filing: 02.03.89

(30) Priority: 17.03.88 US 169308

Date of publication of application: 20.09.89 Bulletin 89/38

② Designated Contracting States:
DE FR GB IT NL

Bate of deferred publication of the search report: 23.10.91 Bulletin 91/43

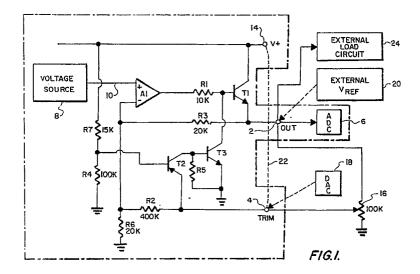
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- Dual mode voltage reference circuit and method.
- © A voltage reference circuit is described which is capable of providing either an internally generated voltage having a trimming capability, or an externally generated voltage, with the use of only two pins (2,4). The internal voltage is connected through an interrupt circuit (T2,T3,R4,R5,R7) to an input/output terminal (2), which can also receive an externally generated voltage. A trimming terminal (4) is used to apply trimming voltage signals to adjust the inter-

nally generated voltage. To convert from the internal (8,A1) to the external voltage source (20), an interrupt voltage is applied to the trimming terminal (4) which is outside of the normal trimming voltage range. This interrupt voltage actuates an interrupt circuit (T2,T3,R4,R5,R7) to interrupt the connection (T1) between the internal voltage source (8,A1) and input/output terminal (2), leaving the output terminal (2) available for the external voltage source (20).



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EUROPEAN SEARCH REPORT

EP 89 30 2113

DOCUMENTS CONSIDERED TO BE RELEVANT					
Category		h indication, where appropriate, vant passages		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. CI.5)
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					TECHNICAL FIELDS SEARCHED (Int. CI.5) G 05 F H 02 J H 03 M
					H 01 L
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Place of search The Hague Date of completion of search 7 August 91			arch	1	Examiner
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CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same catagory A: technological background			E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons		
P:	non-written disclosure intermediate document theory or principle underlying the in		&: member docume		patent family, corresponding