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14.08.91 Bulletin 91/33(71) Applicant: **XAAR LIMITED**
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MATHYS & SQUIRE 10 Fleet Street
London EC4Y 1AY(GB)(54) **Multiplexer circuit.**

(57) A multiplexer circuit for effecting in successive phases of operation thereof actuation of selected devices of respective groups of devices of a series of capacitance actuated devices has a series of parallel electrical paths to which the respective devices are connected. A signal generator is connected across said paths and two capacitors of each device are connected between the path of the associated device and the respective paths on opposite sides of the path of the associated device. First and second switching means are disposed in each path and are closed by respective logic signals applied thereto so that when the first and second switching means of one path are respectively closed and open and the first and second switching means of each of the paths on respective opposite sides of that one path are respectively open and closed, charging of the capacitors connected to that one path takes place and when thereafter the first and second switching means of that one path are respectively open and closed discharge of the capacitors connected to that one path takes place.

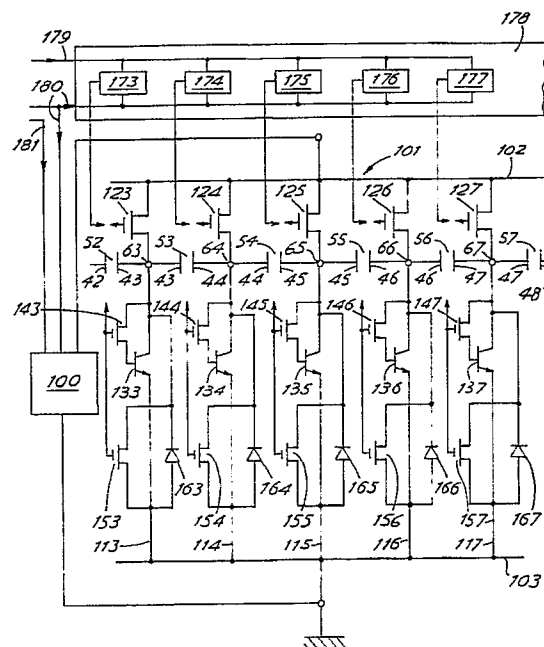


FIG.2

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EUROPEAN SEARCH REPORT

Application Number

EP 89 30 4573

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
A	US-A-4 350 989 (SAGAE et al.) * Figure 7; column 4, line 51 - column 5, line 22 * - - -	1,13	B 41 J 3/04 H 03 K 17/687 H 03 K 17/693
A	GB-A-2 104 005 (K.K. SUWA SEIKOSHA) * Figure 11 * - - - - -	1,13	
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			H 03 K B 41 J
The present search report has been drawn up for all claims			
Place of search		Date of completion of search	Examiner
The Hague		14 May 91	SEGAERT P.A.O.M.P.
CATEGORY OF CITED DOCUMENTS			
X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document T: theory or principle underlying the invention		E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons ----- &: member of the same patent family, corresponding document	