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DESIGNATION

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Electronic means for switching antennas to a common bus.

(57) A circuit for selecting an input to a signal processing network from among several switchable antenna units. Each antenna unit (26) includes a winding (30) which is connected to a common signal bus. The switchable antenna units (12) may be placed in "on" or "off" states in response to a controller, such as a microprocessor (46). One terminal (36,40) of each winding (30,32) is connected to a transistor

(42,48) which causes the terminal to be in an open condition when the unit is unselected, attenuating any signal which might otherwise be transmitted to the signal bus. A second winding (32) is inductively coupled to the first winding (32) and is used to improve attenuation of unselected signals.

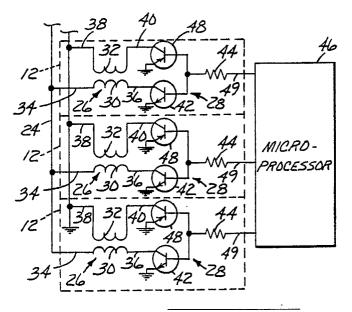


Fig. 3



EUROPEAN SEARCH REPORT

EP 90 30 8154

DOCUMENTS CONSIDERED TO BE RELEVANT						
Category		th Indication, where appropriate, evant passages		elevant o claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)	
Α	GB-A-2 088 173 (RCA) * Page 1, column 1, lines 4: 97-121; claim 1; figure 1 *	3-57; page 1, column 2, li	1,1 nes	0	H 01 Q 3/24	
Α	GB-A-2 057 772 (ELLIOTT BROTHERS) * Page 1, column 2, lines 84-118; claims 1-6; f		1,2 *	0		
Α	DE-A-2 253 115 (INSTITU NIK)			0		
	* Page 3, lines 15-25; page	4, line 24 - page 5, line 6	; fig. *			
Α	DE-A-1 591 926 (HOWAL * Claim 1; fig. *	DTSWERKE)	1,1	0		
				_	TECHNICAL FIELDS SEARCHED (Int. Cl.5)	
				Ī	H 01 Q	
					G 01 V	
					H 01 P H 03 H	
	The present search report has	been drawn up for all claims				
		Date of completion of	search		Examiner	
		04 March 91			BUTLER N.A.	
Y: A:	CATEGORY OF CITED DOCI particularly relevant if taken alone particularly relevant if combined wit document of the same catagory technological background		E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons			
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