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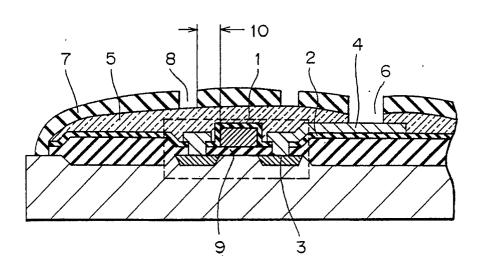
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(54) Semi-conductor device.

The invention provides a semi-conductor device having as a passivation layer covering a metal wire (4) of a MOS transistor and a semi-conductor surface a double layer comprising a silicon nitride layer (7) and a silicide glass layer (5) beneath the silicon

nitride layer. A window (8) is provided through the silicon nitride layer at a location away from a gate electrode (9) of the MOS transistor. Advantageously, the edge of the window lies between 20 μ m and 100 μ m from an edge of the gate electrode.

FIG.1



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EUROPEAN SEARCH REPORT

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CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same catagory A: technological background O: non-written disclosure P: intermediate document			E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document		