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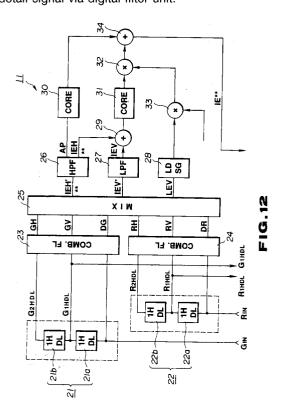
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- (54) Signal processing circuit for solid-state imaging apparatus.
- 57) A signal processing circuit for a solid-state imaging apparatus in which an imaging output signal from each solid-state image sensor of an imaging section making use of spatial offsetting is digitized to form a detail signal for image enhancement by digital signal processing. A first delay circuit (21) having at least one delay unit with a delay time approximately equal to one horizontal scanning period provides the above mentioned delay time to a green color imaging signal of the digital output signal obtained by digitizing image output signals read from the solid-state image sensors. The signal synthesis is then performed in a first synthesis circuit (23) to limit the band of the green color imaging signal in the perpendicular imaging signal. A second delay circuit (22) having at least one digital delay unit having a delay time about equal to one horizontal scanning period provides the above mentioned delay time to one of the red or blue color imaging signals or combined red and blue color imaging signals of the above mentioned digital output signal. Signal synthesis is then performed by a second synthesis unit (24) to limit the band of the red or blue color imaging signal or the combined red and blue color imaging signals in the perpendicular direction. Equal amounts of the first and second synthesis signals are summed together in a summation unit (25) so as to be outputted as the horizontal

detail signal via digital filter unit.





EUROPEAN SEARCH REPORT

EP 90 11 9070

ategory	Citation of document with indication of relevant passages	, where appropriate,	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)	
,Α	EP-A-0 368 354 (SONY CORPORAT	ION)	1-3,8	H04N9/04	
	* column 5, line 48 - column				
	* column 11, line 52 - column				
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	* page 3, line 20 - page 6, l				
	* page 9, line 15 - page 11,				
				TECHNICAL FIELDS SEARCHED (Int. Cl.5)	
				H04N	
	The present search report has been draw	n up for all claims	-		
	Place of search	Date of completion of the search	1	Examiner	
THE HAGUE		26 JUNE 1992	DE [DE DIEULEVEULT A.J.	
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