(19) Europäisches Patentamt
European Patent Office
Office européen des brevets

(11) Publication number: **0 430 211 A3**

(12) **EUROPEAN PATENT APPLICATION**

(21) Application number: **90122753.8**

(51) Int. Cl.5: **H04N 5/95**

(22) Date of filing: **28.11.90**

(30) Priority: **28.11.89 JP 308069/89**

(43) Date of publication of application:
05.06.91 Bulletin 91/23

(84) Designated Contracting States:
DE FR GB

(88) Date of deferred publication of the search report:
26.08.92 Bulletin 92/35

(71) Applicant: **SONY CORPORATION**
7-35, Kitashinagawa 6-chome Shinagawa-ku
Tokyo(JP)

(72) Inventor: **Suzuki, Yoshinori**
c/o Sony Corporation, 7-35 Kitashinagawa
6-chome
Shinagawa-ku, Tokyo(JP)
Inventor: **Nakamura, Yuji**
c/o Sony Corporation, 7-35 Kitashinagawa
6-chome
Shinagawa-ku, Tokyo(JP)

(74) Representative: **Schmidt-Evers, Jürgen,**
Dipl.-Ing. et al
Patentanwälte Mitscherlich, Gunschmann
Dr. Körber, Schmidt-Evers, Melzer, Dr. Schulz
Steinsdorfstrasse 10
W-8000 München 22(DE)

(54) **Time base correcting apparatus.**

(57) A time base correcting apparatus having a memory (5) for storing a reproduced video signal reproduced by a rotary magnetic head from slant tracks of a magnetic tape (101) which is transported at a desirable tape running speed relative to a tape running speed upon recording mode, a write in-line address counter (4x) for generating a write in-line address signal and a write line address counter (4y) for generating a write line address signal for the memory (5), a write clock signal generating circuit (3) for generating a write in-line address increment clock signal and a write line address increment clock signal each being synchronized with a horizontal synchronizing signal separated from the reproduced video signal, and for supplying the same to the write in-line address counter (4x) and the write line address counter (4y), a read in-line address counter (15x) for generating a read in-line address signal and a read line address counter (15y) for generating a read line address signal for the memory (5), and a read clock signal generating circuit (14) for generating a read in-line address increment clock signal and a read line address increment clock signal each being synchronized with the reference horizontal synchronizing signal, and for supplying the same to the read in-line address counter (15x) and to the read line address counter (15y), in which when a detecting circuit (6 to 13) detects that the single cycle of one periodical signal of the periodical signals which are respectively synchronized with the reproduced horizontal synchronizing signal and the reference horizontal synchronizing signal is involved in the single cycle of the other periodical signal, the line address increment clock signal which relates to the horizontal synchronizing signal synchronized with the other periodical signal is inhibited from being supplied to the line address counter (4y, 15y) so that the reproduced video signal is removed or interpolated in the unit of lines at an equal position of a reproduced picture. Thus, a reproduced picture having no distortion can be obtained regardless of tape running speed and tape running direction in the reproduction mode.

EP 0 430 211 A3

FIG. 6

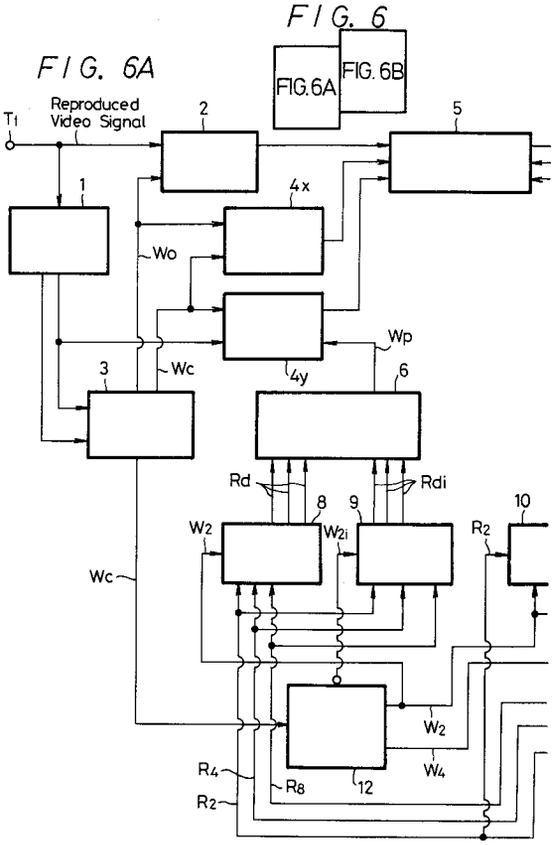
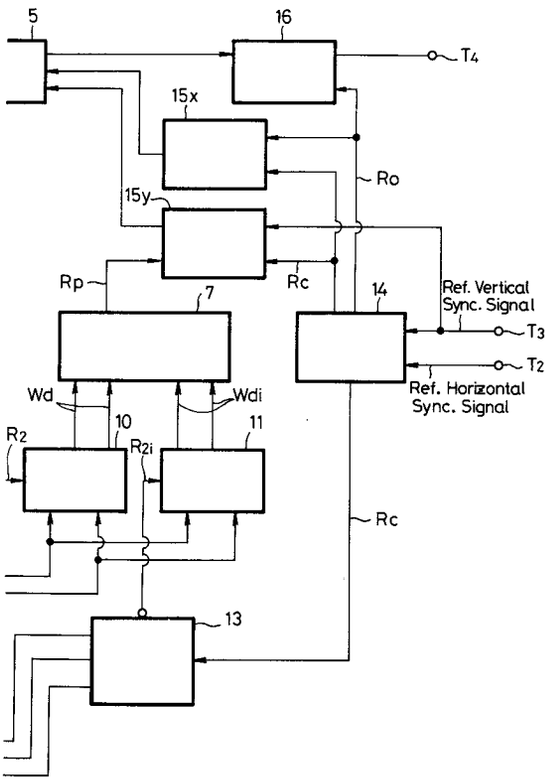


FIG. 6A

FIG. 6B



European Patent Office

EUROPEAN SEARCH REPORT

Application Number

EP 90 12 2753

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
Y A	US-A-4 063 284 (TATAMI) * column 1, line 65 - column 2, line 17 * * column 2, line 35 - line 54 * * column 7, line 1 - column 8, line 19 * * column 9, line 24 - column 10, line 38 * * column 11, line 57 - line 60; figures 1,3,4,5 * ---	1 2,3	H04N5/95
Y	US-A-4 389 678 (MIZUKAMI ET. AL.) * column 1, line 7 - line 65 * * column 2, line 28 - line 40 * * column 7, line 27 - column 8, line 15; figure 5 * ---	1	
A	PATENT ABSTRACTS OF JAPAN vol. 6, no. 211 (E-137)23 October 1982 & JP-A-57 116 485 (HITACHI DENSHI KK) 20 July 1982 * abstract * ---	1,3	
A	US-A-4 134 131 (HOPKINS) * column 1, line 57 - column 2, line 15 * * column 3, line 66 - column 4, line 12 * * column 4, line 25 - column 5, line 52; figures 1,3,4 * *abstract* ---	1-3	TECHNICAL FIELDS SEARCHED (Int. Cl.5) H04N
A	US-A-3 860 952 (TALLENT ET. AL.) * column 2, line 49 - line 56 * * column 12, line 64 - column 15, line 44 * -----	1-3	

The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
THE HAGUE	03 JULY 1992	WENTZEL J.F.

CATEGORY OF CITED DOCUMENTS

X : particularly relevant if taken alone
Y : particularly relevant if combined with another document of the same category
A : technological background
O : non-written disclosure
P : intermediate document

T : theory or principle underlying the invention
E : earlier patent document, but published on, or after the filing date
D : document cited in the application
L : document cited for other reasons

& : member of the same patent family, corresponding document

EPO FORM 1503 03.82 (P0401)