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- A phase locked loop with reduced frequency/phase lock time.
- ⑤ A phase locked loop provides a programmable frequency output signal. A phase detector (18) detects a phase difference between a reference frequency (F_{REF}) divided by a first number (R), and a frequency of the output signal (FR_{OUT}) divided by a second number (N). First and second counters (14,16) receive the first and the second input numbers to divide a respective frequency. Whenever an input number is loaded, a load signal (JAM LOAD) resets the phase detector and causes each counter to be loaded, which reduces the lock time of the loop.

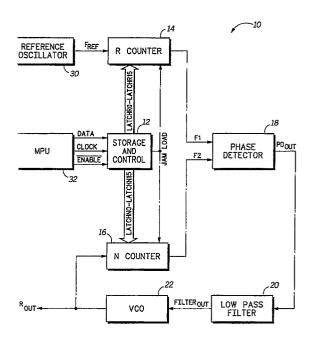


FIG.1



EUROPEAN SEARCH REPORT

EP 90 31 3927

DOCUMENTS CONSIDERED TO BE RELEVANT					
Category		th indication, where appropriate, vant passages		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. CI.5)
Υ	US-A-4 817 199 (WALLRA * column 3, line 23 - line 33 line 18; figures 3,4 *	•	I	-8,10	H 03 L 7/199
Υ	CH-A-6 700 20 (WILLI STI * page 2, right column, line 63; figures 1-3 *	•		-8,10	
Υ	EP-A-0 272 938 (NIPPON CORPORATION) * column 3, line 2 - line 15;		HONE 1,	5,6,8,10	
Y	US-A-4 378 509 (HATCHE * figure 3 *	ETT ET AL.)	3,	4	
Α	US-A-4 714 899 (KURTZN * column 1, line 57 - line 59 		1-	-8,10	
					TECHNICAL FIELDS SEARCHED (Int. CI.5)
					H 03 L
	The present search report has I	been drawn up for all claims			
Place of search Date of completion of search			arch		Examiner
	The Hague	29 August 91			BALBINOT H.
CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same catagory A: technological background O: non-written disclosure			E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding		
Р:	intermediate document theory or principle underlying the in		documen		patent rainny, corresponding