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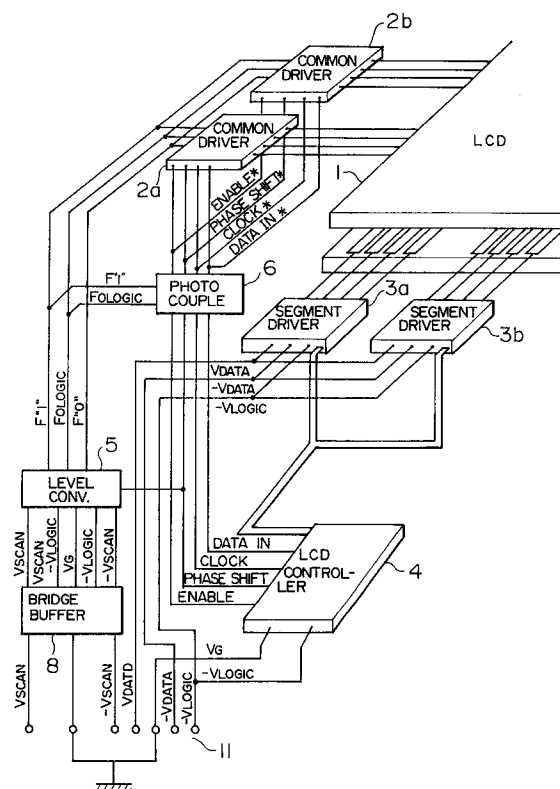
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(54) **Drive circuit for dot matrix display.**

(57) A drive circuit for a dot matrix liquid crystal display which receives polarity changing drive voltages in different fields, comprising segment drivers for supplying low voltage picture signals and common drivers for supplying a first voltage having a large voltage difference from the picture signal during a selected time and a second voltage having a small voltage difference from the picture signal during non-selected time.

FIG. 1



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EUROPEAN SEARCH REPORT

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EP 91 10 3736

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
X	US-A-4 413 256 (S. YASUDA et al.) * abstract; figure 6; column 3, line 58 - column 4, line 21 * - - -	4	G 09 G 3/36
X	WO-A-8 705 429 (ALCATEL) * page 13, line 32 - page 15, line 11; figure 1 * - - -	3	
A	EP-A-0 216 168 (CANON) * abstract; figure 1; page 6, line 5 - page 7, line 9; page 8, lines 1-14 * - - -	1,3,4	
A	PATENT ABSTRACTS OF JAPAN vol. 13, no. 80 (P-832), 23 February 1989; & JP - A - 63262622 (NEC) 28.10.1988 * the whole document * - - -	1,3,4	
A	DE-C-3 212 863 (SEIKO) * column 5, line 47 - column 6, line 15; figure 10 * - - - - -	4	
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			G 09 G
Place of search		Date of completion of search	Examiner
Berlin		18 October 91	SAAM C A
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone		E : earlier patent document, but published on, or after the filing date	
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