



(12)

EUROPEAN PATENT APPLICATION

(21) Application number : **91305387.2**

(51) Int. Cl.⁵ : **G06F 13/42**

(22) Date of filing : **14.06.91**

(30) Priority : **25.06.90 US 543276**

(43) Date of publication of application :
02.01.92 Bulletin 92/01

(84) Designated Contracting States :
DE FR GB

(88) Date of deferred publication of search report :
07.10.92 Bulletin 92/41

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(54) **Multiple speed expansion card.**

(57) A timing circuit (18) for a multiple speed expansion card (14) generates a variable ready signal indicating when the expansion card can accept data enabling the expansion card to function in computer systems having different bus speeds. A controller circuit (12) of such a computer system exchanges information with the expansion card via a bus (16) at a speed related to the ready signal.

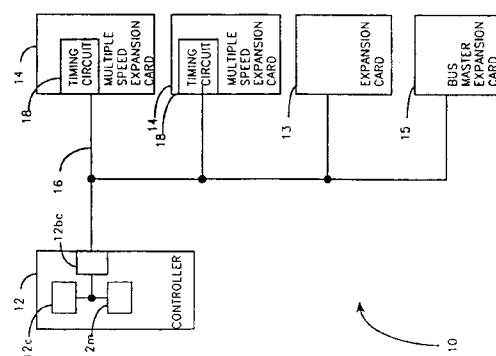


FIG. 1



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number

EP 91 30 5387

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
A	EP-A-0 138 045 (IBM) * abstract * * page 3, line 18 - page 4, line 6 * * claims 1,3 * ----	1,10	G06F13/42
A	EP-A-0 133 359 (AT&T) * page 2, line 3 - line 14; claims 1,2,6,7 * -----	1,10	
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			G06F
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 03 AUGUST 1992	Examiner MCDONAGH F.M.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

EPO FORM 1503 01.92 (P0401)