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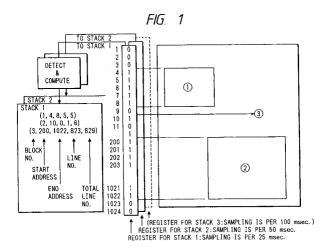
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- [54] Image information control apparatus and display system.
- An image information control apparatus includes a partial write detector having at least two types of memory units for detecting and storing addresses accessed to a VRAM in units of lines in a scanning direction, thereby repeating the detection and the storage at different cycles, a circuit for performing calculations to recognize partial write information from contents of each of the memory units, memory units for storing the respective calculation results, a circuit for comparing the memory contents to determine a size relationship between partial write areas,

a partial write ID signal controller for controlling a partial write ID signal on the basis of the size relationship between partial write areas and externally outputting the signal, and a circuit for, even when partial writing is being executed, forcibly interrupting the partial writing in accordance with a state of an external refresh control signal, starting refresh, and restarting the partial writing in accordance with a partial write state and a change in state of the refresh control signal.





EUROPEAN SEARCH REPORT

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ategory	Citation of document with indication, where appropriate, of relevant passages		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
`	EP-A-0 368 117 (CANO	N K.K.)	1	G09G3/36
١	EP-A-0 361 471 (CANO	N K.K.)	1	
				TECHNICAL FIELDS SEARCHED (Int. Cl.5)
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	The present search report has be	en drawn up for all claims Date of completion of the search	<u> </u>	Examiner
Place of search THE HAGUE		23 OCTOBER 1992	-	FARRICELLA L.
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