

(19)



Europäisches Patentamt
European Patent Office
Office européen des brevets



(11) Publication number:

0 482 678 A3

(12)

EUROPEAN PATENT APPLICATION(21) Application number: **91121918.6**(51) Int. Cl.⁵: **G09G 1/16**(22) Date of filing: **23.07.85**(30) Priority: **23.07.84 US 633367****23.07.84 US 633383****23.07.84 US 633384****23.07.84 US 633385****23.07.84 US 633386****23.07.84 US 633387****23.07.84 US 633388****23.07.84 US 633389**

(43) Date of publication of application:

29.04.92 Bulletin 92/18(60) Publication number of the earlier application in accordance with Art.76 EPC: **0 182 454**

(84) Designated Contracting States:

DE FR GB

(88) Date of deferred publication of the search report:

16.09.92 Bulletin 92/38(71) Applicant: **TEXAS INSTRUMENTS INCORPORATED****13500 North Central Expressway
Dallas Texas 75265(US)**(72) Inventor: **Bond, Jeffrey C.****3207 Rifle Gap Lane
Sugar Land, TX 77478(US)
Inventor: Gutttag, Karl M.****11602 Ensbrook****Houston, TX 77099(US)**Inventor: **Thaden, Robert C.****6425 S. Gessner No. 1220****Houston, TX 77036(US)**Inventor: **Pinkham, Raymond****2023 Retriever Lane****Missouri City, TX 77489(US)**Inventor: **Novak, Mark****4225-D Airport Road****Colorado Springs, CO 80910(US)**Inventor: **Watts, Mark W.****24307 Kennedy Ranch Dr.****Hockley, TX 77447(US)**Inventor: **Vanaken, Jerry****13563 Fernhill****Sugar Land, TX 77478(US)**Inventor: **Moravec, John V.****212 Hinricher Dr.****Willow Springs, IL. 60480(US)**Inventor: **Albachten, Rudy J. III****87 Lyons Drive****Centerville, OH 45459(US)**(74) Representative: **Blanco White, Henry Nicholas et al****ABEL & IMRAY Northumberland House****303-306 High Holborn****London WC1V 7LH(GB)****EP 0 482 678 A3**(54) **Video system controller with a row address override circuit.**

(57) The present invention is a video system which includes a data processor (1), such as a microprocessor, for processing data, a video memory (5) for storing data from the data processor corresponding to an image to be displayed, a display (11), such as a raster scan cathode ray tube, for displaying the image data stored in the video memory means, and a video system controller (3) connected to the video memory (5) For controlling the transfer of data from the video memory (5) to the

display (11) and between the data processor (1) and the video memory (5). The video memory (5) is preferably a multiport dynamic random access memory including an addressable memory array. The video system controller (3) performs a number of functions including refresh of the dynamic random access memory, multiplexing of the various access requests of the video memory and control of the blanking interval of the display. This is accomplished by having a first portion which operates synchro-

nously with the video memory (5) and a second portion which operates synchronously with the data processor (1). The transfer operations in the video

system controller are preferably controlled through the use of a programmable state machine which manipulate inputs in a logic array.

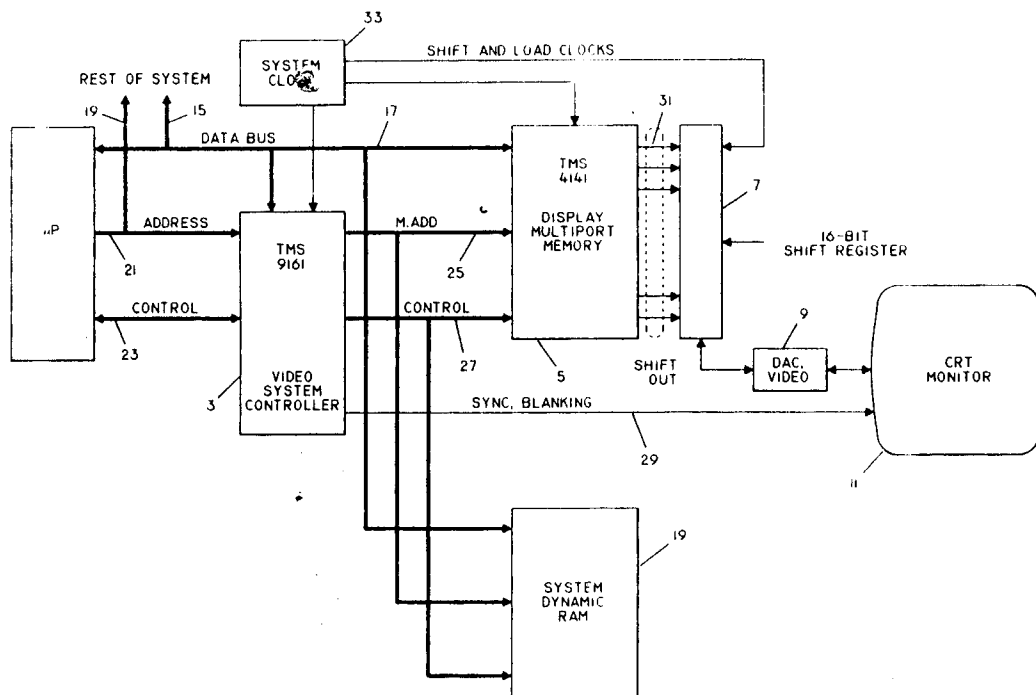


FIG. 1



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number

EP 91 12 1918

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)
A	US-A-4 298 931 (TSUGUJI TACHIUCHI) * abstract; figures 1,10,11 * * column 5, line 11 - column 5, line 23 * * column 9, line 29 - column 10, line 4 * ---	1	G09G1/16
A	EP-A-0 080 043 (SIEMENS AKTIENGESELLSCHAFT) * abstract; figures 1-3 * * page 5, line 24 - page 7, line 32 * -----	1-3	
			TECHNICAL FIELDS SEARCHED (Int. Cl.4)
			G09G
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 30 JUNE 1992	Examiner VAN ROOST L, L.A.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons * : member of the same patent family, corresponding document			