



(12) **EUROPEAN PATENT APPLICATION**

(21) Application number : **92400072.2**

(51) Int. Cl.<sup>5</sup> : **H01L 39/24, H01L 21/90**

(22) Date of filing : **10.01.92**

(30) Priority : **10.01.91 JP 12377/91**

(43) Date of publication of application :  
**15.07.92 Bulletin 92/29**

(84) Designated Contracting States :  
**DE FR GB**

(88) Date of deferred publication of search report :  
**16.12.92 Bulletin 92/51**

(71) Applicant : **FUJITSU LIMITED**  
**1015, Kamikodanaka Nakahara-ku**  
**Kawasaki-shi Kanagawa 211 (JP)**

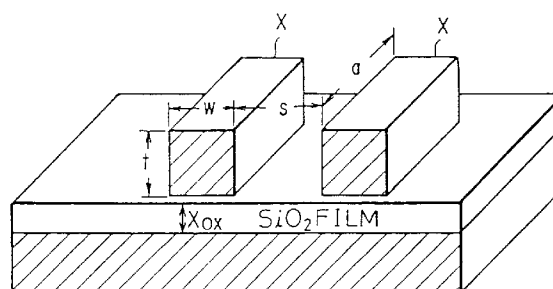
(72) Inventor : **Sasaki, Nobuo, c/o Fujitsu Limited**  
**1015, Kamikodanaka, Nakahara-ku**  
**Kawasaki-shi, Kanagawa 211 (JP)**  
Inventor : **Ishigaki, Toru, c/o Fujitsu Limited**  
**1015, Kamikodanaka, Nakahara-ku**  
**Kawasaki-shi, Kanagawa 211 (JP)**

(74) Representative : **Joly, Jean-Jacques et al**  
**CABINET BEAU DE LOMENIE 55, rue**  
**d'Amsterdam**  
**F-75008 Paris (FR)**

(54) **A signal processing device and a method for transmitting signal.**

(57) The device comprises superconducting wiring for providing an output signal or a transmitted signal correctly corresponding to an input signal without increasing the temperature of the device. The signal processing device has a signal input end, a signal output end, and proper signal processing circuits interposed between the signal input and output ends. The superconducting wiring connects the signal input end with the signal output end. An output end of the wiring is open or in a high-impedance state. The pulse width of an output pulse signal (O) provided from the output end is narrower than that of an input pulse signal (T3) provided to the input end, and the pulse voltage of the output pulse signal is higher than that of the input pulse signal. The length of the wiring is set to be approximately 25% of the product of the pulse width and the phase velocity of the signal transmitted through the wiring.

Fig.2





European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number

EP 92 40 0072

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
A	SOLID STATE ELECTRONICS vol. 32, no. 11, November 1989, OXFORD, GB pages 947 - 959 Tewksbury S.K. et al: 'High Tc superconductors for digital system interconnection' * page 948; figure 1 * * page 953, paragraph 2 - page 955, paragraph 1 *	1-5	H01L39/24 H01L21/90
A	PATENT ABSTRACTS OF JAPAN vol. 13, no. 272 (E-777)22 June 1989 & JP-A-10 62 020 ( FUJITSU LTD ) * abstract *	1-2	
A	PATENT ABSTRACTS OF JAPAN vol. 12, no. 72 (E-588)5 March 1988 & JP-A-62 214 714 ( FUJITSU LTD ) * abstract *	1-2	
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			H01L
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 07 OCTOBER 1992	Examiner HAMMEL E.J.
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ..... &amp; : member of the same patent family, corresponding document</p>			

EPO FORM 1503 03.82 (P0401)