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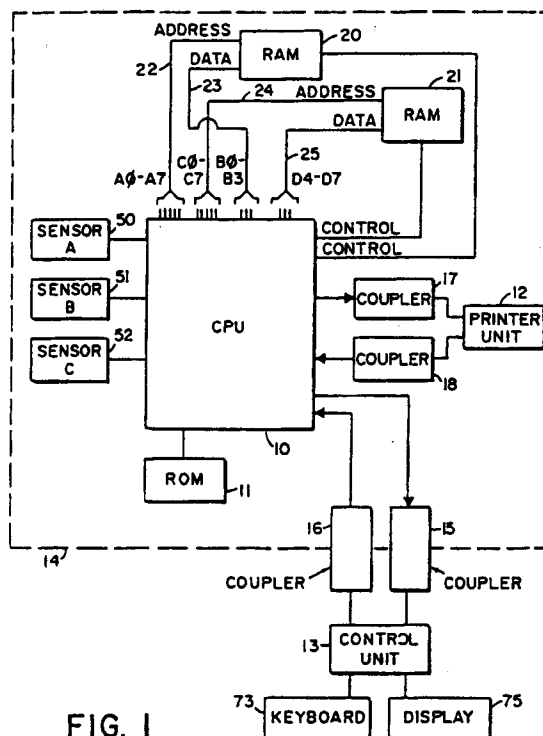
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EUROPEAN PATENT APPLICATION(21) Application number: **92114140.4**(51) Int. Cl.⁵: **G07B 17/02**(22) Date of filing: **25.01.83**(30) Priority: **29.01.82 US 343877**(43) Date of publication of application:
19.11.92 Bulletin 92/47(60) Publication number of the earlier application in
accordance with Art.76 EPC: **0 231 452**(84) Designated Contracting States:
BE CH DE FR GB LI NL(88) Date of deferred publication of the search report:
13.01.93 Bulletin 93/02(71) Applicant: **PITNEY BOWES INC.**
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Stamford Connecticut 06926-0790(US)(72) Inventor: **Check, Frank T., Jr.**
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Hoffmann, Eitle & Partner Patentanwälte
Arabellastrasse 4
W-8000 München 81(DE)(54) **Microprocessor systems for electronic postage arrangements.**

(57) A microprocessor system suitable for an electronic postage arrangement includes a microprocessor (10) having controllable address, data and control lines. First and second random access memories (20,21) are connected to the address, data and control lines. A printer (12) is coupled to the microprocessor. Feedback means (18) is provided to signal the printer setting to the microprocessor to enable updating of accounting data in the first and second random access memories.

**FIG. 1**



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EUROPEAN SEARCH REPORT

Application Number

EP 92 11 4140

Page 1

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)
Y,D	EP-A-0 019 515 (FRIDEN) * page 3, line 26 - page 5, line 27 * * page 10, line 10 - page 12, line 2 * * page 15, line 31 - page 16, line 9 * * page 18, line 12 - page 19, line 6 * * page 40, line 15 - page 42, line 21; claims 1-9; figures *	1,2,5-8, 10-12	G07B17/02
A	---	13,14, 16-19	
Y	DE-A-3 024 370 (SIEMENS) * page 5, line 32 - page 6, line 12; figures *	1,2,12	
Y	IBM TECHNICAL DISCLOSURE BULLETIN. vol. 10, no. 10, March 1968, NEW YORK US pages 1484 - 1485 HACKL E.A. 'Dynamic allocation of high availability storage' * the whole document *	1,12	
Y	EP-A-0 017 406 (PITNEY BOWES) * page 3, line 10 - page 4, line 32 * * page 13, line 29 - page 14, line 22; figures *	5-7	TECHNICAL FIELDS SEARCHED (Int. Cl.4)
A	---	1,2,12	G07B G06F
Y	US-A-3 544 777 (WINKLER) * claims; figures *	5,10,11	
Y,D	US-A-4 301 507 (SODERBERG) * column 1, line 26 - column 2, line 48 * * column 7, line 24 - column 8, line 61 * * column 14, line 55 - column 15, line 65; figures 3-9,14-16 *	8	
A,D	---	1,2,5-7, 10-13, 16-19	
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 16 NOVEMBER 1992	Examiner MEYL D.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			



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EUROPEAN SEARCH REPORT

Application Number

EP 92 11 4140
Page 2

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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)
A	GB-A-2 079 223 (ALCATEL) * abstract; claims 20-25; figures 10,11 * ---	1,5-7, 10-14	
A	IBM TECHNICAL DISCLOSURE BULLETIN. vol. 20, no. 10, March 1978, NEW YORK US pages 4071 - 4072 CHAMOFF E.A. 'Nonvolatile totals implementation' * the whole document * ---	1,5,12	
A	EP-A-0 026 734 (LICENTIA) * page 5, line 6 - page 9, line 7; claims 1,4-6; figures * -----	1,5,12	
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int. Cl.4)
Place of search THE HAGUE		Date of completion of the search 16 NOVEMBER 1992	Examiner MEYL D.
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