



(12) **EUROPEAN PATENT APPLICATION**

(21) Application number : **93304852.2**

(51) Int. Cl.⁵ : **H01R 13/66**

(22) Date of filing : **22.06.93**

(30) Priority : **30.06.92 US 906610**
30.06.92 US 906813

(43) Date of publication of application :
05.01.94 Bulletin 94/01

(84) Designated Contracting States :
DE FR GB IT NL

(71) Applicant : **THE WHITAKER CORPORATION**
Suite 450, 4550 New Linden Hill Road
Wilmington, Delaware 19808 (US)

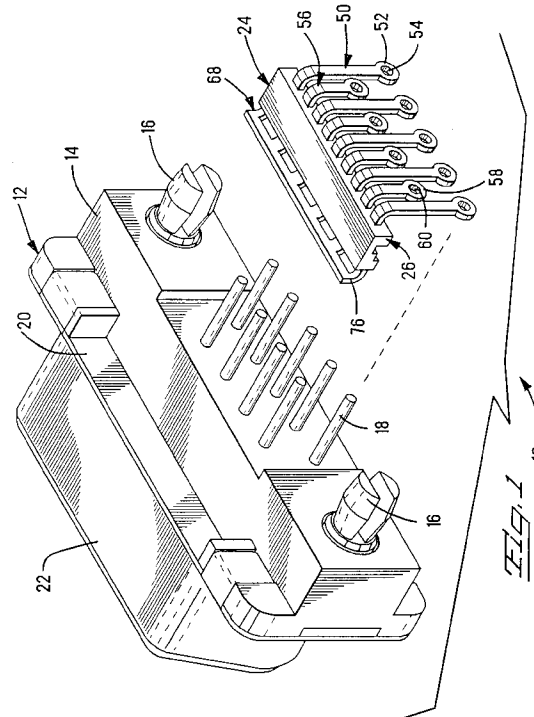
(72) Inventor : **Brown, Thomas Edmund**
304 Indian Creek Drive
Mechanicsburg, Pennsylvania 17055 (US)

Inventor : **Heinlen, Frederick Jay**
25 Fairway Drive
Etters, Pennsylvania 17319 (US)
Inventor : **Lynch, Thomas John**
110 E Powderhorn Road
Mechanicsburg, Pennsylvania 17035 (US)
Inventor : **Collins, Christopher J.**
643 Cuenca Way
Fremont, California 94536 (US)
Inventor : **English, James Marsh**
219 West Main Street
Annaville, Pennsylvania 17003 (US)
Inventor : **Farrar, John Charles**
4096 Wimbledon Drive
Harrisburg, Pennsylvania 17112 (US)

(74) Representative : **Warren, Keith Stanley et al**
BARON & WARREN 18 South End Kensington
London W8 5BU (GB)

(54) **Electrical over stress device and connector.**

(57) A device (24) for protecting against electrical over stress (EOS) of electrical components includes first contacts (50, 56) that interconnect to signal contacts (18) of a connector (12) and a second contacts (68) that connect to the ground circuit (20) of the connector (12), with a rigid plastic housing (26) holding the device contacts so that ends (55, 65) have a precise spacing gap (71), the gap being filled with a material matrix (72) of insulating and conductive materials having characteristics in conjunction with the gap (71) to define transit voltage suppression by grounding the signal contacts (18) of a connector (12). The device (24) is made bendable to fit onto connectors and conform to the connector geometry.



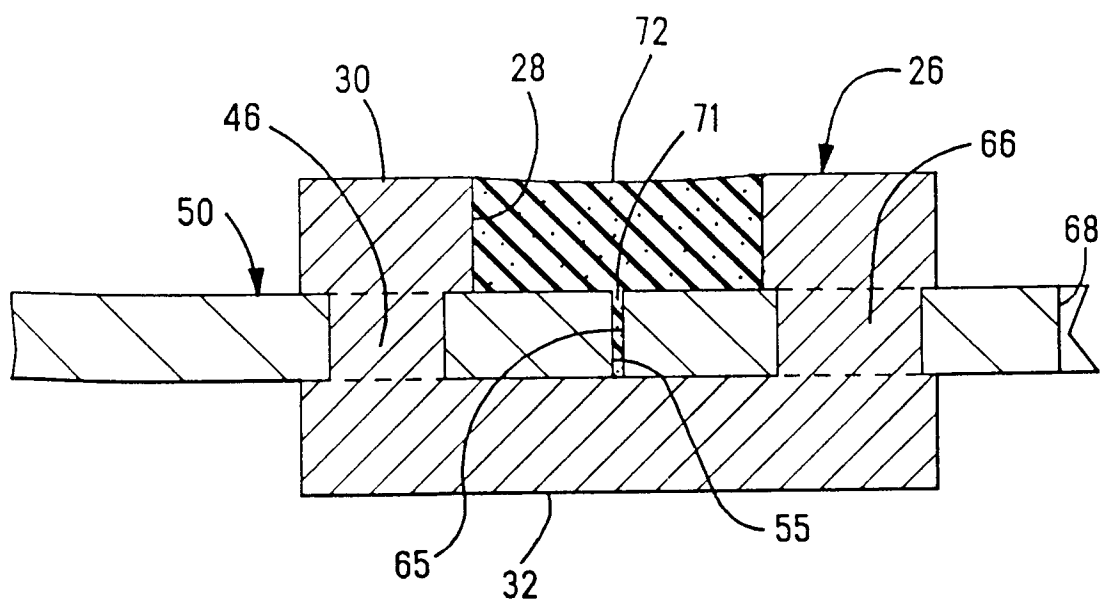


Fig. 9

This invention relates to an electrical over stress device and electrical connector for protecting sensitive electronic components.

Packaging of electronic components such as integrated circuits routinely finds hundreds of circuit functions within a volume that heretofore was occupied by a single element, such as a resistor, capacitor, or inductor. As a result, the physical spacings of the elements have become quite small, and the elements themselves relatively fragile and susceptible to damage from transient signals caused by a host of phenomena met in the practical environment of use. Induced voltages from short circuits, lightning strikes, static electric charges built up on individuals or equipment, all may find their way into components and destroy the elements thereof to cause a lack of function. This failure, resulting from electrical over stress (EOS) has led to a host of circuit protection devices, typically mounted on the same circuit board upon which the components are carried. These devices have included electrical fuses that open up responsive to IR heating, typically relatively slow in action, as well as varistors, zener diodes, and a host of other devices including spark gap devices, thin film devices, and LC filters have been employed. U.S. Patent Number 4,729,752 discloses a transient suppression device in the form of a back-to-back diode mounted on a substrate that fits within an electrical connector. This device suppresses voltages outside a specified level as they are conducted through the signal conductors of the connector and incorporation into the connector saves valuable board space.

With respect to the foregoing, all of the protection devices require either a special installation and handling or an alteration of existing designs of connectors in order to accommodate the EOS devices. Many of the prior art EOS elements are bulky, slow to respond, and expensive to acquire and install to provide protection to components upon boards. Accordingly, it is an object of the present invention to provide an EOS device that may be fitted onto electrical connectors to provide board mounted component protection against unwanted transients.

It is a further object to provide an EOS device that can be utilized with existing connector designs, without significant alteration of such connectors.

It is an additional object to provide a cost effective, readily employed EOS device and connector to provide component protection.

It is still a further object to provide, in combination, an EOS device and an electrical connector of improved features.

The present invention achieves the foregoing objective through the provision of an EOS device of a geometry to be fitted onto electrical connectors. The device includes a plastic housing of a temperature stable material containing first and second contacts insert molded therein, each of the contacts having an

end contained within an interior volume of the housing to define a precise spacing gap. That gap is filled with a material matrix of insulating, conductive, or semi-conductive particles of an extremely fine size flowed into the volume of the housing and having characteristics, taken in conjunction with the spacing gap to provide EOS protection through a change in resistance allowing unwanted transients to flow from signal paths in a connector to a ground circuit of the connector. The contacts of the device include first contacts that attach to the signal contacts of a connector and further contact that attach to the ground circuit of a connector. The various contacts of the device are made of a thin conductive metal that is bendable to allow the device to be made to conform to a portion of the geometry of the connector, to nest and rest on portions of the connector. This allows a given device to be made to conform to the geometries of different connectors to extend the utility of a given device part number.

A method of manufacture includes forming, as by stamping or etching, thin conductive sheet stock to define the conductive elements of the device to include carrier means such as holes in the stock allowing a precise fixturing to define the spacing gap between contacts. Alternatively, a spring-loaded shim may be used to define a precise gap. The fixtured contact portions are then insert molded by a mold clamping onto the sheet stock to mold around the surfaces of portions defining said contacts with the spacing gap between the ends of the contacts being kept clear by a shim. Once the insert molding step is achieved, the carrier portions of the sheet stock may be removed and an interior volume formed in the housing filled under pressure with a material matrix of an appropriate mixture of insulating, conductive, or semi-conductive materials to fill the gap and establish a precise dimension for the matrix that results in a precise voltage suppression, clamping voltage characteristic for the device. The contact of the device are readily bendable to conform to a variety of standard connector formats and be applied thereto between the signal contacts of such connector and the ground circuits.

Embodiments of the present invention will now be described by way of example with reference to the accompanying drawings in which:

Figure 1 is a perspective view taken from the rear of a multi-contact electrical connector, showing an EOS device spaced therefrom preparatory to application thereto.

Figure 2 is a view taken from the left-hand side of the connector as shown in Figure 1 with the rearwardly projecting fastener portions removed and with the EOS device shown applied and, in phantom, positioned prior to application.

Figure 3 is a side view similar to that of Figure 2 with an alternative embodiment of the device shown

applied to the connector.

Figure 4 is a plan view of the sheet metal blank, profiled prior to the application of a housing thereon to form a device.

Figure 5 is a plan view of the elements shown in Figure 4 following application of a hosing to such elements.

Figure 6 is an end, elevational and sectional view taken through lines 6-6 of Figure 5 including a sectioning view of a mold preparatory to closure shown in phantom.

Figure 7 is a view of the device shown in Figure 6 following removal of carriers and formed into one configuration.

Figure 8 is a perspective view in partial section of the device shown in Figure 7.

Figure 9 is a side, elevational, and sectioned view of the EOS device following insert molding and molding of the matrix material within the housing.

Figure 10 is a voltage, time plot of a characteristic response of the EOS device of the invention.

Figure 11 is a sectional view of an alternative embodiment of the EOS device.

Referring now to Figure 1, an electrical connector assembly 10 is shown to include the connector 12 and an EOS device 24 preparatory to assembly. The connector 12 is representative of board mounted connectors that serve input, output functions to boards containing a variety of components that define computer, communication, and business machine functions. Typically, such boards contain arrays of integrated circuits that are interconnected through conductive traces within the board and connected to drive and be driven by circuits through the connector, cables, and other connectors attached to such connector. Connectors such as 12 typically include plastic and insulating housing 14, fasteners such as 16 that attach the housing to a printed circuit board, fitting through holes therein. Signal contacts 18 are shown extending from the rear face of housing 14, the forward ends being contained within the housing to mate with posts or receptacles of a mating connector, not shown, that engages connector 12. Also typical of connectors such as 12 is the provision of a grounding shield 20, made of metal that extends around the periphery of the housing 14 and, as indicated, joins an integral metal shell 22 projecting forwardly, note the shell in Figures 2 and 3. Signal contacts 18 typically include post portions ended as at 19 that fit through holes or apertures in boards and are soldered thereto to join the traces of such boards and be interconnected to the components on the board.

Signals to and from these components pass through the contacts 18, and it is these signals that can, on occasion, cause EOS problems. A lightning strike, for example, not proximate to the equipment served by the connector and the components on a board upon which the connector is mounted may nev-

ertheless induce voltages and cause a surge in transient currents to flow coupling the cable to which the connector is connected too induce transients on such cable that are conducted through the connector, onto the board, and into the components to destroy them. Static charges can build up on individuals and can readily exceed 15,000 volts and be discharged by touching a piece of equipment, cable, a keyboard, or other object interconnected by the cable and connector to a board and component. Frequently, these transient voltages are of short duration, having rise times on the order of nanoseconds and durations well under a millisecond. They nevertheless carry energy levels quite sufficient to destroy the fragile, closely-spaced traces within electronic components such as integrated circuits. The problem is complicated by the fact that high speed data transfer, typically digital pulses, typically includes fast rise time pulse configurations, but these are generally of finite voltage levels, well below those levels that can destroy or damage components.

In Figure 10, a voltage spike labeled T_v with a rise time as indicated, and an actual level indicated in phantom following the rise time. The energy contained within the envelope of T_v may very well be sufficient to damage or destroy electronic components. With respect to Figure 10, the scale for time and voltage may vary considerable from application to application with the ordinate units ranging from hundreds of volts to thousands of volts, and with the time units ranging from picoseconds to hundreds of nanoseconds per division. An EOS device should have a function that follows the solid curve, sensing the fast rise time and excessive voltage, operating to cause a conduction between a signal path where the transit is located to a ground circuit to in essence clamp the voltage in the manner shown by the portion of the curve labeled C_v for clamping voltage. In this way, a circuit component will be protected by virtue of not having to experience a continuation of the high fields associated with the higher voltages of T_v and additionally, the joule energy of the transient voltage, noting the reduction of energy associated with the difference between T_v and C_v in Figure 10.

Reference is hereby made to three U.S. Patent Numbers 4,331,948 and 4,726,991 drawn to electrical over stress protection materials and U.S. Patent Number 4,977,357 drawn to an over voltage protection device and material; the three of which are incorporated by reference herein with respect to defining types of material matrices useful in EOS protection. In these patents, materials are taught, including an insulating material combined with conductor or semi-conductor particles coated with insulating material in a matrix that results in a rapid response to high energy electrical transients and controls resistance as between ground and signal circuits to provide transient protection. In the description hereinafter to follow, matrix

materials are referred to in use and method of manufacture with respect to EOS devices and connectors like that shown in Figure 1, the materials of the above-mentioned patents being preferred in certain applications due to the fast rise time responses; but it also being understood that in the broad application of transient protection, other materials having matrix constituents varying to accommodate different voltages of transients of different characteristics are fully contemplated. As mentioned in the patents, the particle size, the choice of insulating film or insulating material, the choice of semi-conductor materials, can all have an effect on the response characteristic of the device utilizing such matrix.

An additional parameter that regulates voltage transient response is the gap between electrodes attached to signal and ground circuits, such gap defining a spacing in which the matrix material resides and defining the chains of conductive or semi-conductive particles existing in such matrix and their characteristic response to voltage transients, the spacing of the gap helping to determine response characteristics. Satisfactory performance has been found in the range of 0.001 inches to 0.010 inches, preferably in the range 0.002 inches to 0.004 inches.

Referring back to Figure 1, an EOS device 24 includes a plastic housing 26 with a series of first contacts 50 and 56 extending from one side thereof and a series of further or second contacts 68 extending from the other side thereof, connected together by a commoning bar 76. The device 24 is shown in a configuration to be applied to the connector 12 with the body of housing 26 resting on a surface of housing 14 of the connector in the manner shown in Figure 2. As can be seen, the portion 76 rests against the grounding shield 20 and is joined thereto as by soldering or other means as known in the art. The first contacts 50 and 56 end in portions that fit over contact 18. Contact 50 thus includes a rounded portion 52 having an aperture 54 that slips over a post portion of contact 18 and is interconnected thereto as by soldering or other means; with the contact 56 having a rounded portion 58 having an aperture 60 to fit over contact 18 and be interconnected thereto as by solder or other means. As shown in Figure 2, the EOS device 24 fits closely to the connector 12, piggybacks on a surface thereof with the various contacts made to conform to the geometry of connector 12.

In Figure 3, and alternative embodiment 12' of the connector includes a housing 14', signal contacts 18' ended as at 19', a grounding shield 20' extending forwardly as at 22'. The essential difference between connector 12' and 12 is that 12' includes right angle posts 19 as part of signal contacts 18', which are intended to extend through the holes of a printed circuit board and be soldered thereto, with the device 24' nested beneath the connector 12', between the connector and a circuit board (not shown)

to be joined to the signal and grounding shield as indicated in Figure 3.

As can be appreciated from Figures 1-3, the invention contemplates an EOS device package that is conformable to reside within different connector profiles and configurations.

Referring now to the construction of the EOS device, reference is made to Figures 4-9. In Figure 4, a profile including flat conductive metal blanks 40 and 62 are shown separated by edges as at 55 and 65, respectively. The blank 40 includes a carrier edge portion 42 having carrier holes 44, and the blank 62 includes an outside carrier 63 having carrier holes 64. Additionally, interiorly of the blanks are further holes 46 and 66 and the separation edge surfaces 55 and 65 of the blanks. Blank 40 further includes a series of first contacts 50 and 56 joining carrier 42 through terminal portions, such as the terminal portion 52 having aperture 54 connected to first contact 50; shorter first contacts 56 having terminal portions 58 having aperture 60. The first contacts 50 and 56 and the remaining corresponding first contacts of blank 40 are arranged in a pattern so that the apertures 54 and 60 are on centers complimentary to the contact centers of the connector, the post portions of signal contacts 18. The blanks 40 and 62 are preferably stamped and formed out of copper sheet material that is readily bendable. The blanks may be formed for prototype purposes by etching, but if this technique is utilized, the end surfaces 55 and 65 are preferably electropolished to provide a precise finish of constant surface dimension. The formed shape of an EOS device 24 can be seen in Figures 1, 2, 7 and 8 wherein the first contacts of 50 and 56 are bent, essentially at a right angle to the body of the device with the portion 76, oppositely bent to facilitate an interconnection of the device to a connector 12. In the embodiment of Figure 3, the first contacts 50' and 56' are left unbent and straight, with the portion 76' being bent to be attached to the grounding shield 20' in the manner indicated.

Following a blanking as shown in Figure 4, with the blanks 40 and 62 separated, a further step of manufacture is accomplished with the blanks being precisely positioned within a mold having an interior configuration to provide a molding of a cross-sectional configuration as shown in Figures 6, 7, and 9. There, as indicated, a housing 26 of plastic material 30 having an interior channel 28, including a flat base 32, is molded around the first contacts 50 and 56 and the second contacts formed out of blank 62. As can be discerned from Figures 5 and 9, the plastic material of housing 26 is caused to flow through the apertures 46 and 66 to lock the contacts to the housing. Housing 26 is preferably molded onto blanks 40 and 62 of appreciable length, containing more contacts than would typically be used in a connector with subsequent cutting off of the appropriate length to define

the numbers of contact required.

In a preferred method of manufacture, the mold utilized to form a housing 26 has two parts of a cross-sectional configuration indicated in Figure 6, the upper part 80 including a pair of cavities 82 that form the walls 30 of the housing and a lower part 86 having a cavity 88 that forms the bottom half of the housing. Centered in the upper part 80 of the mold is a piece of shim stock 84 that extends down into the cavity and against which the end surfaces 55 and 65 of the blanks are forced to define through the shim stock thickness a precise gap 71 therebetween. The mold utilized to mold housing 26 has a plan profile discernible from Figure 5 with various projections extending down between the contacts 50 and 56 and down against the surfaces of stock 62, the material forming the housing is caused to flow through the apertures 46 and 66 as indicated. A thermoset material having thermally stable characteristics to maintain the gap spacing of the device in use and to withstand soldering temperatures when the device is soldered to the contacts and grounding shield is to be preferred. Certain other types of plastics capable of withstanding the higher temperatures of soldering, such as certain liquid crystal polymers or high temperature thermoplastics, may also be employed. As part of the invention method, the use of shim stock allows a precise dimensioning of the gap in that shim stock is available to extremely close thickness tolerances and in micro-finishes allowing a very close placement of the end surfaces 55 and 65 to define the necessary gap control.

With a length of stock molded in the manner shown in Figure 5, the invention contemplates a next step of method which is the filling of the cavity 28 in housing 26 in the manner indicated in Figure 9 by a matrix material 72 forced therein under substantial pressure to flow within the gap 71. A number of the matrix materials have a rather doughy consistency and require substantial screw pressure in order to be made to flow within the narrow confines of gap 71, but experience has shown that this is readily achievable.

Following the operation indicated in Figure 9 and the filling of gap 71 with matrix material 72, the blank and housing may be cut off to length to fit a given connector application and then left straight for an application in the manner shown in Figure 3 or bent in the manner shown in Figure 2 and as shown in Figures 1 and 8 to conform to the geometry of the connector.

Figure 11 discloses an alternative embodiment 124 of the device having housing 126 with a channel 128. In this embodiment, the first and second contacts 150, 168 extend into channel 128 such that they overlap one another and form a gap 171 between the surfaces of the overlying contacts and in which the matrix material is disposed. The housing 126 is made by overmolding the respective contacts in subse-

quential operation.

The invention contemplates a wide range of performance characteristics in terms of voltage transient suppression by selecting different gap dimensions and selecting different matrix materials for use therewith. In prototype applications utilizing matrix materials similar to those taught in the aforementioned patents and a gap dimension on the order of between 0.0020 and 0.0030 inches, a voltage suppression of a transient pulse on the order of 15,000 volts was clamped to a level of 18 volts within a period of 5 nanoseconds.

The electrical over stress device has the advantage that it can be added to existing electrical connectors to protect the electrical circuits on a circuit board on which the electrical connector is to be mounted. Another advantage is that when an electrical connector having electrical contacts to which are electrically connected first contacts of the electrical over stress device with second contacts of the electrical over stress device electrically connected to a shield member of the connector, surfaces of the first and second contacts are separated by a gap in which a matrix material is disposed such that the characteristics of the matrix material is selected to permit voltages above a given level to pass from a signal contact of the connector through the contacts of the electrical over stress device to the shield member.

Claims

1. A device for use with an electrical connector (12) for use in transmitting signals to and from electronic components of a type having a sensitivity to voltages above a given level to require electrical over stress protection and a protection device (24, 124) therefor, the connector having at least one signal contact (18) and at least a shield member (20) with a plastic housing (14) carrying said signal contact, said device (24, 124) having a plastic housing (26, 126) with means (76, 176, 50, 150) mounting the device on the connector, characterized in that the device further includes a first contact (50, 150) connected to the signal contact (18) of the connector and a second contact (68, 168) connected to the shield member (20) thereof, the device first and second contacts each including a surface (55, 65) held by the housing (26, 126) of the device spaced apart by a given gap dimension (71, 171) with a matrix material (72) extending between the surfaces (55, 65) of the first and second device contacts (50, 150, 68, 168) and with the device and the characteristics of the matrix material selected to permit voltages above a given level to pass from a given signal contact through the contacts of said device to the shield member for circuit protection.

2. The device of claim 1 wherein the device housing includes a channel (28, 128) with a matrix material (72) extending into said channel.

3. The device of claim 1 wherein the said signal contact includes a post portion (18) and the first contact (50, 150) of the device includes a portion (52) engaging said post portion.

4. The device of claim 3 wherein the first contact (50, 56) includes a bendable portion extending between the device housing and the post portion adapted to be formed to conform to the connector housing shape.

5. The device of claim 1 wherein the connector includes a plurality of signal contacts (18) arranged in a given pattern and the device includes a like plurality of first contacts (50, 56, 150), one for each signal contact and positioned in said pattern with at least one ground contact (76, 176) common to the first contacts of the second contacts of the device.

6. The device of claim 1 wherein said gap (71) is located between respective ends of said first and second contacts (50, 56) disposed in said housing (26) contacts.

7. The device of claim 1 wherein a portion of said first contact (150) overlies a corresponding portion of said second contact (168) and said gap is located between said overlying portions.

8. In a method of manufacturing electrical over stress devices (24, 124) of a type to be fitted onto an electrical connector (12) having a plastic housing (14) carrying signal contacts (18) in a given pattern and a shield member (20) of a given configuration, characterized by the steps comprising:

a. forming a first flat sheet (40) of conductive material to define first contacts (50, 56, 150) having first ends (55) in the given pattern of the connector held dimensionally by a common carrier portion of (42) the sheet material,

b. forming a second flat sheet (62) of conductive material to define second contacts (76, 176) adapted to be connected to the shield member (20) having second ends (65) carried dimensionally in a common carrier (63) formed from said sheet,

c. positioning the first and second ends (55, 65) apart to define a gap (71, 171) therebetween

d. molding a plastic housing (26, 126) over portions of said first and second contacts while leaving said gap between the ends open

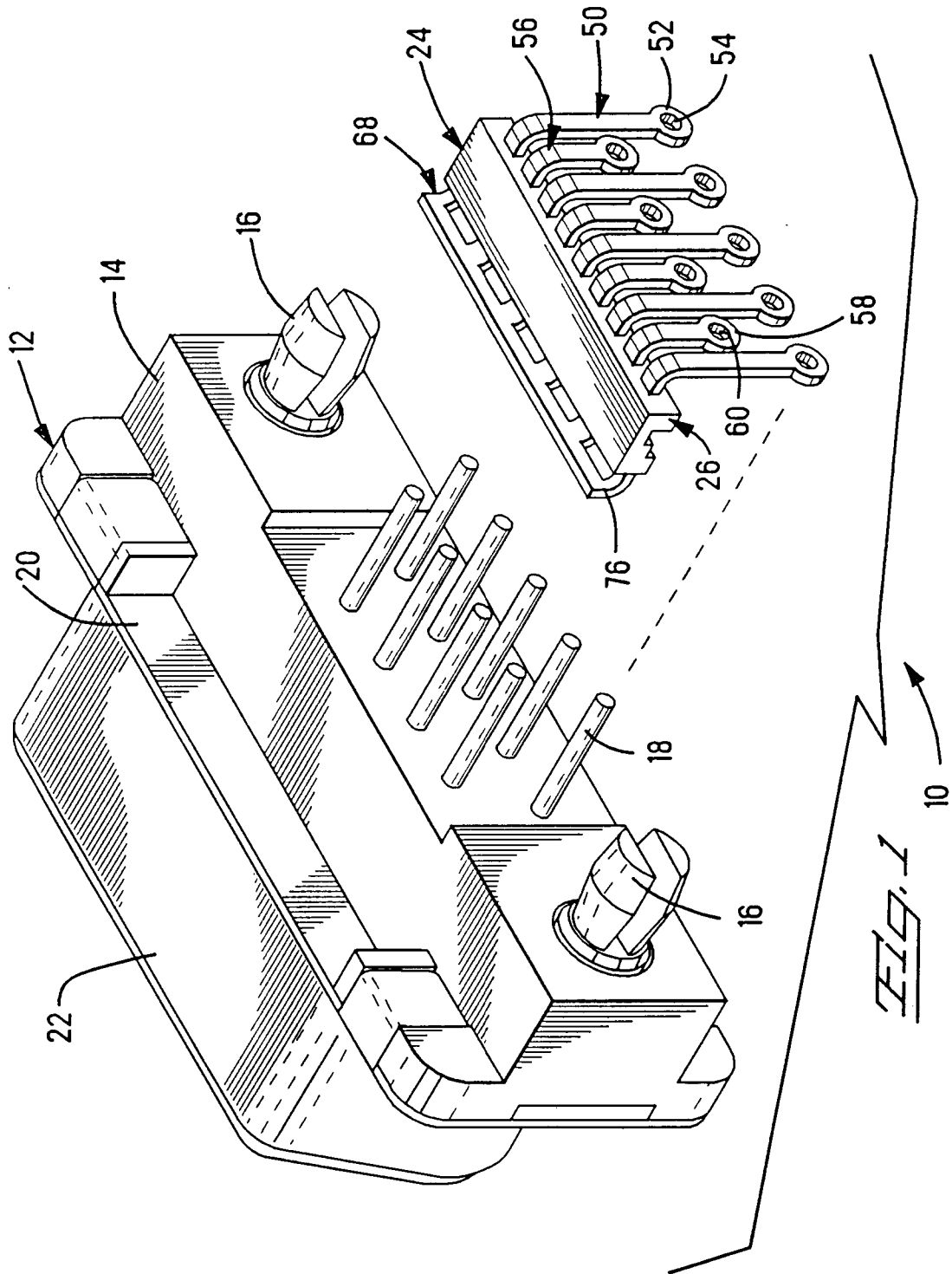
and fixing said gap between said first and second contacts,

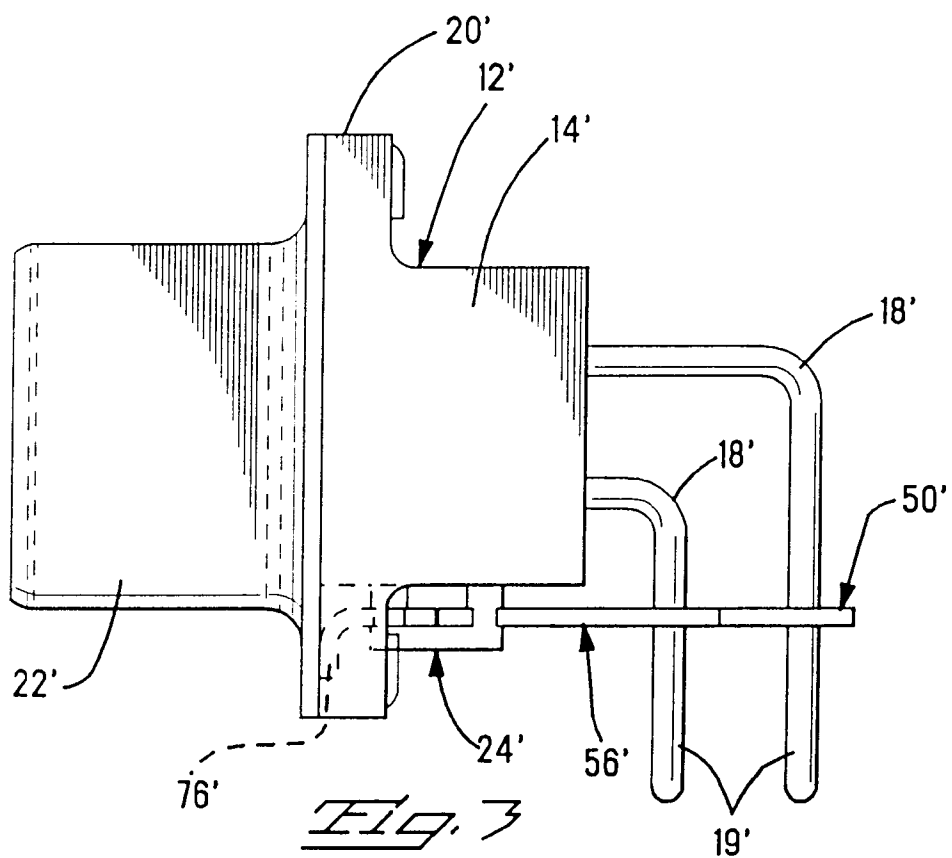
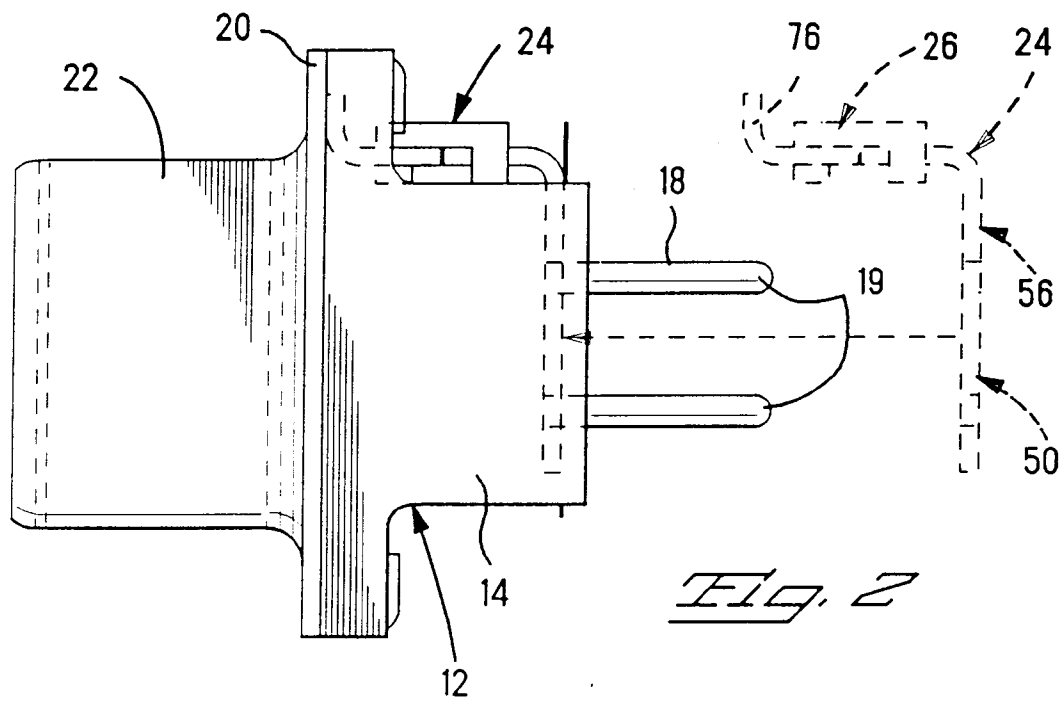
e. filling said gap (71, 171) with a matrix material (72) having characteristics in conjunction with the said gap to provide voltage protection.

9. The method of claim 8 wherein said first and second sheets (40, 62) are formed of a common sheet.

10. The method of claim 8 wherein the said housing (26, 126) is provided with a channel (28, 128) with the said material (72) flowed in said channel under pressure to fill said gap (71, 171).

11. The method of claim 8, including the step of inserting a shim (84) to define said gap (71).





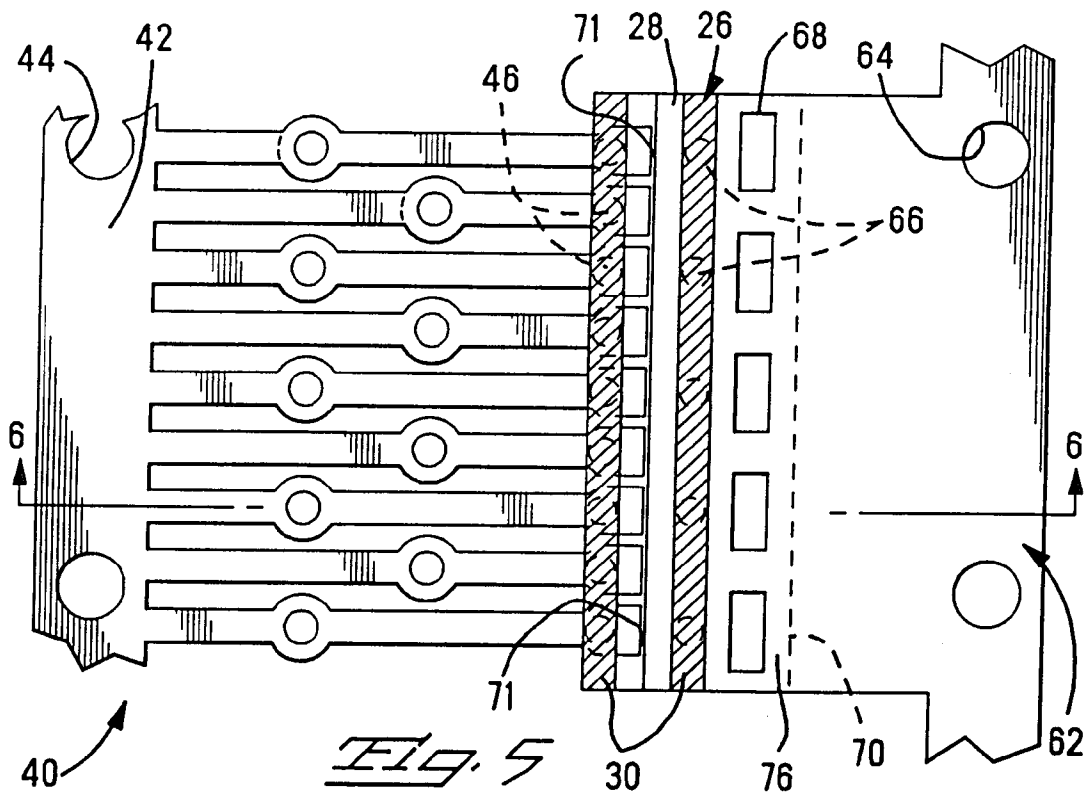
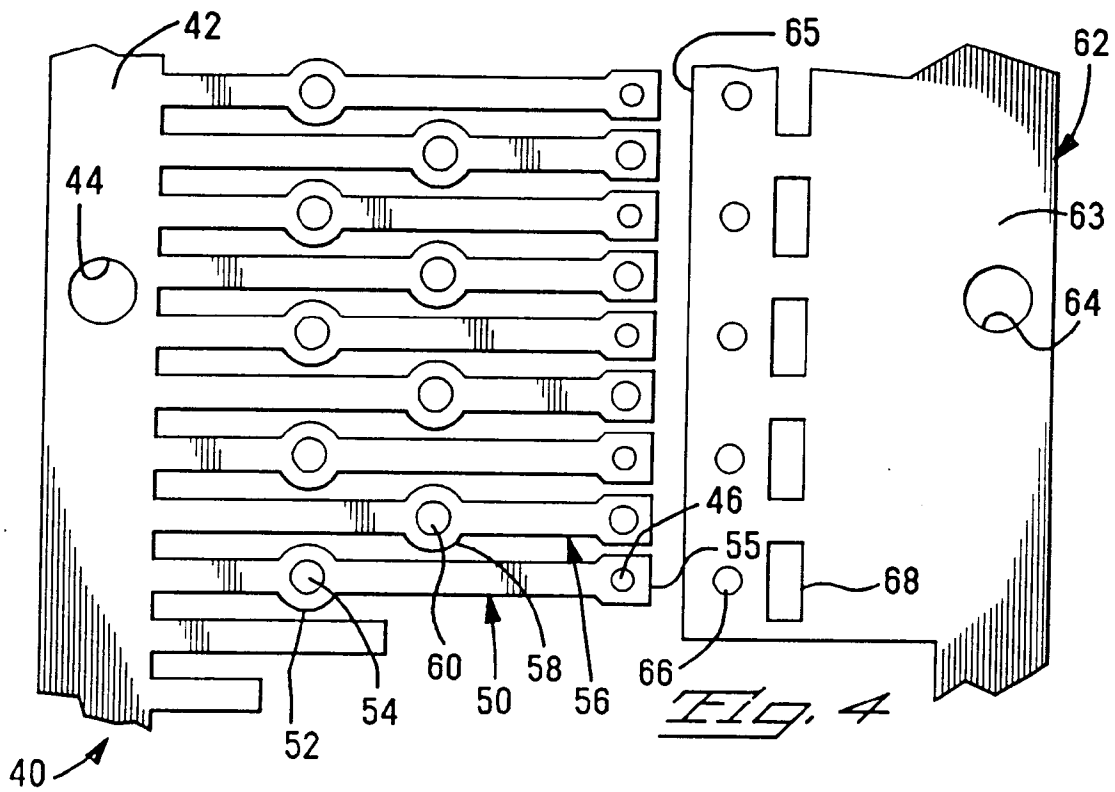


Fig. 6

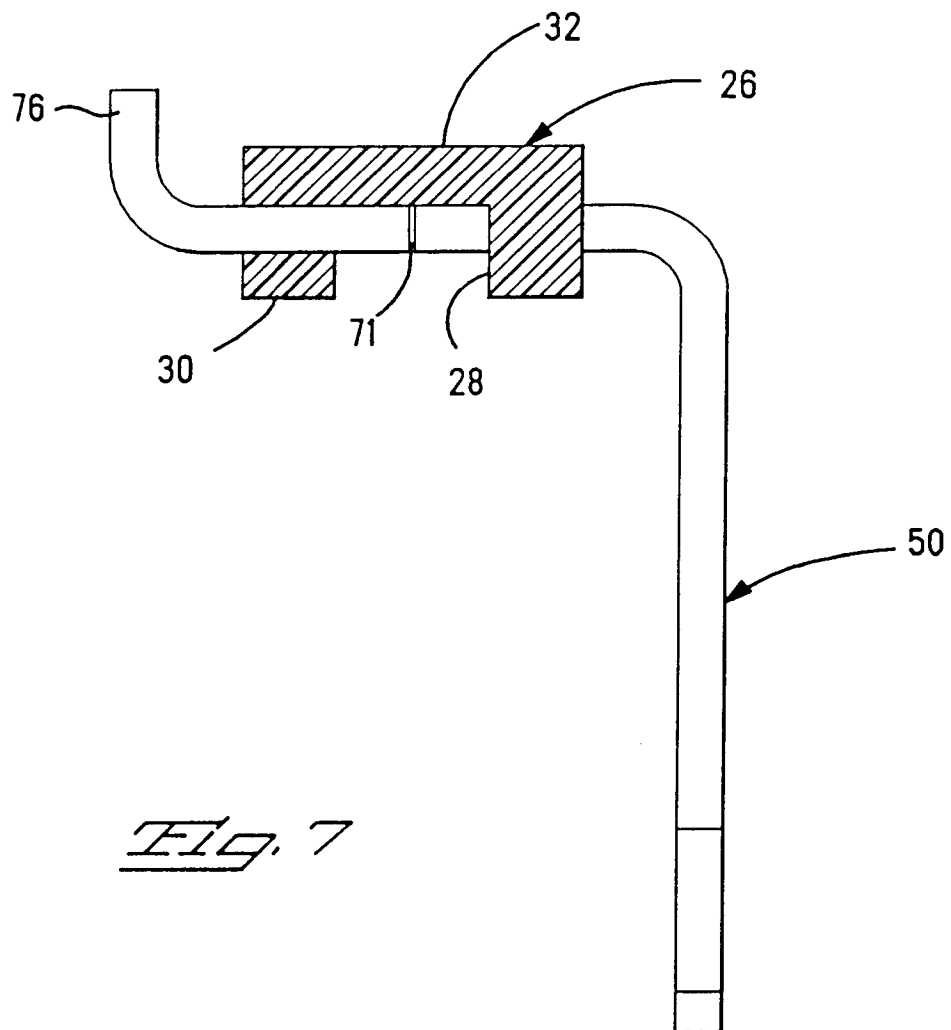
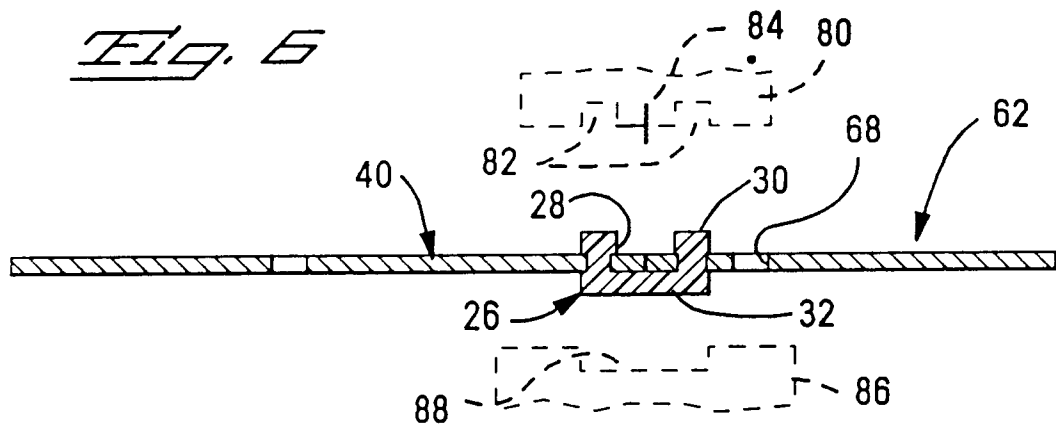
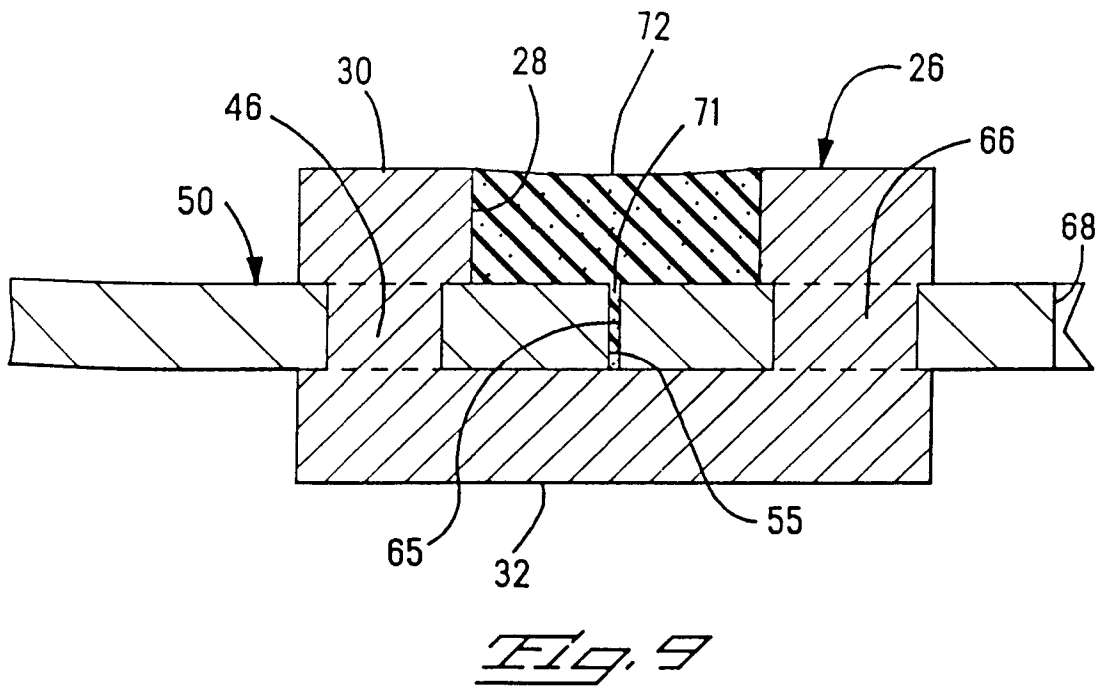
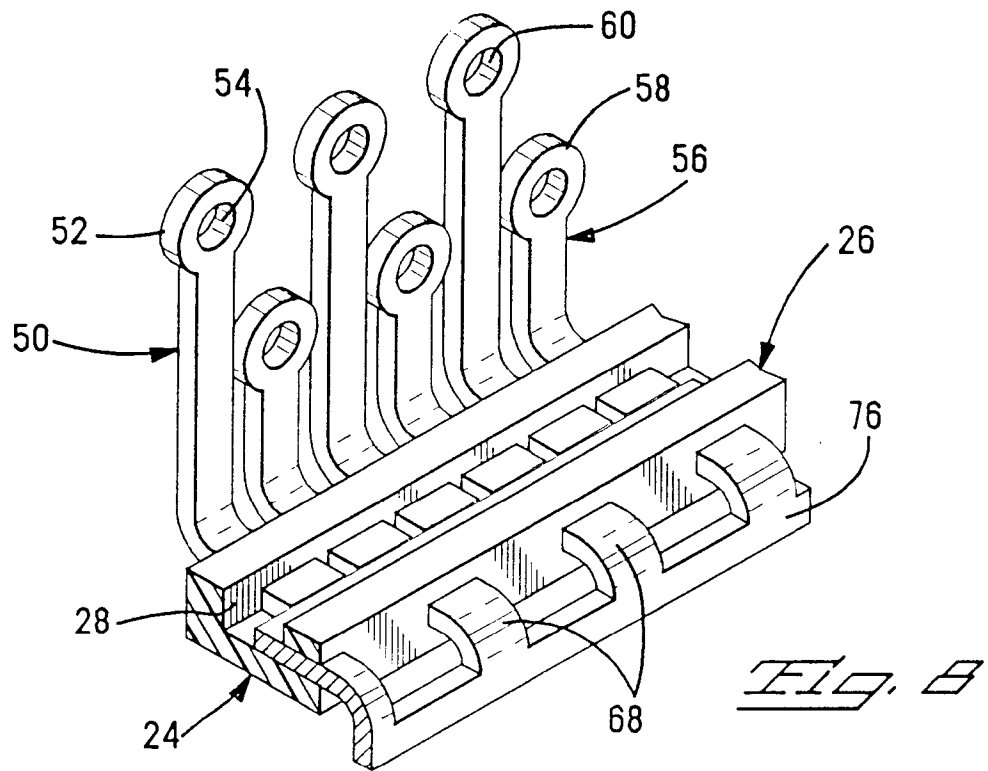
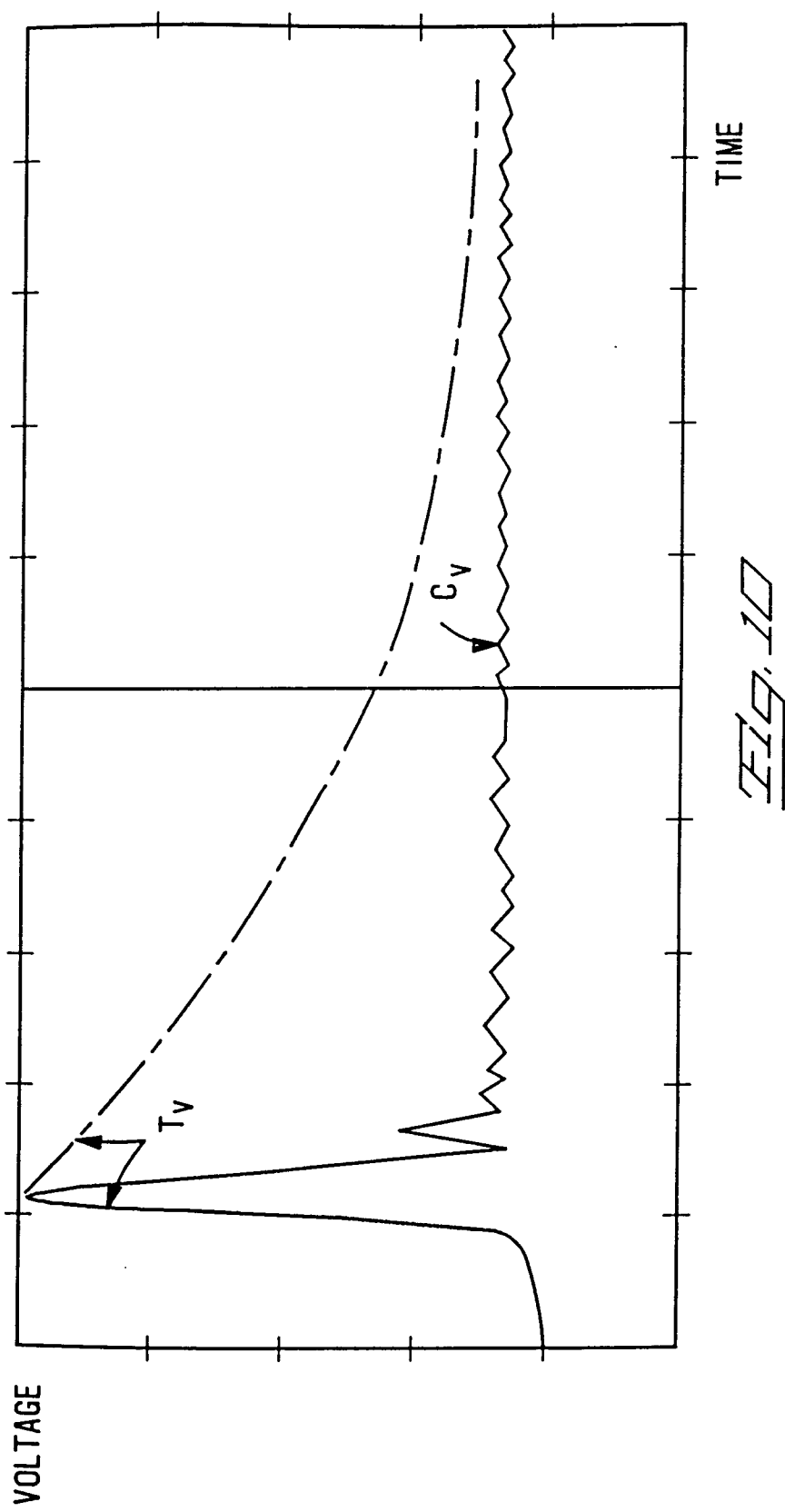


Fig. 7





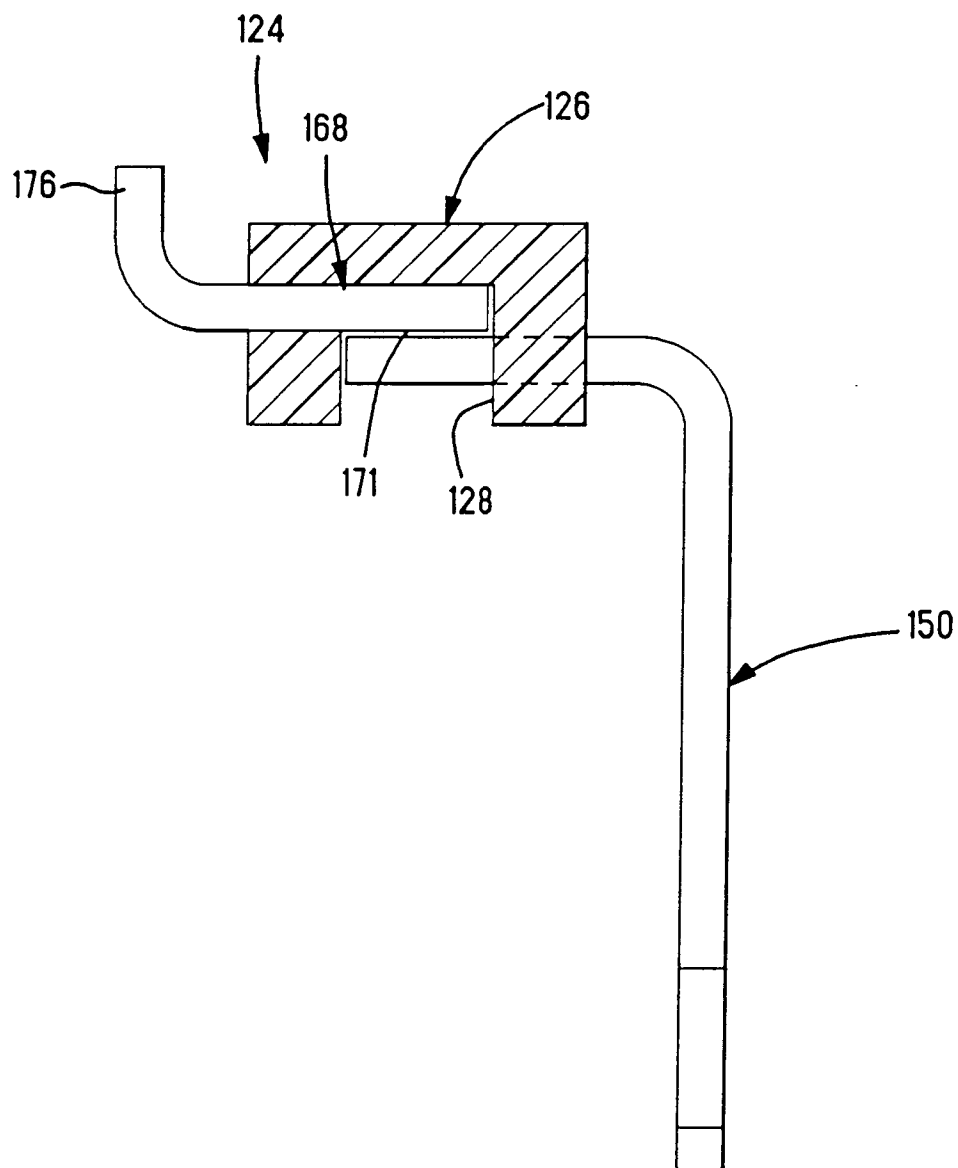


Fig. 11



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number

EP 93 30 4852

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
A	US-A-5 018 989 (BLACK ET AL.) * figures 2,5 *	1,4-5,8	H01R13/66
Y,D	US-A-4 977 357 (K.P.SHRIER) * column 3, paragraph 1 *	8	
A Y	EP-A-0 194 424 (BROWN BOVERI & CIE. AG) * claim 1; figure 2 *	1-2,5-6 8	
A,D	EP-A-0 211 508 (AMP INC.) * the whole document *	1,5,8	
A	DE-U-9 105 206 (DEHN + SÖHNE GMBH + CO KG) * page 2, last paragraph *	1	
P,A	US-A-5 148 354 (G.A.ROTH) * column 2, paragraph 1; figure 4 *	1,5	
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			H01R H01C
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 17 SEPTEMBER 1993	Examiner SIBILLA S.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

EPO FORM 1503 03.82 (P0401)