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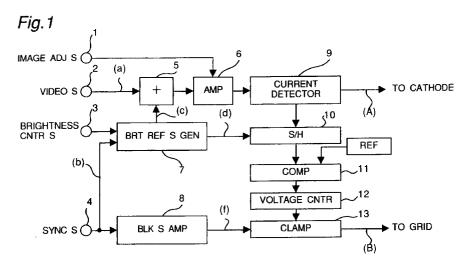
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## (54) Video signal processor

(57) A video signal processor for controlling a displayed image on a CRT having a cathode electrode and a grid electrode includes a generator (7) for generating a brightness reference signal, an adder (5) for adding the brightness reference signal to a black level in a blanking period of an input video signal. The added brightness reference signal is applied to the cathode electrode though a current detector (9) for detecting a

current of the added brightness reference signal. A current level signal produced from the current detector (9) is used for determining and clamping the peak of a control voltage applied to the grid electrode such that a voltage difference between a peak of the added brightness reference signal and the peak of the control voltage is maintained constant (VCF).





## **EUROPEAN SEARCH REPORT**

Application Number EP 93 11 0674

Category	Citation of document with indication of relevant passages	where appropriate,	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.5)
A	US-A-4 786 970 (MOORE LE November 1988 * column 2, line 21 - co		1-7	H04N5/59
A	EP-A-0 459 366 (MATSUSHI LTD) 4 December 1991 * column 4, line 11 - co		1-7	
				TECHNICAL FIELDS SEARCHED (Int.Cl.5)
				H04N
	The present search report has been draw	vn up for all claims		
	Place of search	Date of completion of the search		Excessioner
	BERLIN	11 September 199	6 Ma	terne, A
CATEGORY OF CITED DOCUMENTS  X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category		E : earlier patent do after the filing o D : document cited L : document cited	T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons	
A: tec	chnological background n-written disclosure			ily, corresponding