

(19)



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(11) Publication number:

0 600 160 A3

(12)

EUROPEAN PATENT APPLICATION(21) Application number: **93112936.5**(51) Int. Cl.⁵: **G11C 29/00**(22) Date of filing: **12.08.93**(30) Priority: **12.08.92 JP 236535/92**(43) Date of publication of application:
08.06.94 Bulletin 94/23(84) Designated Contracting States:
DE FR GB IT NL(88) Date of deferred publication of the search report:
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(54) **Semiconductor memory device with a test mode.**

(57) A semiconductor memory device characterized by the fact that the dynamic test time of the semiconductor memory device can be shortened, and the power consumption can be cut.

In the dynamic test for the semiconductor memory device in this invention, multiple word lines are selected at the same time with a prescribed interval corresponding to the element isolation layout. As the word lines are selected corresponding to the element isolating layout, the interference caused by the element isolation state can be excluded. Since multiple word lines are selected at the same time, the time of operation can be shortened. Since the word lines are maintained in the selected state while the sense amplifiers are not reset, there is no increase in the power consumption although multiple word lines are selected at the same time.

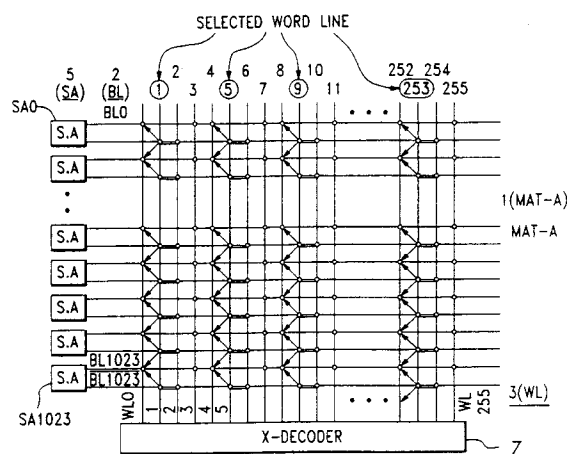


Fig. 1

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EUROPEAN SEARCH REPORT

Application Number
EP 93 11 2936

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.5)
Y	EP-A-0 101 107 (MOTOROLA INC.) * abstract; figures 3-5 * * page 3, line 1 - page 4, line 7 * * page 8, line 29 - page 10, line 5 * * claims 1-10 * ---	1	G11C29/00
Y	DE-A-25 16 973 (PHILIPS PATENTVERWALTUNG)	1	
A	* page 3, line 15 - page 4, line 25 * * page 6, line 24 - page 7, line 16 * ---	2	
A	EP-A-0 223 188 (FUJITSU LIMITED) * abstract; figure 4 * -----	1,2	
			TECHNICAL FIELDS SEARCHED (Int.Cl.5)
			G11C
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 5 August 1994	Examiner Sarasua Garcia, L
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			