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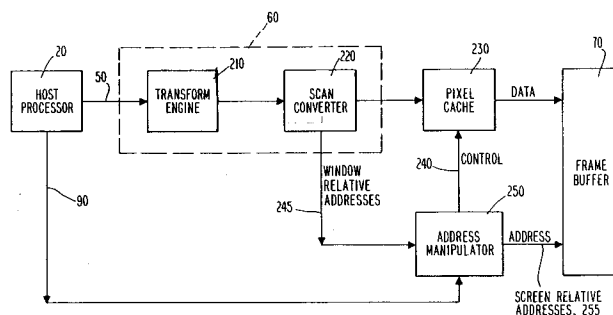
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(54) **Methods and apparatus for accelerating windows in graphics systems.**

(57) Graphics window systems which utilize graphics pipelines and graphics pipeline bypass buses. Hardware solutions for window relative rendering of graphics primitives, block moving of graphics primitives, transfer of large data blocks, and elimination of pipeline flushing are disclosed. The hardware implementations provided in accordance with the in-

vention are interfaced along the pipeline bypass bus, thereby eliminating gross overhead processor time for the graphics pipeline and reducing pipeline latency. Methods and apparatus provided in accordance with the invention exhibit significant pipeline efficiency and reductions in time to render graphics primitives to the screen system.

**Fig. 4****EP 0 617 400 A3**



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EUROPEAN SEARCH REPORT

Application Number
EP 94 20 1808

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.5)
A	US-A-4 814 884 (WILLIAM K. JOHNSON ET AL.) * abstract * ---	1	G09G5/14
A	EP-A-0 202 166 (OLDONNEL) * abstract * * column 4, line 30 - column 5, line 11 * -----	1,2	
			TECHNICAL FIELDS SEARCHED (Int.Cl.5)
			G09G
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 22 February 1995	Examiner Van Roost, L
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			