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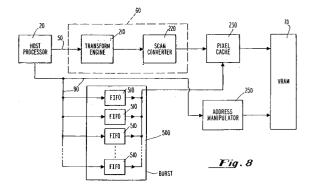
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Methods and apparatus for accelerating windows in graphics systems.

Graphics window systems which utilize graphics pipelines and graphics pipeline bypass buses. Hardware solutions for window relative rendering of graphics primitives, block moving of graphics primitives, transfer of large data blocks, and elimination of pipeline flushing are disclosed. The hardware implementations provided in accordance with the invention are interfaced along the pipeline bypass bus, thereby eliminating gross overhead processor time for the graphics pipeline and reducing pipeline latency. Methods and apparatus provided in accordance with the invention exhibit significant pipeline efficiency and reductions in time to render graphics primitives to the screen system.





EUROPEAN SEARCH REPORT

Application Number EP 94 20 1810

Category	Citation of document with ind of relevant pass		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.CL5)
A	EP-A-O 279 230 (INTE MACHINES CORPORATION * abstract; figure 8)	1	G09G5/14 G09G1/16
A	EP-A-O 284 905 (INTE MACHINES CORPORATION * abstract; figure 3)	1	
A	US-A-4 829 294 (HIDE * abstract; figures * column 4, line 21		1	
				TECHNICAL FIELDS SEARCHED (Int.Cl.5)
	The present search report has bee	n drawn up for all claims	-	
	Place of search	Date of completion of the search		Examiner
	THE HAGUE	24 February 199	5 Var	Roost, L
X : par Y : par doc	CATEGORY OF CITED DOCUMENT ticularly relevant if taken alone ticularly relevant if combined with anoth ument of the same category	S T: theory or princ E: earlier patent of after the filling er D: document cited L: document cited	iple underlying the locument, but publicate in the application for other reasons	e invention lished on, or
O : nor	nological background n-written disclosure rmediate document	& : member of the document		