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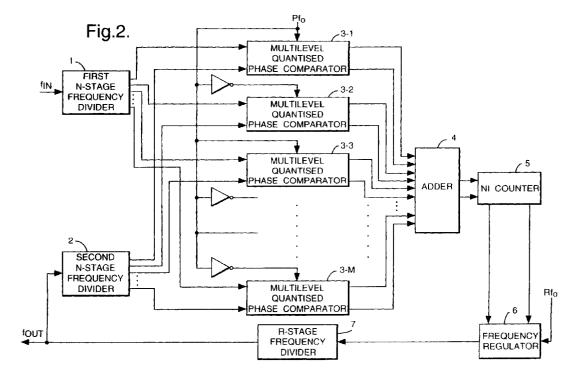
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(54) Digital phase-locked loop

(57) Using positive-phase or negative-phase clocks of phase count clock Pf₀, a number M of multilevel quantized phase comparators output as values quantized in multiple levels the phase differences of output signals outputted from first and second N-stage frequency dividers wherein input clocks and output clocks, respectively, of a digital PLL have been N-stage frequency divided and moreover, divided into M groups. An adder

adds this phase difference information and outputs advanced pulses or delayed pulses. An N1 counter counts up in response to advanced pulses and both outputs an increment pulse and undergoes setting to initial value N1 upon counting up to 2N1. In response to delayed pulses, the N1 counter counts down, and upon counting down to "0" both outputs a decrement pulse and undergoes setting to initial value N1.



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EUROPEAN SEARCH REPORT

Application Number EP 96 30 2922

Category	Citation of document with inc of relevant passaç		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Ci.6)
A	PHASE-LOCKED LOOP FO SYNCHRONIZATION SYST ELECTRONICS & COMMUN PART I - COMMUNICATI vol. 75, no. 4, 1 Ap US, pages 1-12, XP000307 * page 1, column 1,	SHINORI ROKUGO ET AL: "A DIGITAL ASE-LOCKED LOOP FOR STUFFING NCHRONIZATION SYSTEMS" ECTRONICS & COMMUNICATIONS IN JAPAN, RT I - COMMUNICATIONS., 1. 75, no. 4, 1 April 1992, NEW YORK , ges 1-12, XP000307927 page 1, column 1, line 1 - page 3, lumn 2, last line; figure 1 *		H03L7/191 H03L7/087
Α	EP 0 526 227 A (NIPPON ELECTRIC CO) * column 1, last line - column 2, line 7 * * column 5, line 19 - column 7, line 34; figures 7-11 *		1-4	
A	WITH DPLL FOR ASSOCI PROCEEDINGS OF THE I	NTERNATIONAL SYMPOSIUM EMS, NEW ORLEANS, MAY 990, INSTITUTE OF RONICS ENGINEERS, 00167001 *	1-4	TECHNICAL FIELDS SEARCHED (Int.Cl.6) H03L
	Place of search	Date of completion of the search		Examiner
THE HAGUE		17 February 1998	Balbinot, H	
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure		E : earlier patent doo after the filing dat r D : document cited in L : document cited fo	T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons	