### (12)

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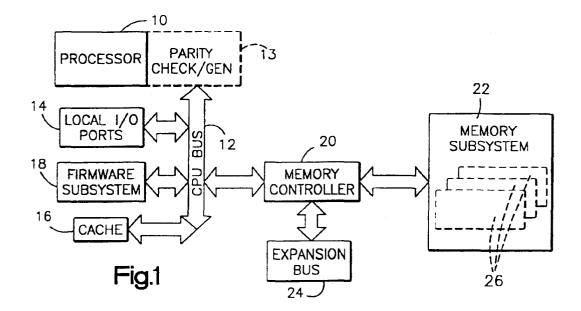
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## (54) High density SIMM or DIMM with RAS address re-mapping

(57) The signal configuration for addressable DRAMs from a system is changed from a signal that actuates a single bank of DRAMs having Y/X row/column addresses actuated by a single RAS, to a signal configuration that provides two RAS signals for two banks of

DRAMs having Y-1/X row/column addresses actuated by two RAS signals. This is done by converting the high order address bit from the system to a RAS signal actuated by a system RAS when, and only when, the high order bit is of a given value.





## **EUROPEAN SEARCH REPORT**

Application Number EP 97 30 0251

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EPO FORM 1503 03.82 (P04C01)

# ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

EP 97 30 0251

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

23-04-1999

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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82