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(71) Applicant: **NEC CORPORATION Tokyo (JP)** 

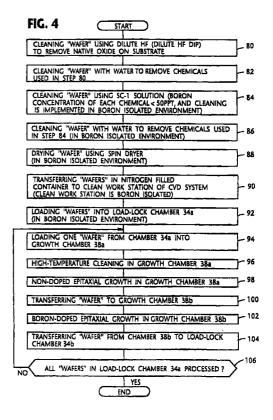
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#### (54) A method for boron contamination reduction in IC fabrication

(57) In order to reduce boron concentration between a silicon substrate and an Si or  $Si_{1-x}Ge_x$  layer which is epitaxially grown in a CVD (chemical vapor deposition) apparatus, the silicon substrate is pretreated, before being loaded into the CVD apparatus, such as to prevent the substrate from being contaminated by boron in a clean room. Further, in accordance with one embodiment, a CVD growth chamber itself is cleaned, before the substrate is loaded into the growth chamber, using an  $F_2$  gas at a predetermined temperature of the substrate, thereby to remove boron residues in the growth chamber;





## **EUROPEAN SEARCH REPORT**

**Application Number** EP 97 12 0374

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	The present search report has I	Date of completion of the search	<u> </u>	Examiner
	THE HAGUE	5 August 1999	K	öpf, C
X : par Y : par doc A : tec O : no	CATEGORY OF CITED DOCUMENTS  rticularly relevant if taken alone rticularly relevant if combined with anot cument of the same category chnological background n-written disclosure ermediate document	L : document cited	ocument, but p late I in the applicat I for other reaso	oublished on, or tion ons



# **EUROPEAN SEARCH REPORT**

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	THE HAGUE	5 August 1999	Köp <sup>.</sup>	f, C
CATEGORY OF CITED DOCUMENTS  T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document  T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons E: member of the same patent family, corresponding document				

EPO FORM 1503 03.82 (P04C01)

### ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

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