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(11) **EP 0 917 201 A3**

(12) **EUROPEAN PATENT APPLICATION**

(88) Date of publication A3:
13.09.2000 Bulletin 2000/37

(51) Int. Cl.⁷: **H01L 27/092, H03K 19/00**

(43) Date of publication A2:
19.05.1999 Bulletin 1999/20

(21) Application number: **98250397.1**

(22) Date of filing: **13.11.1998**

(84) Designated Contracting States:
**AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE**
Designated Extension States:
AL LT LV MK RO SI

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(30) Priority: **14.11.1997 JP 31398597**

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(54) **Complementary MOS semiconductor device**

(57) A complementary MOS semiconductor device comprising: a complementary MOS logic circuit having a plurality of field effect transistors; a first wiring and a second wiring as a source for supplying therethrough a power source voltage to the complementary MOS logic circuit; a first power supply circuit for controlling the supply of the power source voltage from said first wiring to said complementary MOS logic circuit; a second power supply circuit for controlling the supply of the power source voltage from said second wiring to said complementary MOS logic circuit; and a third power supply circuit for controlling the operation of said first power supply circuit, wherein said third power supply circuit includes field effect transistors each having a gate insulating film of 2.5 nm or more thickness.

FIG.1B

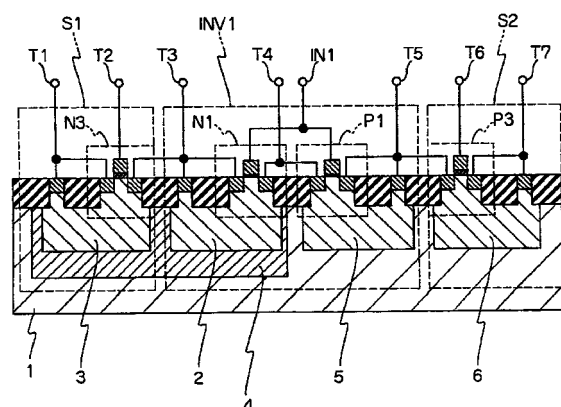
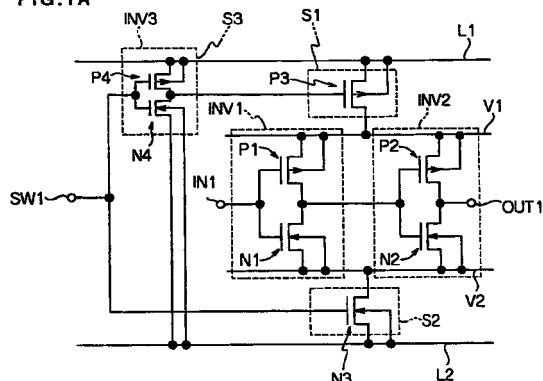


FIG.1A



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EPO FORM 1503 03.92 (P4/C01)



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**ANNEX TO THE EUROPEAN SEARCH REPORT
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