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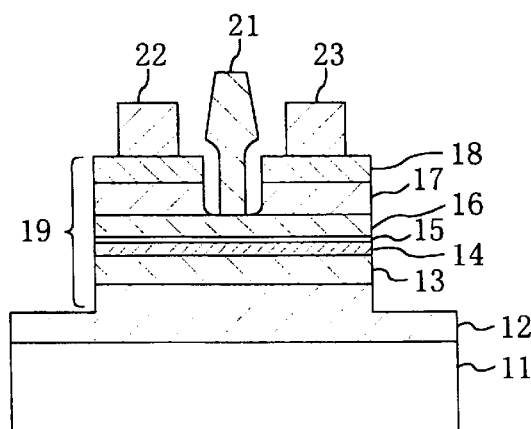
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(54) **High electron mobility transistor**

(57) An undoped $\text{In}_{0.52}\text{Al}_{0.48}\text{As}$ buffer layer (12) (thickness: 500nm), an undoped $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ channel layer (13) (thickness: 30nm), an n-type delta doped layer (15) for shortening the distance between the channel layer (13) and a gate electrode (21) and attaining a desired carrier density, an undoped $\text{In}_{0.52}\text{Al}_{0.48}\text{As}$ Schottky layer (16), and an n-type $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ cap layer (17) doped with Si (thickness: 50nm) are formed in this order on the principal surface of an Fe-doped InP semi-insulating substrate. An n-type GaAs protective layer (18) doped with Si (thickness: 7.5nm) is formed between the cap layer (17) and source/drain electrodes (22,23) for protecting the cap layer (17).

FIG. 4



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EUROPEAN SEARCH REPORT

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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
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The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 17 November 1999	Examiner Baillet, B
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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