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(54) **Circuit for an optical information reproduction device, optical information reproduction device and method of reproducing information**

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Description

BACKGROUND OF THE INVENTION

[0001] The present invention relates to an optical information reproduction device. More specifically, this invention relates to a device for optically reproducing information from a disk medium on which concentric or spiral tracks are disposed, the medium comprising identification information areas in which identification information representing address information and the like are shifted radially inward and radially outward each approximately by a predetermined distance with respect to a track center and user information areas in which user information and the like are recorded on the center of a track.

[0002] In recent years, it has been demanded that a memory means for storing information store image information, video information, and other voluminous information in comparison with the conventional textual information and sound information, and optical disks have been attracting attention as a response to the demand. Conventional recordable optical disks are provided with guide grooves formed at the time of the production of the disk. The guide grooves are used to keep the light beam for recording and reproduction in the center of a track. These guide grooves result in convex areas (lands) and concave areas (grooves) formed in spiral or concentric form on the disk. The disk using both the convex areas and concave areas as recording tracks (land tracks and groove tracks) can record twice as much information as a disk using either areas as recording tracks. This method is referred to as a land/groove recording method and is described in Japanese Patent Application Kokoku Publication No. 57859/1988.

[0003] A recording track is generally divided into sectors in the direction of the track, and in each of the sectors, identification information, such as a track number, and a sector number, is preformatted as physically formed convex and concave pits. The identification information is disposed in either of these methods: In one method, dedicated identification information for a land track or groove track is disposed; in another method, the information is disposed in a position shifted in a radial direction so that the information can be shared by a land track and a groove track adjacent to the land track, more specifically, along the boundary between a land track and a groove track.

[0004] The former method in which exclusive identification information is disposed in each track enables track-specific information to be included, making it easy to control the device. In the mastering of this type of disk, the pit width needs to be sufficiently narrower than the track pitch. It is therefore difficult to form desired pits with the same laser beam as that used for forming guide grooves. Thus, disk production process is complicated.

[0005] In the latter method of sharing identification information by a land track and a groove track adjacent to

the land track, the device needs to judge which track is being reproduced because two tracks share the identification information, and the control is a little more complicated than that for the former exclusive disposition method. However, the same laser beam as that used for forming guide grooves can be used for pre-formatting the identification information in mastering, which can be done by deflecting the laser beam in a radial direction just by 1/4 of the distance between the centers of the adjacent lands, by the use of a light deflection means. This type of optical disk and an optical information reproduction device using such optical disks are disclosed in Japanese Patent Application Kokai Publication No. 176404/1994.

[0006] An optical information reproduction device using an optical disk on which identification information is disposed in the latter method will next be described. Fig. 7 shows the track format of the conventional optical disk. Fig. 8 shows how the conventional identification information portion is disposed. Fig. 9 is a block diagram showing the configuration of the optical information reproduction device for reproducing information from that type of optical disk.

[0007] In Fig. 7 and Fig. 8, reference numeral 1 denotes an identification information area in which identification information is preformatted; reference numeral 2 denotes a user information area in which user information is recorded by means of a variation in the local optical constant or physical shape; reference numeral 3 denotes a groove track; and reference numeral 4 denotes a land track. As shown in the figures, the groove track 3 or land track 4 is disposed in spiral form in the full circumferential extent, and the tracks are divided into sectors in the direction of the tracks. A sector includes, at the beginning, the identification information area 1 in which information for identifying the sector, such as the track number and sector number is recorded, and the identification information area 1 is followed by the user information area 2 for recording user data and the like. The identification information is shared by the land track 4 and the groove track 3 adjacent to the land track, and the displacement of the identification information from the track center of the land track 4 or groove track 3 is 1/4 of the distance between adjacent land tracks 4 or of adjacent groove tracks 3.

[0008] The configuration of the conventional optical information reproduction device will next be described with reference to Fig. 9. In the figure, reference numeral 11 denotes an optical disk; reference numeral 12 denotes a spindle motor; reference numeral 13 denotes an optical head; reference numeral 14 denotes a first current-to-voltage (I/V) converting means; and reference numeral 15 denotes a second I/V converting means.

[0009] An adding means 16 adds the output of the first I/V converting means 14 and the output of the second I/V converting means 15.

[0010] A sum signal detecting means 17 detects the information recorded on the disk by processing and then converts the output (a) of the adding means 16 into binary

values.

[0011] A subtracting means 18 obtains a difference between the output of the first I/V converting means 14 and the output of the second I/V converting means 15.

[0012] A polarity reversing means 19 reverses the polarity of the output waveform (b) of the subtracting means 18 according to the control signal from a controller 26, which will be described later.

[0013] A difference signal detecting means 20 detects the information recorded on the disk by processing and then converts the output (d) of the polarity reversing means 19 into binary values.

[0014] A signal selecting means 21 selects the output (c) of the sum signal detecting means 17 or the output (e) of the difference signal detecting means 20 according to the control signal (f) from a control gate generation means 25, which will be described later.

[0015] A clock generation means 22 generates the reproduction clock (CK) in synchronization with the output (g) of the signal selecting means 21 according to a control gate signal (RG) from the control gate generation means 25.

[0016] A data demodulating means 23 judges whether the output (g) of the signal selecting means 21 is at level "1" or "0" at the timing of the reproduction clock from the reproduction clock generation means 22 and demodulates the data.

[0017] An address information reproduction means 24 reproduces an address after reproducing identification information by judging at the timing of the reproduction clock from the reproduction clock generation means 22 whether the output (g) of the signal selecting means 21 is at level "1" or "0".

[0018] The control gate generation means 25 generates the control gate signal mentioned earlier, with reference to the timing of the address reproduction completion signal from the address information reproduction means 24.

[0019] The controller 26 outputs a control signal to the polarity reversing means 19 according to the address information from the address information reproduction means 24.

[0020] The optical head 13 comprises a laser diode (LD) 131, a collimate lens 132, a beam splitter (BS) 133, a converging lens 134, and a photodetector (PD) 135.

[0021] The operation of a prior-art optical information reproduction device configured as described above will be described with reference to Fig. 9 and Fig. 10A to Fig. 10H. Fig. 10A to Fig. 10H show waveforms at various points in Fig. 9.

[0022] When the optical disk 11 is placed, by a mechanism, not shown, on the spindle motor 12, the controller 26 sends a spindle activation signal and speed information, which are not shown, to a rotation controlling means, which is not shown, to adjust the spindle motor 12 to a predetermined speed. Then, the laser diode 131 of the optical head 13 is turned on by a lighting command, which is not shown, from the controller 26. The output of the

laser diode 131 is kept to a constant value by a feedback controlling means, which is not shown.

[0023] The collimate lens 132 converts the laser beam emitted from the laser diode 131 into parallel rays of light, which pass the beam splitter 133 and are converged onto the optical disk 11 by the converging lens 134. After passing the converging lens 134, the far field pattern of the light reflected from the optical disk 11 (return light), which includes the information component on the optical disk 11, is reflected by the beam splitter 133 and projected onto the photodetector 135. The photodetector 135 has at least two light-receiving parts which are disposed on opposite sides (radially outward and radially inward sides) of a track tangential line in the projected far-field pattern, i.e., of a track tangential line in the far field of the information pits on the optical disk 11. In this connection, the optical system for passing the light beam from the laser diode 131 in the optical head 13 to the surface of the optical disk 11, and passing the light reflected at the surface of the optical disk 11 to the photodetector 135 is so designed that the center of the far field pattern of the information pits on the optical disk 11 is formed at the boundary between the two parts of the photodetector 135.

[0024] The optical system of the optical head 13 is adjusted so that the position of the beam spot in the radial direction can be controlled through the use of the light distribution information on the two-part split photodetector 135.

[0025] When the beam spot is in the center of a track, identical amounts of light strike the two parts of the split photodetector 135, and the difference between the two outputs, that is a push-pull signal, is zero. As the beam spot goes away from the center of the track, the distribution of light on the two-part split photodetector becomes unbalanced, increasing the value of the push-pull signal. The value of the push-pull signal is zero when the beam spot is in the center of the land track 4 or groove track 3, and the value is maximized or minimized when the beam spot is on the boundary between the land track 4 and groove track 3.

[0026] Tracking to keep the beam spot in the center of a track can be performed through feedback control to set the difference between the output signals from the two-part split photodetector 135 to zero. This technique is commonly utilized as a tracking method for optical disks having guide grooves.

[0027] The light reflected from the optical disk 11 (return light) including the information component on the optical disk 11, which is mentioned above, is converted into current signals by the photodetector 135, which are then converted into voltage signals by the first I/V converting means 14 and the second I/V converting means 15 in the subsequent stage. By obtaining the difference between the voltage signals at the subtracting means 18, a tracking error signal can be obtained in the push-pull method mentioned above. Using this signal, a tracking control means, which is not shown, performs control so

that the beam spot always scans the center of a track.

[0028] As for displacements in the direction of the optical axis of the light beam such as undulation of the surface of the disk, a focus controlling means, which is not shown, performs control to keep the beam spot converged onto the optical disk 11.

[0029] With the beam spot controlled by the controlling means mentioned above, the information on the optical disk 11 is read. Information is recorded onto the optical disk 11. The following description is limited to the reproduction of the information.

[0030] The voltage signals from the first I/V converting means 14 and the second I/V converting means 15 are added by the adding means 16 to form a waveform as shown as (a) in Fig. 10A. The output from the subtracting means 18 mentioned above has a waveform as shown as (b) in Fig. 10C or (b') in Fig. 10D when the beam spot is at the center of a track. Whether the waveform is like (b) or (b') depends on whether the position of pits in the identification information area 1 is shifted radially inward or radially outward with respect to the center of the land track 4. Since the beam spot is at the center of the track in the user information area, the outputs from the two parts of the split photodetector 135 are the same, and the output of the subtracting means 18 is 0 (or a reference level).

[0031] Suppose the light beam is scanning pits in the identification information area which are shifted radially inward with respect to the center of the land track. If the outputs from the two I/O converting means are connected to the subtracting means 18 in such a manner that the signal reproduced from the identification information area has the waveform as shown as (b) in Fig. 10C when the land track of the optical disk 11 is reproduced, the signal reproduced from the identification information area in the groove track reproduction has a waveform as shown as (b') in Fig. 10D. The reverse also holds.

[0032] When the information on the optical disk 11 is reproduced according to the output of the adding means 16, the analog signal output from the adding means 16 is judged to be larger or smaller than a predetermined slice level (V_{th1}) and is converted into binary values by the sum signal detecting means 17 and has a waveform as shown as (c) in Fig. 10B.

[0033] The reproduction of the identification information in the identification information area 1 on the optical disk 11 according to the output from the subtracting means 18 will next be described. This method is disclosed in Japanese Patent Application Kokai Publication No. 176404/1994.

[0034] The polarity of the reproduction waveform of the analog signal output from the subtracting means 18 differs depending on whether the reproduced track is a land track or a groove track, as described above. Generally, the level slicing means that produces binary values by slicing the analog waveform is designed with the polarity of input signal fixed. A system in which the polarity of the reproduced waveform changes therefore requires

the polarity reversing means 19 to provide a signal of a fixed polarity.

[0035] The polarity reversing means 19 can be easily controlled if the relationship between the track and the polarity of the reproduced waveform is known beforehand. That is, the controller 26, which will be described below, may operate the polarity reversing means 19 by judging whether the sector being scanned is on the land track or groove track. As a result, as shown as (d) in Fig. 10E, the polarity of the reproduced waveform of the identification information is maintained, irrespective of whether the sector being scanned is in the land track or groove track. In the shown example, the waveform is kept below the reference level. The output of the polarity reversing means 19 is converted into binary values by the difference signal detecting means 20, resulting in the waveform as shown as (e) shown in Fig. 10F.

[0036] The method of reproducing the identification information such as a track address and sector address and the user information such as user data from binary signals will next be described. It is incidentally noted that "user information" includes data used for phase-locked loop pulling-in, synchronization pattern, error-detection and correction codes, as well as "user data".

[0037] First, if the identification information and user information are reproduced through the use of the output (c) of the sum signal detecting means 17, the signal selecting means 21 is set to always select the output (c) of the sum signal detecting means 17.

[0038] If the output (e) of the difference signal detecting means 20 is used to reproduce the identification information and the output (c) of the sum signal detecting means 17 is used to reproduce the user information, the signal selecting means 21 accordingly switches to select the desired signal. The switching of the signal selecting means 21 is made according to the switching signal (f) (shown in Fig. 10G) from the control gate generation means 25, which will be described later. The switching signal is generated by an internal timer or the like, which is started by a timing signal which can identify a known position within a sector, such as the address detection completion timing signal from the address information reproduction means 24.

[0039] The output (g) of the signal selecting means 21 is supplied to the reproduction clock generation means 22, the data demodulating means 23, and the address information reproduction means 24.

[0040] The reproduction clock generation means 22 generates the reproduction clock in synchronization with the output signal (g) from the signal selecting means 21, using a phase locked loop (PLL) means. The reproduction clock generation means 22 is designed to operate within the area in which the information to be reproduced is present, according to a read gate signal (RG) from the control gate generation means 25.

[0041] The address information reproduction means 24 reproduces the identification information by checking at the timing of the reproduction clock from the reproduc-

tion clock generation means 22 whether the output signal (g) from the signal selecting means 21 is at "1" or "0", and thereby detects an address.

[0042] The data demodulating means 23 demodulates data by checking at the timing of the reproduction clock from the reproduction clock generation means 22 whether the output signal (g) from the signal selecting means 21 is at "1" or "0" and then performs decoding, error correction, and deinterleaving.

[0043] The control gate generation means 25 generates the switching signal (f) (shown in Fig. 3I) of the signal selecting means 21 and the read gate signal (RG) (shown in Fig. 3J, to be supplied to the reproduction clock generation means 22) according to the internal timer, started at the address detection completion timing (shown in Fig. 3H) provided by the address information reproduction means 24. The address detection completion timing is a timing when the decoding of the address having been read from the identification information area is completed. In the drawing, it is shown to be immediately after the end of the identification information area.

[0044] The controller 26 identifies the sector from which information is being reproduced according to the address reproduced by the address information reproduction means 24 and the information indicating whether information is being reproduced from a land track or a groove track, which is obtained from the tracking control means. If the track from which information is to be reproduced is changed at an access or on other occasions, the controller judges whether the track containing the target sector is the land track or groove track and outputs a control signal for setting the polarity of the output signal to the polarity reversing means 19. In addition, the controller 26 controls the whole device, which is not shown.

[0045] As described above, because the identification information is disposed between a land track and a groove track, being shifted from a track center, the identification information can be reproduced and detected regardless of whether the beam spot is scanning a land track and a groove track. Because the identification information need not be separately formed for the land track and for the groove track, the number of processes in optical disk mastering can be reduced.

[0046] However, because identification information is shared by a land track and a groove track, as described above, it is hard to identify a sector just by reproducing the address, and the information indicating whether information is being reproduced from a land track or a groove track is additionally required.

[0047] Because the identification information is shifted from a track center, any track offset of the beam spot in a radial direction away from the identification information causes the amplitude of the signal reproduced from the identification information to be degraded, reducing the address detection accuracy. A low address detection accuracy will result in a low recording reliability or a low reproduction reliability.

[0048] In addition, it is hard to identify whether the track

from which information is being reproduced is a land track or a groove track just by detecting the identification information.

[0049] Further, because the identification information and user information are separately detected, the scale of the circuit is large.

[0050] To solve these problems, an optical disk with the identification information disposed in a staggering manner as shown in Fig. 11 is provided. A feature of this disposition is that identification information is split into two areas and that the information of one area (hereafter referred to as the first identification information) is shifted by a predetermined distance radially outward with respect to the track center, and the information of the other area (hereafter referred to as the second identification information) is shifted by a predetermined distance radially inward with respect to the track center and radially inward with respect to the center of the track. The amount of shift is preferably about 1/2 of the track pitch.

[0051] If this identification information is used for tracking control, the tracking offset can be canceled. That is, when the first identification information and second identification information reproduced by the subtracting means mentioned above have the same reproduction amplitude, the beam spot is judged to be at the center of the track.

[0052] Based on the order of detection in which the first identification information area and the time at which the second identification information area are detected, the track on which the beam spot is scanning can be known. That is, when the beam spot is scanning a land track, the first identification information (shifted radially outward) is detected, and then the second identification information (shifted radially inward) is detected. When the beam spot is scanning a groove track, the second identification information (shifted radially inward) is detected, and then the first identification information (shifted radially outward) is detected. Through the use of this relationship, whether the track from which the information is being reproduced is in a land track or in a groove track can be judged, and the sector can be identified just by reproducing the identification information.

[0053] The conventional optical information reproduction device as described above with reference to Fig. 8, however, is designed on the assumption that the identification information is shifted in only one direction with respect to the track center, and the polarity reversing means can be controlled just in units of tracks, that is, the polarity is not switched until the end of each land track or each groove track. With this configuration, it is impossible to control the signal polarity for each piece of identification information. Accordingly, if the conventional optical information reproduction device is used to reproduce information from an optical disk as shown in Fig. 11, just the output of the adding means can be used to detect the identification information, and the push-pull signal output from the subtracting means cannot be used. In this case, the first identification information and second

identification information cannot be separately detected, and whether the track from which information is reproduced is a land track or a groove track cannot be judged solely from the detected identification information.

[0054] Moreover, a judging means, such as one which makes judgment by evaluating the accuracy or reliability of address detection, for deciding which of the outputs from the adding means and subtracting means is to be used for reproduction of the identification information is not available, and the address detection accuracy may therefore be degraded.

[0055] EP-A-0 801.382 relates to an optical information recording and reproducing device and associated method adapted to record or reproduce information on a recording medium having an optical detector including two light receiving portions connected to means for generating a difference signal and means for generating an addition signal between the two portions and which includes means for differentiating between a header portion and a data portion by detecting an envelope of the difference signal.

SUMMARY OF THE INVENTION

[0056] The invention is set out in the accompanying claims.

[0057] The present invention relates to an optical information reproduction device for reproducing information from an optical disk with identification information disposed in a staggering manner, and seeks to solve the problems of the conventional device as described above.

[0058] A first object of the invention is to provide an optical information reproduction device for reproducing information from an optical disk having a first identification information area and a second identification information area in which identification information representing address information and the like are disposed, being shifted respectively radially outward and radially inward by a predetermined distance with respect to the track center and a user information area in which user information and the like are recorded on the center of a track.

[0059] It is further desirable to enable reproduction of both the first identification information and the second identification information to be effected by a common means, by utilizing the information indicating the position of appearance of the first identification information and the second identification information, and by making the first identification information and second identification information similar in reproduced waveform pattern in the stage of analog waveform.

[0060] It is further desirable to enable reproduction of information after binarizing the analog waveform, with regard to the identification information and the user information to be effected by a common means, by utilizing the information indicating the position of appearance of the first identification information and second identification information and the information indicating the position of appearance of the user information, and by making

the first and second identification information and user information similar in reproduced waveform pattern in the stage of analog waveform.

[0061] It is further desirable to provide a means for enabling a high-speed return even if the reproduced waveform is disturbed by a defect or flaw on the medium.

[0062] It is further desirable to improve the probability of signal detection by keeping an optimum slice level for binarization of an analog waveform and by applying an offset.

[0063] It is further desirable to improve the probability of signal detection by separately setting offset values to be applied to the slice level for the first and second identification information areas and the user area.

[0064] It is further desirable to optimize the slice level at a high speed by reducing the time constant for slice level control immediately after the beginning of the first and second identification information and user information and immediately after a defect on the medium.

[0065] It is further desirable to improve the probability of signal detection by separately setting the period for which the control time constant of the direct-current component rejection means is reduced and the period for which the time constant of slice level control is reduced.

[0066] It is further desirable to improve the probability of signal detection at a variety of reproduction speeds by adjusting the period for which the control time constant of the direct-current component rejection means is reduced and the period for which the time constant of slice level control is reduced, according to the reproduction speed.

[0067] It is further desirable to improve the probability of signal detection at a variety of reproduction speeds by adjusting the control time constant of the direct-current component rejection means and the time constant of slice level control, according to the reproduction speed.

[0068] An eleventh object of the invention is to improve the probability of signal detection by separately setting offset value for the first and second identification information and user information of the land track and of the groove track on a disk medium.

[0069] An optical information reproduction device according to a preferred embodiment of the present invention is for reproducing information from an optical disk medium on which an identification information area including first identification information shifted radially outward with respect to the center of a recording track by a specified distance and second identification information shifted radially inward with respect to the center of a recording track by a specified distance, and a user information area disposed along the center of a recording track are disposed in concentric or spiral form, said optical information reproduction device comprising:

an optical head (13) illuminating the optical disk medium (11) with a light beam, and including a split photodetector (135) having at least two light-receiving parts disposed on opposite side of a track tan-

gential line in the far field of the optical disk medium and receiving light reflected from the optical disk medium,
 an adding means (16) for adding the outputs of said optical head (13);
 a subtracting means (18) which obtains a difference between the outputs of said optical head (13);
 a difference signal processing means (35) which makes the output of said subtracting means (18) similar to the output signal of said adding means (16) in waveform pattern;
 a signal selecting means (36) which selectively outputs the output of said adding means (16) and the output of said difference signal processing means (35); and
 a waveform shaping means (37) which detects the information recorded on said disk medium from the output signal of said signal selecting means (36);
 said signal selecting means (36) being set to select the output of said difference signal processing means (35) or the output of said adding means (36) when said identification information area is being scanned and to select the output of said adding means (16) when said user information area is being scanned.

[0070] A signal processing circuit according to a preferred embodiment of the invention is for used in an optical information reproduction device for reproducing information from an optical disk medium on which an identification information area including first identification information shifted radially outward with respect to the center of a recording track by a specified distance and second identification information shifted radially inward with respect to the center of a recording track by a specified distance, and a user information area disposed along the center of a recording track are disposed in concentric or spiral form, and including an optical head (13) illuminating the optical disk medium with a light beam, and including a split photodetector (135) having at least two light-receiving parts disposed on opposite side of a track tangential line in the far field of the optical disk medium and receiving light reflected from the optical disk medium, and comprises
 an adding means (16) for adding the outputs of said split photodetector (135);
 a subtracting means (18) which obtains a difference between the outputs of said split photodetector (135);
 a difference signal processing means (35) which makes the output of said subtracting means (18) similar to the output signal of said adding means (16) in waveform pattern;
 a signal selecting means (36) which selectively outputs the output of said adding means (16) and the output of said difference signal processing means (35); and
 a waveform shaping means (37) which detects the information recorded on said disk medium from the output signal of said signal selecting means (36);

said signal selecting means (36) being set to select the output of said difference signal processing means (35) or the output of said adding means (36) when said identification information area is being scanned and to select the output of said adding means (16) when said user information area is being scanned.

[0071] The difference signal processing means (35) may comprise:

a polarity inverting means (352) for inverting the polarity of the output of the subtracting means (18);
 a selecting means (354) for selectively outputting the output of the subtracting means (18) and the output of the polarity inverting means (352); and
 a polarity switching signal generation means (353) for generating a switching timing for the selecting means (354).

[0072] The polarity switching signal generation means (353) may comprise:

a first gate generating means (281) for generating a first gate signal indicating the position at which the first identification information appears;
 a second gate generating means (282) for generating a second gate signal indicating the position at which the second identification information appears; and
 a polarity judging means (355) for judging the polarity of the waveform of the output of the subtracting means (18).

[0073] The waveform shaping means (37) may comprise:

a direct-current component rejection means (374) for removing the direct-current component of the output of the signal selecting means (36) with a predetermined time constant;
 a third gate generation means (371) for generating a gate signal (o) indicating the position at which the user information appear;
 a fourth gate generation means (372) for generating a gate signal (p) indicating the position of a defect;
 a boost gate generation means (373) for generating a first boost gate signal (q) for changing the time constant of the direct-current component rejecting means (374) in accordance with the outputs of said first gate generation means (281), said second gate generation means (291), said third gate generation means (371), and said fourth gate generation means (373); and
 a signal slice means (375) for binarizing the output of the direct-current component rejection means (374);
 said direct-current component rejection means (374) reducing said time constant for a predetermined period after the beginning of the first identification in-

formation, the beginning of the second identification information, the beginning of the user information, and the end of the defect.

[0074] The signal slice means (375) may comprise:

a first binarization means (376) for binarizing the output of the direct-current component rejection means (374) at a slice level;
a slice level control means (377) for controlling the slice level to an optimum value with a predetermined time constant based on the output of said first binarization means (376);
a slice level adjustment means (378) for applying an offset to the slice level set by said slice level control means (377); and
a second binarization means (379) for binarizing the output of the direct-current component rejection means (374) at the output of said slice level adjustment means (377).

[0075] The slice level adjusting means (378) may be configured so that it can set the offset values separately for the first identification information, the second information, and the user information.

[0076] The slice level adjustment means (378) may be configured to generate a second boost gate signal of a predetermined duration generated in accordance with the outputs of the first to fourth gate generation means, and reduces the time constant for control of the slice level, for the period of said second boost gate signal.

[0077] The boost gate generation means (373) and the slice level adjustment means (378) may be configured so that they can set the period of the boost gate signal separately from each other.

[0078] The boost gate generation means (373) and the slice level adjustment means (378) may be configured so that they can vary the period of the boost gate signal according to the reproduction speed.

[0079] The direct-current component rejection means, and said slice level control means may be configured so that they can vary the time constant for control according to the reproduction speed.

[0080] The recording tracks on the optical disk may comprise groove tracks in concentric or spiral form on the optical disk medium, and land tracks formed between groove tracks, and said slice level adjustment means (378) may be configured so that it can set the offset separately for the first identification information, the identification offset information, and the user information of the groove tracks and land tracks.

BRIEF DESCRIPTION OF THE DRAWINGS

[0081] In the accompanying drawings:-

Fig. 1 is a block diagram of the optical information reproduction device of the first embodiment of the

present invention;

Fig. 2A to Fig. 2K show the waveforms of outputs from different blocks forming the optical information reproduction device of the first embodiment of the present invention;

Fig. 3A to Fig. 3J show the waveform of output from the waveform shaping means forming the optical information reproduction device of the first embodiment of the present invention;

Fig. 4 shows the polarity switching signal generation means forming the optical information reproduction device of the first embodiment of the present invention;

Fig. 5 shows the waveform shaping means forming the optical information reproduction device of the first embodiment of the present invention;

Fig. 6A to Fig. 6C show the waveforms of outputs from different blocks of the waveform shaping means when there is a flaw in the medium on the optical information reproduction device of the first embodiment of the present invention;

Fig. 7 shows the track format of the conventional optical disk;

Fig. 8 shows an example of disposition of the conventional identification information portion;

Fig. 9 shows a block diagram of the conventional optical information reproduction device;

Fig. 10A to Fig. 10H shows the waveforms of outputs from different blocks forming the conventional optical information reproduction device;

Fig. 11 shows another example of disposition of the conventional identification information portion;

Fig. 12 shows the details of the boost gate generation means;

Fig. 13 shows details of each of the one-shot pulse generation means; and

Fig. 14A to Fig. 14E shows the waveforms of the signals at various parts in the circuit of Fig. 13.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0082] Preferred embodiments of the present invention will next be described in more detail with reference to figures showing embodiments. In the description of the embodiments, blocks having the same reference numerals as those used in the description of the prior art is basically the same as the corresponding blocks of the prior-art optical information reproduction device shown in Fig. 9, and the detailed description of such blocks will be omitted.

Embodiment 1

[0083] Fig. 1 shows the optical information reproduction device of the first embodiment of the present invention. In the figure, reference numeral 11 denotes an optical disk; reference numeral 12 denotes a spindle motor;

reference numeral 13 denotes an optical head; reference numeral 14 denotes a first I/V converting means; reference numeral 15 denotes a second I/V converting means; reference numeral 16 denotes an adding means; reference numeral 18 denotes a subtracting means; reference numeral 35 denotes a difference signal processing means; reference numeral 36 denotes a signal switching means; reference numeral 37 denotes a waveform shaping means; reference numeral 22 denotes a reproduction clock generation means; reference numeral 23 denotes a data demodulating means; reference numeral 24 denotes an address information reproduction means; and reference numeral 25 denotes a control gate generation means that generates a control gate signal with reference to the timing of the address reproduction completion signal from the address information reproduction means 24.

[0084] The difference signal processing means 35 comprises a buffer amplifier 351, reversing buffer amplifier 352, polarity switching signal generation means 353, and switching means 354.

[0085] The operation of the optical information reproduction device of the embodiment as described above for reproducing information from the optical disk shown in Fig. 11 will be described with reference to Fig. 1, Fig. 2A to Fig. 2K, and Fig. 3A to Fig. 3G. Fig. 2A to Fig. 2K and Fig. 3A to Fig. 3G show the waveforms at various points in Fig. 1.

[0086] It is assumed that the identification information is disposed in a staggering manner as shown in Fig. 11.

[0087] The output from the subtracting means 18 has a waveform as shown as (b) in Fig. 2B or (b') in Fig. 2C while the beam spot is scanning the center of a track. (b) and (b') indicate that the polarity of the output waveform from the subtracting means 18 depends on whether the track scanned by the beam spot is a land track or groove track. That is, the polarity of the output waveform depends on the direction of displacement of the identification information with respect to the position of the beam spot. When the beam spot passes the identification information area while scanning the center of a track, a "track offset with respect to the identification information" is a maximum (this is because the identification information is shifted relative to the center of the track by half a track pitch). In this case, the difference between the outputs of the two-part split photodetector 135 is maximum, and the output (b) of the subtracting means 18, that is a push-pull signal, is maximum or minimum. Consequently, the signal reproduced from the first identification information shifted radially outward with respect to the track center and the signal reproduced from the second identification information shifted radially inward form a waveform with opposite polarities with respect to the zero level (or reference level).

[0088] In the user information area, the beam spot scans the center of a track, and the outputs of the two-part split photodetector 135 are the same, and the output (b) from subtracting means 18 is zero (or reference level).

[0089] As shown in Fig. 4, an example of the polarity switching signal generation means 353 comprises a first gate generation means 281, a second gate generation means 291, and a polarity judging means 355. Moreover, the polarity judging means 355 comprises an R-S flip-flop means 356 and an AND means 357.

[0090] The polarity switching signal generation means 353 generates a discrimination gate signal (h) shown in Fig. 2H used to discriminate the first identification information from the output signal (b) of the subtracting means 18. According to the discrimination gate signal, the switching means 354 selects and outputs either the output (i) (shown in Fig. 2D) of the buffer amplifier 351 or the output (j) (shown in Fig. 2E) of the reversing buffer amplifier 352 to the signal switching means 36. This makes the waveform (k) (shown in Fig. 2I) of the output signal of the difference signal processing means 35 similar to the output waveform (a) (shown in Fig. 2A) of the adding means 16, in regard to the polarity.

[0091] The signal switching means 36 selects the output (a) of the adding means 16 and the output (k) of the difference signal processing means 35 according to the switching signal (1) (shown in Fig. 2J) from the control gate generation means 25, and supplies the selected outputs to the waveform shaping means 37. The switching signal can be generated by an internal timer, started by a timing of which position of appearance can be identified. An example of such a timing is the address detection completion timing signal from the address information reproduction means 24. If the reproduced identification information is detected from the output of the adding means 16 in the same way as the reproduced user information is detected, the control gate generation means 25 may be set to always select the output (a) of the adding means 16 for the signal switching means 36.

[0092] As shown in Fig. 5, the waveform shaping means 37 comprises a third gate generation means 371 which detects the user information area, a fourth gate generation means 372 which detects the position of a flaw on the medium, a boost gate generation means 373 which generates a first boost gate signal for changing the time constant of the direct-current component rejection, a direct-current component rejection means 374, and a signal slicing means 375. The signal slicing means 375 comprises a first binarization means 376, a slice level control means 377, a slice level adjustment means 378, and a second binarization means 379.

[0093] In the waveform shaping means 37, the third gate generation means 371 detects the user information area from the signal (m) (shown in Fig. 2K, and in Fig. 3A) input by the signal switching means 36, and the fourth gate generation means detects the position of a flaw on the medium. The boost gate generation means 373 generates a first boost gate signal (q) (shown in Fig. 3E) having a predetermined period at the beginning of the first and second identification information in the identification information area and the beginning and end of the user information area, by means of a one-shot means.

The direct-current component rejection means 374 decreases the time constant for the period of the first boost gate signal input from the boost gate generation means 373 and reduces the direct-current component from the output signal (m) of the signal switching means 36 at a high speed, thereby keeping substantially constant direct-current components in the output (a) from the adding means 16 and the output (k) from the difference signal processing means 35. In addition, processing is performed to keep the amplitude of the output signal of the direct-current component rejection means 374 to a constant value, degradation in waveform resulting from the frequency response of the optical system is separately improved (reduced) in the identification information area and in the user information area by an equalization means, which is not shown. The improvement for the identification information area and the improvement for the user information area are made separately. Then, binarization is performed by the signal slicing means 375, and the waveform as shown as (n) shown in Fig. 3G is output.

[0094] The signal slicing means 375 optimizes the slice level to minimize the reproduction error. The signal (r) (shown in Fig. 3F) of which direct-current component has been removed by the direct-current component rejection means 374 is first converted into binary values by the first binarization means 376 at a specified slice level. Then, the slice level control means 377 adjusts the slice level to set the average duty ratio of the binary pulse to a specified value, and the slice level adjustment means 378 generates a second boost gate signal, so that the slice level is optimized at a high speed in the boost period.

[0095] The slice level adjustment means 378 may apply an offset to the slice level mentioned above. This offset may be set to such a value that the reproduction jitter and the reproduction error rate are minimized. Further, the value may be separately set for the first identification information area, second identification information area, and user information area, or the value may be separately set, depending on whether the beam spot is scanning a land track or groove track. Moreover, the offset value may be adjusted according to the reproduction speed. Furthermore, the durations of the first and second boost gate signals may be set independently or may be varied according to the reproduction speed, error rate, or jitter.

[0096] Moreover, the time constants of direct-current component rejection and slice level control may vary according to the reproduction speed.

[0097] The operation for reproducing address information and user information from the output of the waveform shaping means 37 is the same as that in the prior art and will not be described here.

[0098] Fig. 12 shows the details of the boost gate generation means 373. As illustrated, it includes first to fourth one-shot pulse generation means 401 to 404 respectively receiving the signals (d), (e), (o) and (p), and an OR gate 405 receiving the outputs of the one-shot pulse generation means 401 to 404. Each of the one-shot pulse gen-

eration means produces a pulse which rises at the rising edge of the input, and has a certain duration.

[0099] Fig. 13 shows details of each of the one-shot pulse generation means 401 to 404. Fig. 14A to Fig. 14E shows the waveforms of the signals at various parts in the circuit of Fig. 13.

[0100] As illustrated in Fig. 13, the one-shot pulse generation means comprises a flip-flop 411 having its CK input connected to the input terminal IN and having its Q output connected to the output terminal OUT of the one-shot pulse generation means, a transistor 402 having its base connected to the /Q (inversion of Q) terminal of the flip-flop 411, a constant-current source 413 having its one end connected to the emitter of the transistor 412, a capacitor 414 connected across the collector and the emitter of the transistor 412, and a comparator 415 having a first input terminal connected to the emitter of the transistor 412, and a second input terminal connected to a reference voltage means 416.

[0101] When the input (Fig. 14A) supplied via the terminal IN goes from "Low" to "High", the Q output (Fig. 14B) of the flip-flop 411 goes from "Low" to "High", while the /Q output (Fig. 14C) goes from "High" to "Low". The capacitor 414 is discharged with a time constant dependent on the capacitance C of the capacitor 414 and the current I of the constant-current source 413. Because of the discharge, the potential (Fig. 14D) at first input of the comparator 415 is lowered. The comparator 415 compares this potential with a reference level Vref supplied from the reference voltage means 416. If the potential at the first input becomes lower than the reference level Vref, the output (Fig. 14E) of the comparator 415 will be "Low". The output of the comparator 415 is connected to the reset terminal R of the flip-flop 411. When "Low" signal is supplied to the reset terminal R of the flip-flop 411, the Q output of the flip-flop 411 goes "Low", while the /Q output of the flip-flop 411 goes "High". When the /Q output of the flip-flop 411 goes "High", the transistor 412 is turned on, and the capacitor 414 is charged with a time constant dependent on the capacitance of the capacitor 414 and the driving capacity of the transistor. The potential on the first input of the comparator 415 rises, and when it exceeds the reference level Vref, the output of the comparator 415 goes from "Low" to "High". The upper limit of the input of the comparator 415 is the "High" level of the /Q output of the flip-flop 411 minus the base-to-emitter voltage V_{BE} of the transistor 412.

[0102] The output of the flip-flop 411 is a pulse which rises at the rising edge of the input signal to the input terminal IN and has a duration determined by the capacitance C of the capacitor 414 and the current I of the constant-current source 413, and the reference level Vref.

[0103] For changing the duration of the pulse, the value of the current I of the constant-current source 413 is varied.

[0104] When the relationship between the condition for changing the duration of the pulse (reproduction speed,

or the like) and the optimum duration of the pulse is not clear, an arrangement is adopted wherein the the current I of the constant-current source 413 is set by a digital-to-analog converter (DAC), and the DAC is set digitally by a controller. With such an arrangement, the freedom in varying the duration is expanded.

[0105] When the relationship between the condition for changing the duration of the pulse and the optimum duration of the pulse is known to be linear or logarithmic, the information for changing the duration of the pulse is provided in the form of an electric current, and the current I of the constant-current source 413 is set in an analog manner by the use of the current signal.

[0106] The operation for a medium with a defect or flaw will next be described with reference to Fig. 6A to Fig. 6C. As shown in Fig. 6A to Fig. 6C, if the medium has a defect, the amplitude of the signal reproduced from the defective portion is reduced, varying the direct-current component of the signal and the slice level. Accordingly, the direct-current component and slice level need to be stabilized at a high speed immediately after the defective portion. Therefore, a fourth gate generation means 372 is provided so that the defective portion can be detected and the control time constant is reduced for a specified period immediately after the defect, thereby enabling high-speed stabilization.

[0107] A defect increasing the signal amplitude can be processed in the same way. That is, if a defect or flaw causes such a large variation in the direct-current component that cannot be followed with the time constant in the steady state, the time constant is reduced after the end of such an area so that the direct-current component and slice level are stabilized at a high speed.

[0108] The fourth gate generation means 372 detects the defective portion from the signal (m) (shown in Fig. 6A) input by the signal switching means 36 and outputs a flaw detection signal, which is not shown. The boost gate generation means 373 opens the boost gate for a specified period after the end of the defective portion according to the input flaw detection signal (p) (shown in Fig. 6C) and reduces the direct-current component at a high speed.

[0109] Likewise, the slice level adjustment means 378 opens the boost gate for a specified period after the end of the defective portion according to the input flaw detection signal, which is not shown, and adjusts the slice level at a high speed.

[0110] In the embodiment described above, the signal switching means 36 keeps selecting the output of the difference signal processing means 35 while the identification information area is being scanned. In an alternative arrangement, the signal switching means 36 selects the output of the difference signal processing mean 35 or the output of the adding means 16 according to the type of the disk, the probability of identification information detection, and the jitter in the reproduced identification information, so as to obtain a better result (lower error rate). "To select the output of the difference signal

processing means 35" as recited in the appended claims should be construed to cover an arrangement where the selection of the output of the difference signal processing means 35 is continued throughout the period when the identification information area is being scanned, and an arrangement where the output of the difference signal processing means 35 and the output of the adding means 16 are selectively output according various conditions.

[0111] In the embodiment described, the two-part split photodetector 135 is used. Alternatively, a photodetector, such as four-part split photodetector, having more than two light-receiving parts may be used.

[0112] With the configuration as described above, the present invention produces the following effects.

[0113] When reproducing the identification information from the first and second identification information areas in which the identification information is disposed, being shifted by a specified distance radially outward and radially inward with respect to the track center, processing is performed by which the waveform pattern of the difference signal is made to be similar in regard to polarity to the waveform pattern of the sum signal at the difference signal processing means in an analog manner. As a result, the waveform shaping circuit and subsequent blocks for detecting information from the analog signal can be used commonly for both signals. The size of the circuits can therefore be reduced.

[0114] When the switching means is provided to switch between the difference signal and reverse difference signal according to the direction of the shift of the identification signal while the identification information is reproduced, the first identification information and second identification information can be made to have an identical polarity,

[0115] When a gate signal indicating the position of appearance of the first identification information and a gate signal indicating the position of appearance of the second identification information are provided, and a switching means is provided to switch between the difference signal and the reverse difference signal according to the gate signals, the first identification information and second identification information can be made to have an identical polarity,

[0116] When a direct-current component rejection means is provided to remove the direct-current component from the waveform of the difference signal and the waveform of the sum signal and the time constants for control are switched at the points where the variation in the level of the signal occurs, the waveform pattern of the difference signal and the waveform pattern of the sum signal can be made to be similar to each other at a high speed. Further, by switching the time constants for control immediately after a flaw in the medium, a return can also be made at a high speed.

[0117] When a slice level controlling means is provided to optimize the slice level of a signal with a predetermined time constant, and a slice level adjustment means is provided to add an offset to the slice level set by the slice

level controlling means, an optimum slice level can be set and the probability of signal detection can be improved.

[0118] When slice level offset values are set separately for the first and second identification information and user information, an optimum slice level can be set and the probability of signal detection can be improved.

[0119] When the time constant of slice level control is decreased immediately after the beginning of the first and second identification information and the user information, the slice level can be optimized at a high speed. Moreover, the slice level can also be optimized at a high speed by decreasing the time constant of slice level control immediately after a flaw in the medium.

[0120] When the period for which the control time constant of the direct-current component rejection means is reduced and the period for which the time constant of slice level control is reduced are set separately, the probability of signal detection can be improved.

[0121] When the period for which the control time constant of the direct-current component rejection means is reduced and the period for which the time constant of slice level control is reduced are adjusted according to the reproduction speed, the probability of signal detection can be improved at a variety of reproduction speeds.

[0122] When the control time constant of the direct-current component rejection means and the time constant of slice level control are adjusted according to the reproduction speed, the probability of signal detection can be improved at a variety of reproduction speeds.

[0123] When offset values are set separately for the first and second identification information and user information of the land tracks and of the groove tracks of the disk medium, the probability of signal detection can be improved.

Claims

1. A circuit for use in a device for reproducing information from an optical disk having recording tracks the disk including an information area having a first identification information area shifted radially outward from the centre of a recording track and a second identification information area shifted radially inward from the centre of a recording track, the device comprising a light detector (135) having at least two light-detecting regions, said circuit comprising adding means (16) for adding the outputs of the light detector, and subtracting means (18) for subtracting the outputs of the light detector, and **characterised by** means (352) for reversing the polarity with respect to a reference level of the output of the subtracting means for one of the first and second identification information to produce an identification information signal having the same polarity with respect to a reference level for the first and second identification information areas.

2. The circuit according to claim 1 for a device further including an optical head (13) illuminating the optical disk medium with a light beam, wherein said light detector is a split photodetector (135) having at least two light-receiving parts disposed on opposite side of a track tangential line in the far field of the optical disk medium and receiving light reflected from the optical disk medium, wherein on the disk the first identification information is shifted radially outward with respect to the center of a recording track by a specified distance and the second identification information is shifted radially inward with respect to the center of a recording track by a specified distance, said optical disk further including a user information area disposed along the center of a recording track disposed in concentric or spiral form. wherein the adding means (16) adds the outputs of said split photodetector (135); the subtracting means (18) obtains a difference between the outputs of said split photodetector (135); said circuit further comprising:

a difference signal processing means (35) which makes the output of said subtracting means (18) similar in regard to the polarity to the output signal of said adding means (16) in waveform pattern comprising said polarity reversing means; a signal selecting means (36) which selectively outputs the output of said adding means (16) and the output of said difference signal processing means (35); and a waveform shaping means (37) which detects the information recorded on said disk medium from the output signal of said signal selecting means (36); said signal selecting means (36) being set to select the output of said difference signal processing means (35) or the output of said adding means (36) when said identification information area is being scanned and to select the output of said adding means (16) when said user information area is being scanned.

3. The circuit according to claim 2, wherein said difference signal processing means (35) further comprises:

a selecting means (354) for selectively outputting the output of the subtracting means (18) and the output of the polarity inverting means (352); and a polarity switching signal generation means (353) for generating a switching timing for the selecting means (354).

4. The circuit according to claim 3, wherein said polarity switching signal generation means (353) comprises:

- a first gate generating means (281) for generating a first gate signal indicating the position at which the first identification information appears; a second gate generating means (282) for generating a second gate signal indicating the position at which the second identification information appears; and a polarity judging means (355) for judging the polarity of the waveform of the output of the subtracting means (18).
5. The circuit according to claim 2, wherein said waveform shaping means (37) comprises:
- a direct-current component rejection means (374) for removing the direct-current component of the output of the signal selecting means (36) with a predetermined time constant;
 - a third gate generation means (371) for generating a gate signal (o) indicating the position at which the user information appears;
 - a fourth gate generation means (372) for generating a gate signal (p) indicating the position of a defect;
 - a boost gate generation means (373) for generating a first boost gate signal (q) for changing the time constant of the direct-current component rejecting means (374) in accordance with the outputs of said first gate generation means (281), said second gate generation means (291), said third gate generation means (371), and said fourth gate generation means (373); and
 - a signal slice means (375) for binarizing the output of the direct-current component rejection means (374);
 - said direct-current component rejection means (374) reducing said time constant for a predetermined period after the beginning of the first identification information, the beginning of the second identification information, the beginning of the user information, and the end of the defect.
6. The circuit according to claim 5, wherein said signal slice means (375) comprises:
- a first binarization means (376) for binarizing the output of the direct-current component rejection means (374) at a slice level;
 - a slice level control means (377) for controlling the slice level to an optimum value with a predetermined time constant based on the output of said first binarization means (376);
 - a slice level adjustment means (378) for applying an offset to the slice level set by said slice level control means (377); and
 - a second binarization means (379) for binarizing the output of the direct-current component re-
- jection means (374) at the output of said slice level adjustment means (377).
7. The circuit according to claim 6, wherein said slice level adjustment means (378) generates a second boost gate signal of a predetermined duration generated in accordance with the outputs of the first to fourth gate generation means, and reduces the time constant for control of the slice level, for the period of said second boost gate signal.
8. The circuit according to claim 7, wherein said boost gate generation means (373) and the slice level adjustment means (378) can vary the period of the boost gate signal according to the reproduction speed.
9. The circuit according to claim 6, wherein said direct-current component rejection means, and said slice level control means can vary the time constant for control.
10. The circuit according to claim 6, wherein said slice level adjusting means (378) can set the offset values separately for the first identification information, the second information, and the user information.
11. A device for reproducing information from an optical disk having recording tracks the disk including an information area having a first identification information area shifted radially outward from the centre of a recording track and a second identification information area shifted radially inward from the centre of a recording track, the device comprising a light detector (135) having at least two light-detecting regions, and a circuit according to claim 1.
12. An optical information reproduction device for reproducing information from an optical disk medium on which an identification information area including first identification information shifted radially outward with respect to the center of a recording track by a specified distance and second identification information shifted radially inward with respect to the center of a recording track by a specified distance, and a user information area disposed along the center of a recording track are disposed in concentric or spiral form, said optical information reproduction device comprising the circuit according to claim 2 and an optical head (13) wherein the optical head (13) illuminates the optical disk medium (11) with a light beam, and includes the split photodetector (135) having at least two light-receiving parts disposed on opposite side of a track tangential line in the far field of the optical disk medium and receiving light reflected from the optical disk medium.

13. A method of reproducing information from an optical disk having recording tracks and including an information area having a first identification information area shifted radially outward from the centre of a recording track and a second identification information area shifted radially inward from the centre of a recording track, the method comprising detecting light reflected from the recording medium using a light detector having at least two light-detecting regions, adding the outputs of the light detector, and subtracting the outputs of the light detector, **characterised in that** the method further comprises:

reversing the polarity with respect to a reference level of the output of the subtracting means for one of the first and second identification information to produce an identification information signal having the same polarity with respect to a reference level for the first and second identification information areas.

Patentansprüche

1. Schaltung zur Verwendung in einer Vorrichtung zum Wiedergeben von Informationen von einer optischen Scheibe mit Aufzeichnungsspuren, welche Scheibe einen Informationsbereich mit einem ersten Identifikationsinformationsbereich, der von der Mitte einer Aufzeichnungsspur radial nach außen verschoben ist, und einem zweiten Identifikationsinformationsbereich, der von der Mitte einer Aufzeichnungsspur radial nach innen verschoben ist, enthält, wobei die Vorrichtung einen Lichtdetektor (135) mit zumindest zwei Lichterfassungsbereichen aufweist, welche Schaltung Additionsmittel (16) zum Addieren der Ausgangssignale des Lichtdetektors und Subtraktionsmittel (18) zum Subtrahieren der Ausgangssignale des Lichtdetektors aufweist, und **gekennzeichnet durch** Mittel (352) zum Umkehren der Polarität mit Bezug auf einen Bezugspegel des Ausgangssignals der Subtraktionsmittel für die einen von den ersten und den zweiten Identifikationsinformationen zum Erzeugen eines Identifikationsinformationssignals mit derselben Polarität mit Bezug auf einen Bezugspegel für den ersten und den zweiten Identifikationsinformationsbereich.
2. Schaltung nach Anspruch 1 für eine Vorrichtung weiterhin enthaltend einen optischen Kopf (13), der das optische Scheibenmedium mit einem Lichtstrahl beleuchtet, wobei der Lichtdetektor ein geteilter Fotodetektor (135) mit zumindest zwei Lichtempfangsteilen, die sich auf entgegengesetzten Seiten einer Spurentangentiellinie in dem Fernfeld des optischen Scheibenmediums befinden und von dem optischen Scheibenmedium reflektiertes Licht empfangen, ist, wobei auf der Scheibe die ersten Identifikationsin-

formationen radial auswärts mit Bezug auf die Mitte einer Aufzeichnungsspur um einen bestimmten Abstand verschoben sind und die zweiten Identifikationsinformationen mit Bezug auf die Mitte einer Aufzeichnungsspur um einen bestimmten Abstand radial nach innen verschoben sind, welche optische Scheibe weiterhin einen Benutzerinformationsbereich enthält, der entlang der Mitte einer Aufzeichnungsspur angeordnet ist, die eine konzentrische oder Spiralforn aufweist, wobei die Additionsmittel (16) die Ausgangssignale des geteilten Fotodetektors (135) addieren; die Subtraktionsmittel (18) eine Differenz zwischen den Ausgangssignalen des geteilten Fotodetektors (135) erhalten; welche Schaltung weiterhin aufweist:

Differenzsignal-Verarbeitungsmittel (35), die bewirken, dass das Ausgangssignal der Subtraktionsmittel (18) in Bezug auf die Polarität dem Ausgangssignal der Addiermittel (16) im Wellenformmuster aufweisend die Polaritätsumkehrmittel ähnlich gemacht wird; Signalauswahlmittel (36), die selektiv das Ausgangssignal der Additionsmittel (16) und das Ausgangssignal der Differenzsignal-Verarbeitungsmittel (35) ausgeben; und wellenform-Formungsmittel (37), die die auf dem Scheibenmedium aufgezeichneten Informationen in dem Ausgangssignal der Signalauswahlmittel (36) erfassen; welche Signalauswahlmittel (36) so eingestellt sind, dass sie das Ausgangssignal der Differenzsignal-Verarbeitungsmittel (35) oder das Ausgangssignal der Additionsmittel (36) auswählen, wenn der Identifikationsinformationsbereich abgetastet wird, und das Ausgangssignal der Additionsmittel (16) auswählen, wenn der Benutzerinformationsbereich abgetastet wird.

3. Schaltung nach Anspruch 2, bei der die Differenzsignal-Verarbeitungsmittel (35) weiterhin aufweisen:
 - eine Auswahlvorrichtung (354) zum selektiven Ausgeben des Ausgangssignals der Subtraktionsmittel (18) und des Ausgangssignals der Polaritätsumkehrmittel (352); und
 - Polaritätsumschaltsignal- Erzeugungsmittel (353) zum Erzeugen einer Umschaltzeit für die Auswahlmittel (354).
4. Schaltung nach Anspruch 3, bei der die Polaritätsumschaltsignal-Erzeugungsmittel (353) aufweisen:
 - erste Torerzeugungsmittel (281) zum Erzeugen eines ersten Torsignals, das die Position anzeigt, an der die ersten Identifikationsinfor-

- tionen erscheinen;
 zweite Torerzeugungsmittel (282) zum Erzeugen eines zweiten Torsignals, das die Position anzeigt, an der die zweiten Identifikationsinformationen erscheinen; und
 Polaritätsbeurteilungsmittel (355) zum Beurteilen der Polarität der Wellenform des Ausgangssignals der Subtraktionsmittel (18).
5. Schaltung nach Anspruch 2, bei der die Wellenform-Formungsmittel (37) aufweisen; Gleichstromkomponenten-Zurückweisungsmittel (374) zum Entfernen der Gleichstromkomponente des Ausgangssignals der Signalauswahlmittel (36) mit einer vorbestimmten Zeitkonstante; dritte Torerzeugungsmittel (371) zum Erzeugen eines Torsignals (o), das die Position anzeigt, an der die Benutzerinformationen erscheinen; vierte Torerzeugungsmittel (372) zum Erzeugen eines Torsignals (p), das die Position eines Defekts anzeigt; Erhöhungstor-Erzeugungsmittel (373) zum Erzeugen eines ersten Erhöhungstorsignals (q) zum Ändern der Zeitkonstanten der Gleichstromkomponenten-Zurückweisungsmittel (374) gemäß den Ausgangssignalen der ersten Torerzeugungsmittel (281), der zweiten Torerzeugungsmittel (291), der dritten Torerzeugungsmittel (371) und der vierten Torerzeugungsmittel (373); und Signalschneidmittel (375) zum Binärisieren des Ausgangssignals der Gleichstromkomponenten-Zurückweisungsmittel (374); wobei die Gleichkomponenten-Zurückweisungsmittel (374) die Zeitkonstante während einer vorbestimmten Periode nach dem Beginn der ersten Identifikationsinformationen, dem Beginn der zweiten Identifikationsinformationen, dem Beginn der Benutzerinformationen und dem Ende des Defekts verringern.
6. Schaltung nach Anspruch 5, bei der die Signalschneidmittel (375) aufweisen:
- erste Binärisierungsmittel (376) zum Binärisieren des Ausgangssignals der Gleichstromkomponenten-Zurückweisungsmittel (374) bei einem Schneidpegel;
 Schneidpegel-Steuermittel (377) zum Steuern des Schneidpegels auf einen optimalen Wert mit einer vorbestimmten Zeitkonstanten auf der Grundlage des Ausgangssignals der ersten Binärisierungsmittel (376);
 Schneidpegel-Einstellmittel (378) zum Ausüben einer Versetzung auf den von den Schneidpegel-Steuermitteln (377) eingestellten Pegel; und
 zweite Binärisierungsmittel (379) zum Binärisieren des Ausgangssignals der Gleichstromkomponenten-Zurückweisungsmittel (374) an dem Ausgang der Schneidpegel-Einstellmittel (377).
7. Schaltung nach Anspruch 6, bei der die Schneidpegel-Einstellmittel (378) ein zweites Erhöhungstorsignal mit vorbestimmter Dauer erzeugen, das gemäß den Ausgangssignalen der ersten bis vierten Torerzeugungsmittel erzeugt wurde, und die Zeitkonstante für die Steuerung des Schneidpegels während der Periode des zweiten Erhöhungstorsignals herabsetzen.
8. Schaltung nach Anspruch 7, bei der die Erhöhungstor-Erzeugungsmittel (373) und die Schneidpegel-Einstellmittel (378) die Periode des Erhöhungstorsignals gemäß der Wiedergabegeschwindigkeit variieren können.
9. Schaltung nach Anspruch 6, bei der die Gleichstromkomponenten-Zurückweisungsmittel und die Schneidpegel-Steuermittel die Zeitkonstante für die Steuerung variieren können.
10. Schaltung nach Anspruch 6, bei der die Schneidpegel-Einstellmittel (378) die Versetzungswerte getrennt für die ersten Identifikationsinformationen, die zweiten Identifikationsinformationen und die Benutzerinformationen einstellen können.
11. Vorrichtung zum Wiedergeben von Informationen von einer optischen Scheibe mit Aufzeichnungsspuren, welche Scheibe einen Informationsbereich mit einem ersten Identifikationsinformationsbereich, der von der Mitte einer Aufzeichnungsspur radial nach außen verschoben ist, und einen zweiten Identifikationsinformationsbereich, der von der Mitte einer Aufzeichnungsspur radial nach innen verschoben ist, enthält, wobei die Vorrichtung einen Lichtdetektor (135) mit zumindest zwei Lichterfassungsbereichen und eine Schaltung nach Anspruch 1 aufweist.
12. Wiedergabevorrichtung für optische Informationen zum Wiedergeben von Informationen von einem optischen Scheibenmedium, auf dem ein Identifikationsinformationsbereich enthaltend erste Identifikationsinformationen, die mit Bezug auf die Mitte einer Aufzeichnungsspur um einen bestimmten Abstand radial nach außen verschoben sind, und zweite Identifikationsinformationen, die mit Bezug auf die Mitte einer Aufzeichnungsspur um einen vorbestimmten Abstand radial nach innen verschoben sind, und ein Benutzerinformationsbereich, der entlang der Mitte einer Aufzeichnungsspur verläuft, in konzentrischer oder Spitalform angeordnet sind, welche Wiedergabevorrichtung für optische Informationen die Schaltung nach Anspruch 2 und einen optischen Kopf (13) aufweist, bei der der optische Kopf (13) das optische Scheibenmedium (11) mit einem Lichtstrahl beleuchtet und den geteilten Fotodetektor (135) mit zumindest zwei lichtempfindenden Teilen, die sich auf entgegengesetzten

setzten Seiten einer Spurtangentiallinie in dem Fernfeld des optischen Scheibenmediums befinden und von dem optischen Scheibemedium reflektiertes Licht empfangen, enthält.

13. Verfahren zum Wiedergeben von Informationen von einer optischen Scheibe mit Aufzeichnungsspuren und enthaltend einen Informationsbereich mit einem ersten Identifikationsinformationsbereich, der von der Mitte einer Aufzeichnungsspur radial nach außen verschoben ist, und einem zweiten Identifikationsinformationsbereich, der von der Mitte einer Aufzeichnungsspur radial nach innen verschoben ist, welches Verfahren aufweist: Erfassen von von dem Aufzeichnungsmedium reflektiertem Licht unter Verwendung eines Lichtdetektors mit zumindest zwei Lichterfassungsbereichen, Addieren der Ausgangssignale des Lichtdetektors, und Subtrahieren der Ausgangssignale des Lichtdetektors, **dadurch gekennzeichnet, dass** das Verfahren weiterhin aufweist:

Umkehren der Polarität mit Bezug auf einen Bezugspegel des Ausgangssignals der Subtraktionsmittel für die einen der ersten und der zweiten Identifikationsinformationen, um ein Identifikationsinformationssignal mit derselben Polarität mit Bezug auf einen Bezugspegel für den ersten und den zweiten Identifikationsinformationsbereich zu erzeugen.

Revendications

1. Un circuit destiné à être utilisé dans un dispositif de lecture d'informations à partir d'un disque optique pourvu de pistes d'enregistrement, le disque incluant une zone d'informations possédant une première zone d'informations d'identification décalée radialement vers l'extérieur à partir du centre d'une piste d'enregistrement et une deuxième zone d'informations d'identification décalée radialement vers l'intérieur à partir du centre d'une piste d'enregistrement, le dispositif comprenant un détecteur de lumière (135) possédant au moins deux régions de détection de lumière, ledit circuit comprenant des moyens d'addition (16) pour ajouter les sorties du détecteur de lumière, et des moyens de soustraction (18) pour soustraire les sorties du détecteur de lumière, et **caractérisé par** des moyens (352) pour inverser la polarité par rapport à un niveau de référence de la sortie des moyens de soustraction pour l'une des première et deuxième informations d'identification afin de produire un signal d'informations d'identification ayant la même polarité par rapport à un niveau de référence pour les première et deuxième zones d'informations d'identification.

2. Le circuit selon la revendication 1 pour un dispositif incluant en outre une tête optique (13) éclairant le support de disque optique avec un faisceau lumineux, dans lequel ledit détecteur de lumière est un photodétecteur à plusieurs quadrants (135) possédant au moins deux parties de réception de lumière disposées sur un côté opposé d'une ligne tangente à la piste dans le champ éloigné du support de disque optique et recevant une lumière réfléchie par le support de disque optique, dans lequel, sur le disque, la première information d'identification est décalée radialement vers l'extérieur par rapport au centre d'une piste d'enregistrement à une distance spécifiée et la deuxième information d'identification est décalée radialement vers l'intérieur par rapport au centre d'une piste d'enregistrement à une distance spécifiée, ledit disque optique incluant en outre une zone d'informations utilisateur disposée le long du centre d'une piste d'enregistrement disposée en forme concentrique ou spirale, dans lequel les moyens d'addition (16) ajoutent les sorties dudit photodétecteur à plusieurs quadrants (135); les moyens de soustraction (18) obtiennent une différence entre les sorties dudit photodétecteur à plusieurs quadrants (135); ledit circuit comprenant en outre :

des moyens de traitement de signal de différence (35) qui rendent la sortie desdits moyens de soustraction (18) similaire au niveau de la polarité au signal de sortie desdits moyens d'addition (16) en motif de forme d'onde comprenant lesdits moyens d'inversion de polarité ;
des moyens de sélection de signal (36) qui produisent sélectivement la sortie desdits moyens d'addition (16) et la sortie desdits moyens de traitement de signal de différence (35) ; et
des moyens de mise en forme de forme d'onde (37) qui détectent les informations enregistrées sur ledit support de disque à partir du signal de sortie desdits moyen de sélection de signal (36) ;
lesdits moyens de sélection de signal (36) étant réglés pour sélectionner la sortie desdits moyens de traitement de signal de différence (35) ou la sortie desdits moyens d'addition (36) lorsque ladite zone d'informations d'identification est balayée et pour sélectionner la sortie desdits moyens d'addition (16) lorsque ladite zone d'informations utilisateur est balayée.

3. Le circuit selon la revendication 2, dans lequel lesdits moyens de traitement de signal de différence (35) comprennent en outre:

des moyens de sélection (354) pour produire sélectivement la sortie des moyens de soustraction (18) et la sortie des moyens d'inversion de

polarité (352) ; et
des moyens de génération de signal de commutation de polarité (353) pour générer une synchronisation de commutation pour les moyen de sélection (354).

4. Le circuit selon la revendication 3, dans lequel lesdits moyens de génération de signal de commutation de polarité (353) comprennent :

des premiers moyens de génération de porte (281) pour générer un premier signal de porte indiquant la position à laquelle la première information d'identification apparaît ;
des deuxièmes moyens de génération de porte (282) pour générer un deuxième signal de porte indiquant la position à laquelle la deuxième information d'identification apparaît ; et
des moyen d'évaluation de polarité (355) pour évaluer la polarité de la forme d'onde de la sortie des moyens de soustraction (18).

5. Le circuit selon la revendication 2, dans lequel lesdits moyens de mise en forme de forme d'onde (37) comprennent :

des moyens de rejection de la composante de courant continu (374) pour Éliminer la composante de courant continu de la sortie des moyens de sélection du signal (36) avec une constante de temps prédéterminée ;
des troisièmes moyens de génération de porte (371) pour générer un signal de porte (o) indiquant la position à laquelle les informations utilisateur apparaissent ;
des quatrièmes moyens de génération de porte (372) pour générer un signal de porte (p) indiquant la position d'un défaut ;
des moyens de génération de porte rapide (373) pour générer un premier signal de porte rapide (q) pour modifier la constante de temps des moyens de rejection de la composante de courant continu (374) conformément aux sorties desdits premiers moyens de génération de porte (281), desdits deuxièmes moyens de génération de porte (291), desdits troisièmes moyens de génération de porte (371), et desdits quatrièmes moyens de génération de porte (373) ; et
des moyens de découpage en tranches de signal (375) pour binariser la sortie des moyens de rejection de la composante de courant continu (374) ;
lesdits moyens de rejection de la composante de courant continu (374) réduisant ladite constante de temps pour une période prédéterminée après le début de la première information d'identification, le début de la deuxième information d'identification, le début des informations utili-

sateur et la fin du défaut.

6. Le circuit selon la revendication 5, dans lequel lesdits moyens de découpage en tranches du signal (375) comprennent :

des premiers moyens de binarisation (376), pour binariser la sortie des moyens de rejection de la composante de courant continu (374) au niveau d'une tranche ;
des moyens de contrôle de niveau de tranche (377) pour contrôler le niveau de tranche à une valeur optimale avec une constante de temps prédéterminée basée sur la sortie desdits premiers moyens de binarisation (376) ;
des moyens d'ajustement de niveau de tranche (378) pour appliquer un décalage au niveau de la tranche défini par lesdits moyens de contrôle de niveau de tranche (377) ; et
des deuxièmes moyens de binarisation (379) pour binariser la sortie des moyens de rejection de la composante de courant continu (374) à la sortie des moyens d'ajustement du niveau de tranche (377).

7. Le circuit selon la revendication 6, dans lequel lesdits moyens d'ajustement de niveau de tranche (378) génèrent un deuxième signal de porte rapide d'une durée prédéterminée généré conformément aux sorties des premiers aux quatrièmes moyens de génération de porte et réduisent la constante de temps pour contrôler le niveau de tranche pour la période dudit deuxième signal de porte rapide.

8. Le circuit selon la revendication 7, dans lequel lesdits moyens de génération de porte rapide (373) et les moyens d'ajustement de niveau de tranche (378) peuvent modifier la période du signal de porte rapide conformément à la vitesse de lecture.

9. Le circuit selon la revendication 6, dans lequel lesdits moyens de rejection de la composante de courant continu, et lesdits moyens de contrôle de niveau de tranche peuvent modifier la constante de temps pour le contrôle.

10. Le circuit selon la revendication 6, dans lequel ledit moyen d'ajustement de niveau de tranche (378) peut définir les valeurs de décalage séparément pour la première information d'identification, la deuxième information et les informations utilisateur.

11. Un dispositif de lecture d'informations à partir d'un disque optique pourvu de pistes d'enregistrement, le disque incluant une zone d'informations possédant une première zone d'informations d'identification décalée radialement vers l'extérieur à partir du centre d'une piste d'enregistrement et une deuxième

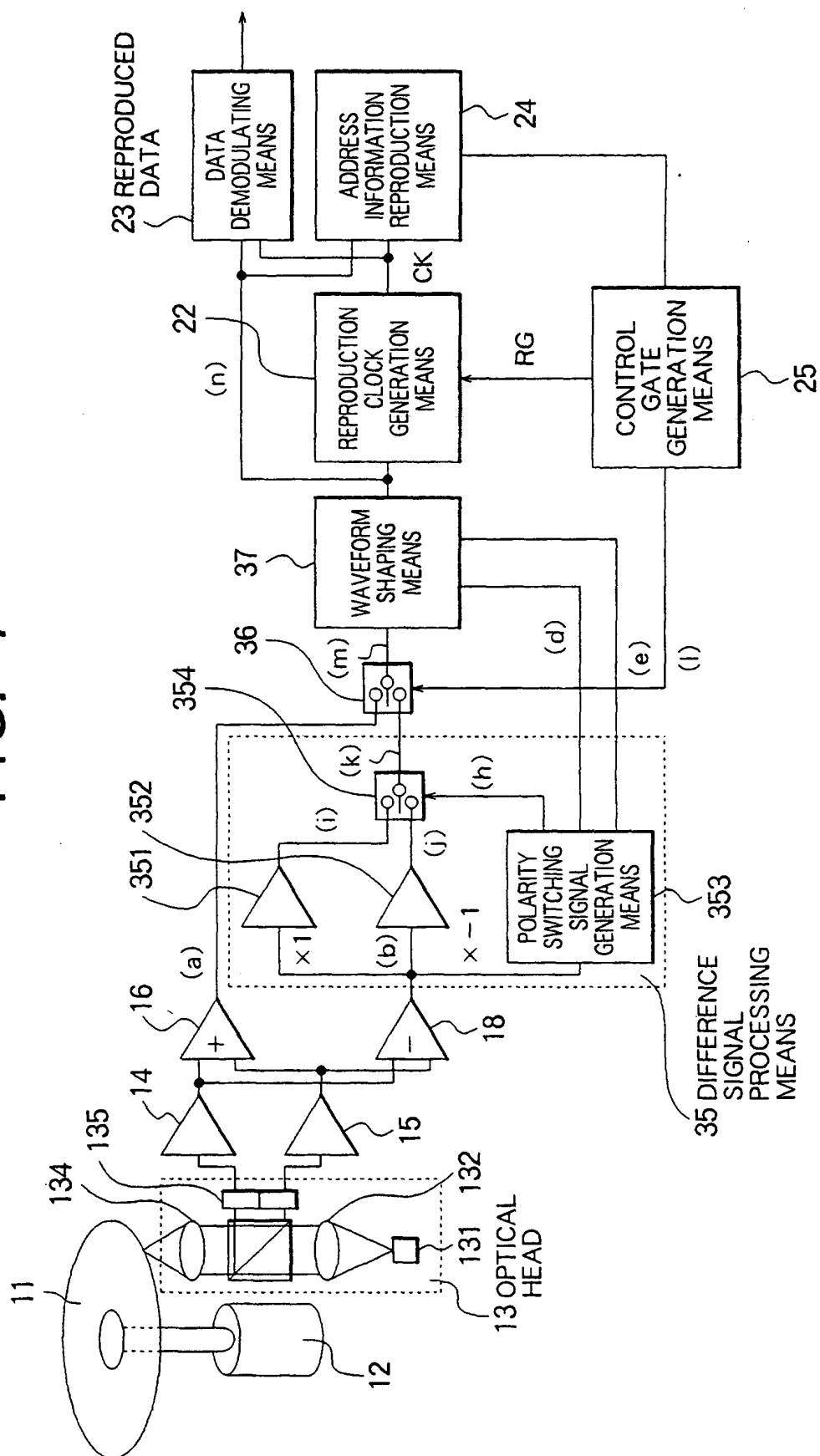
zone d'informations d'identification décalée radialement vers l'intérieur à partir du centre d'une piste d'enregistrement, le dispositif comprenant un détecteur de lumière (135) possédant au moins deux régions de détection de lumière, et un circuit selon la revendication 1. 5

12. Un dispositif de lecture optique d'informations pour lire des informations à partir d'un support de disque optique sur lequel une zone d'informations d'identification incluant une première information d'identification décalée radialement vers l'extérieur par rapport au centre d'une piste d'enregistrement à une distance spécifiée et une deuxième information d'identification décalée radialement vers l'intérieur par rapport au centre d'une piste d'enregistrement à une distance spécifiée, et une zone d'informations utilisateur disposée le long du centre d'une piste d'enregistrement sont disposées en forme concentrique ou spirale, ledit dispositif de lecture optique d'informations comprenant le circuit selon la revendication 2 et une tête optique (13) dans lequel la tête optique (13) éclaire le support de disque optique (11) avec un faisceau lumineux et comprend le photodétecteur à plusieurs quadrants (135) possédant au moins deux parties recevant de la lumière disposées sur un côté opposé d'une ligne tangente à la piste dans le champ éloigné du support de disque optique et recevant la lumière réfléchiée par le support de disque optique. 10 15 20 25 30

13. Un procédé de lecture d'informations à partir d'un disque optique pourvu de pistes d'enregistrement et incluant une zone d'informations possédant une première zone d'informations d'identification décalée radialement vers l'extérieur par rapport au centre d'une piste d'enregistrement et une deuxième zone d'informations d'identification décalée radialement vers l'intérieur par rapport au centre d'une piste d'enregistrement, le procédé comprenant une détection de la lumière réfléchiée par le support d'enregistrement au moyen d'un détecteur de lumière possédant au moins deux régions de détection de lumière, une addition des sorties du détecteur de lumière et une soustraction des sorties du détecteur de lumière, **caractérisé en ce que** le procédé comprend en outre : 35 40 45

une inversion de la polarité par rapport à un niveau de référence de la sortie des moyens de soustraction pour l'une des première et deuxième informations d'identification afin de produire un signal d'informations d'identification ayant la même polarité par rapport à un niveau de référence pour les première et deuxième zones d'informations d'identification. 50 55

FIG. 1



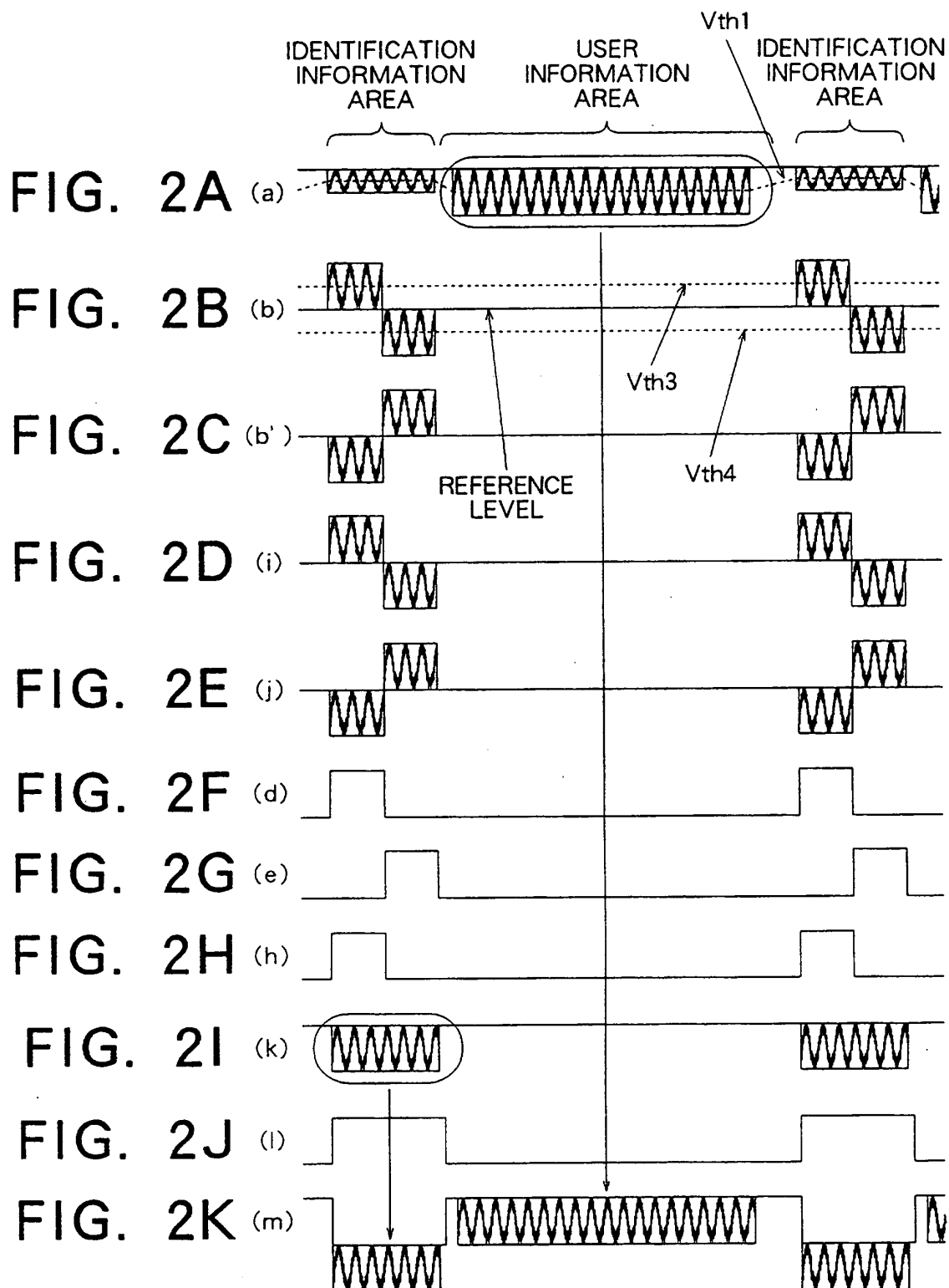




FIG. 4

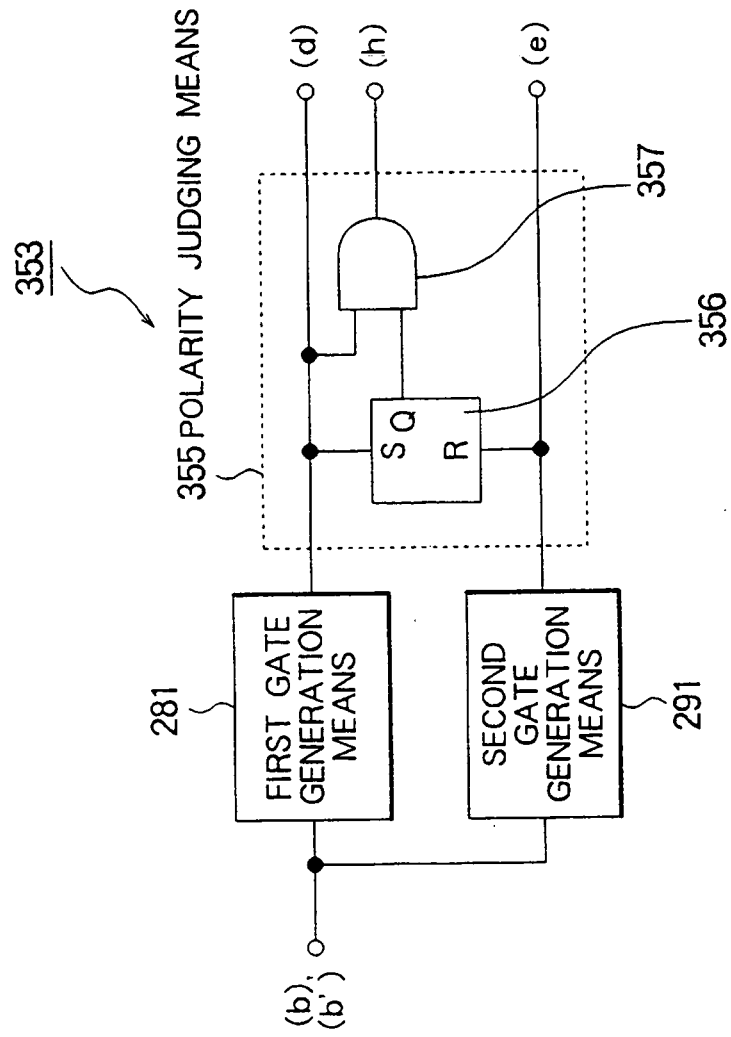
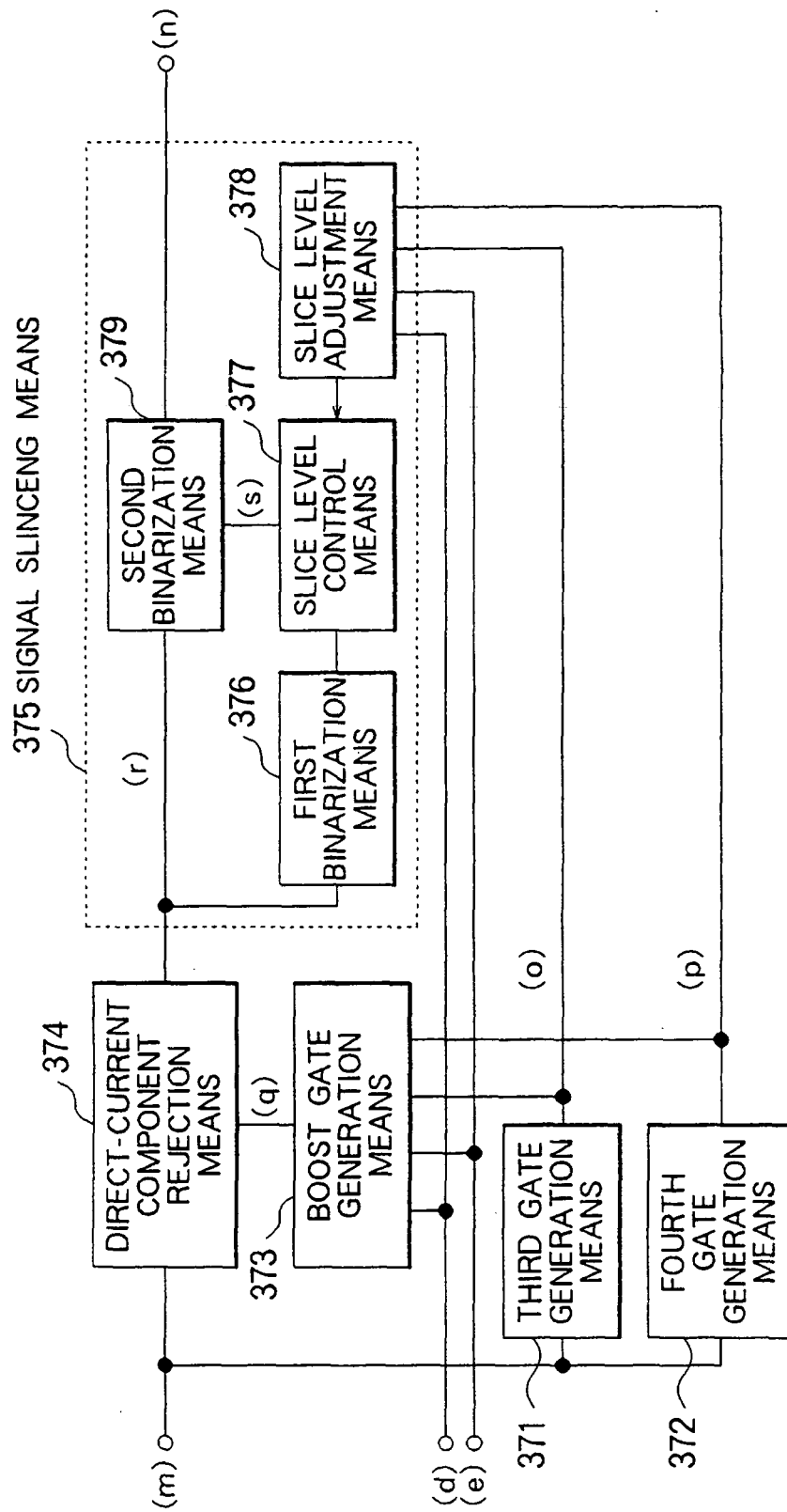


FIG. 5



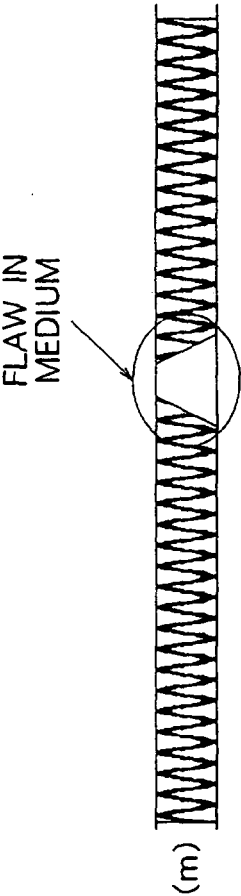


FIG. 6A

FIG. 6B

FIG. 6C

FIG. 7

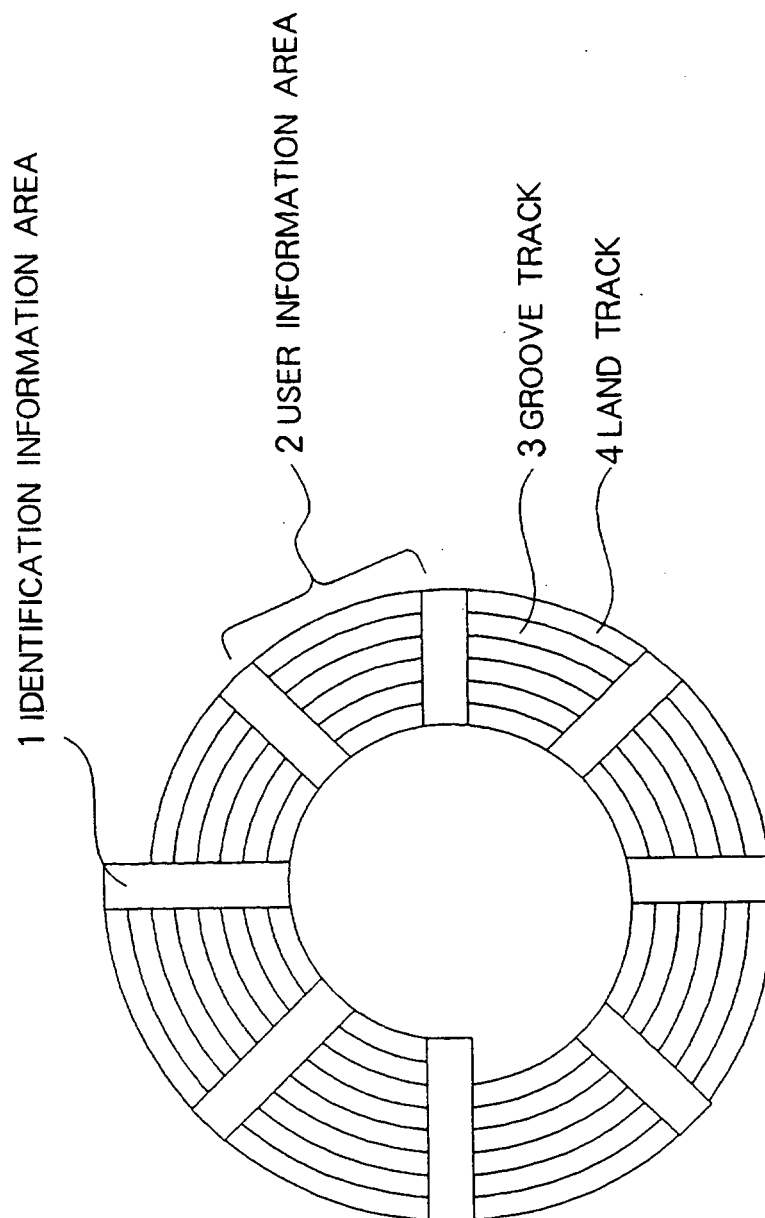


FIG. 8

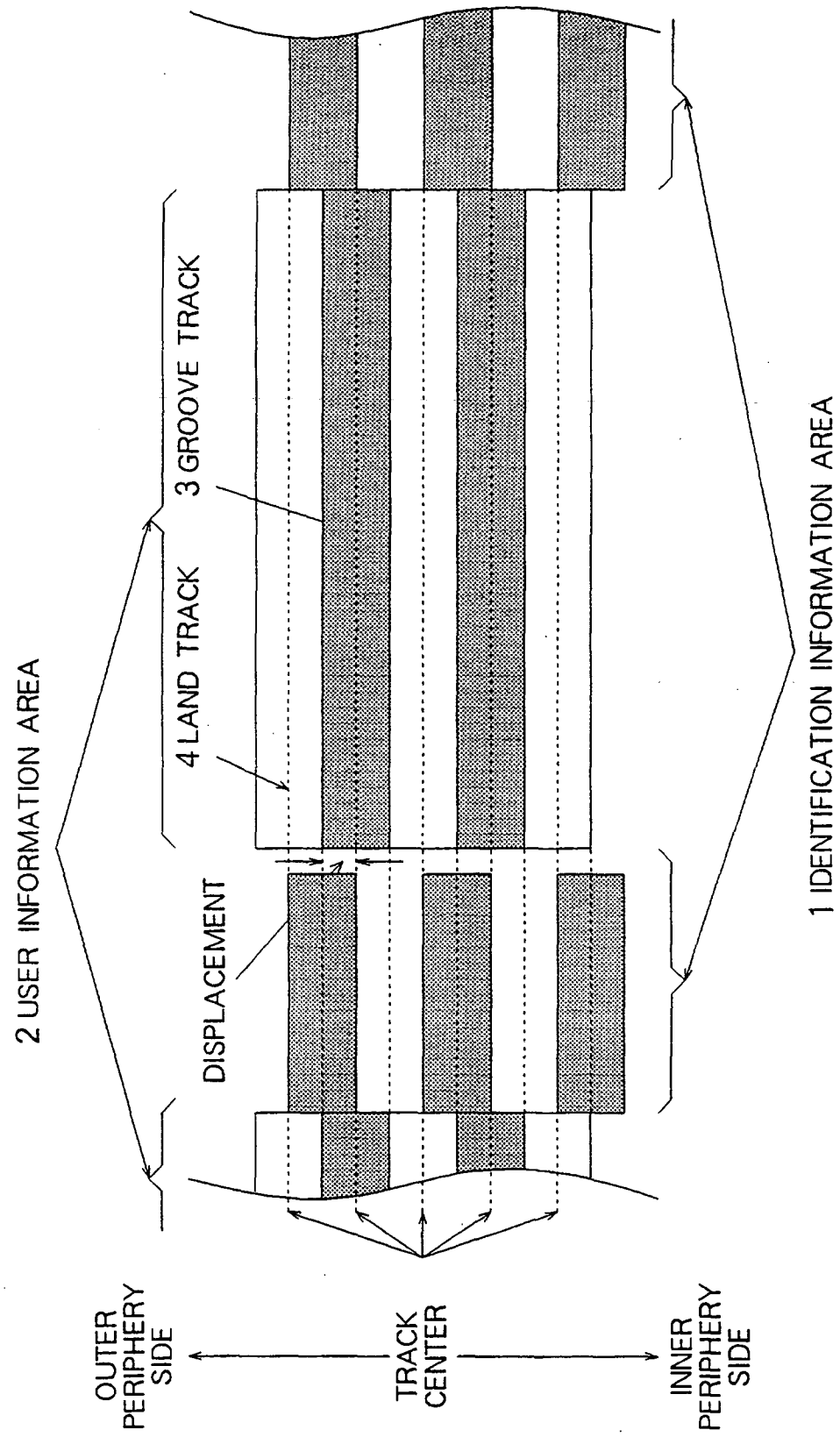
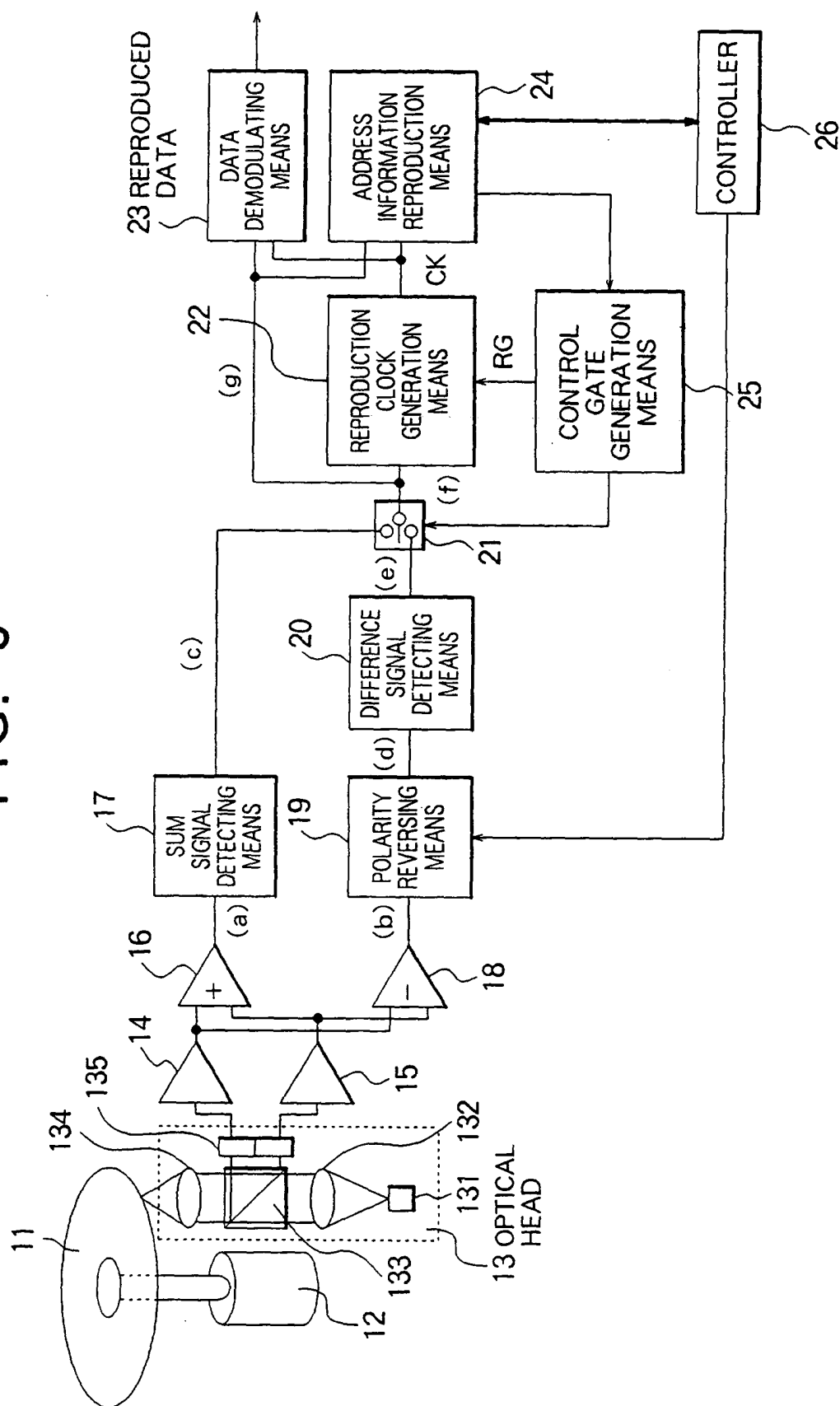


FIG. 9



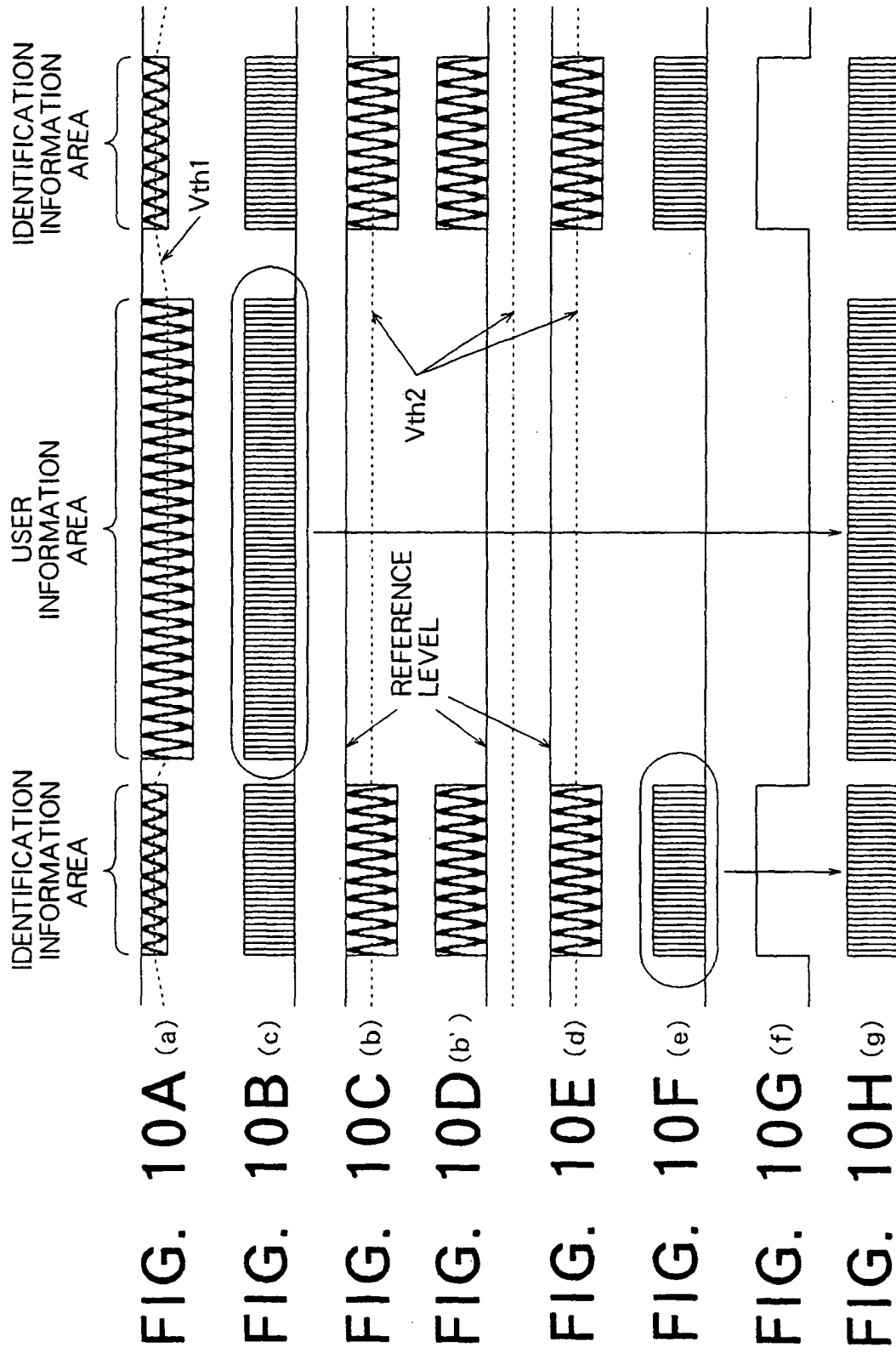


FIG. 11

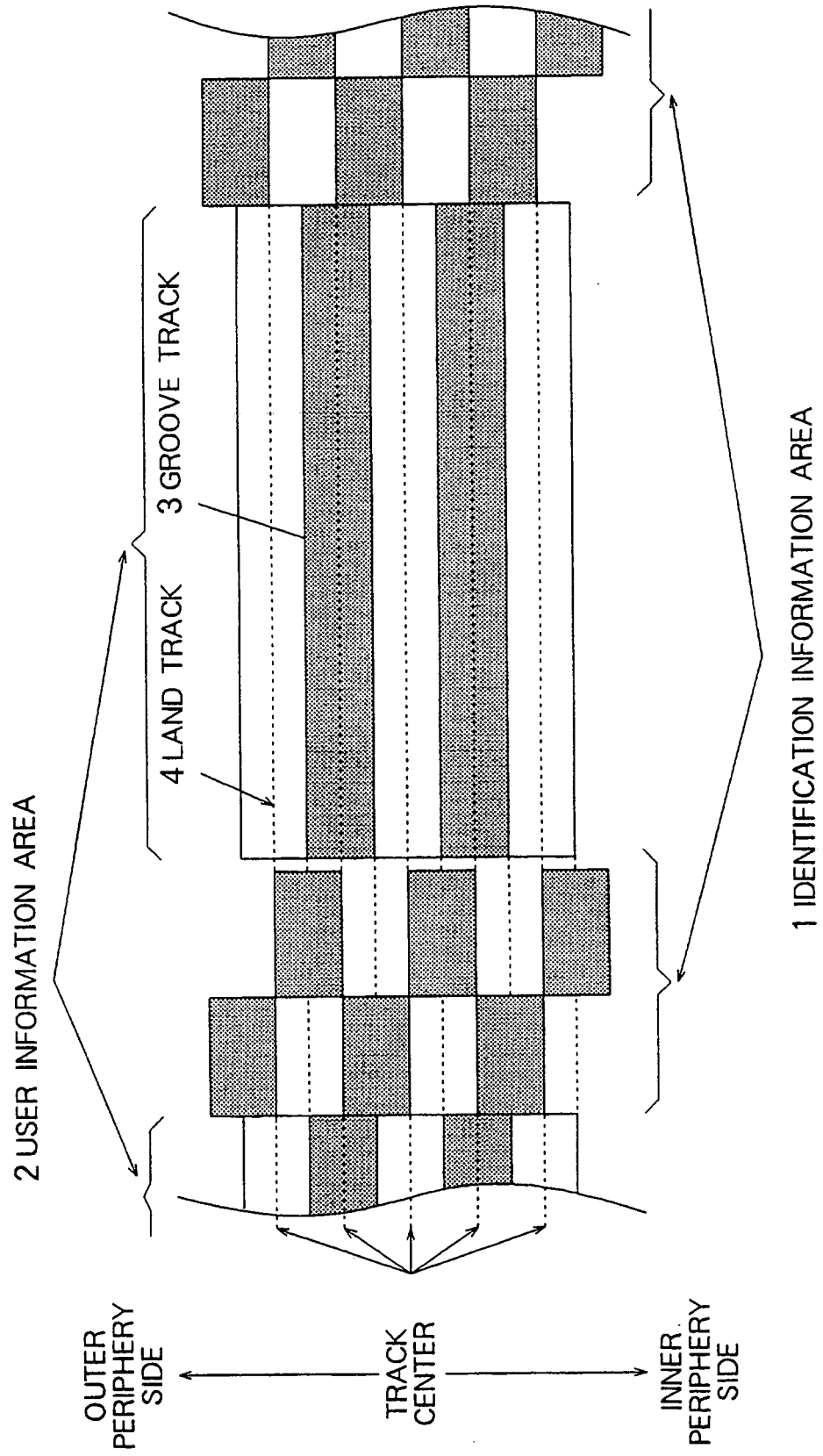


FIG. 12

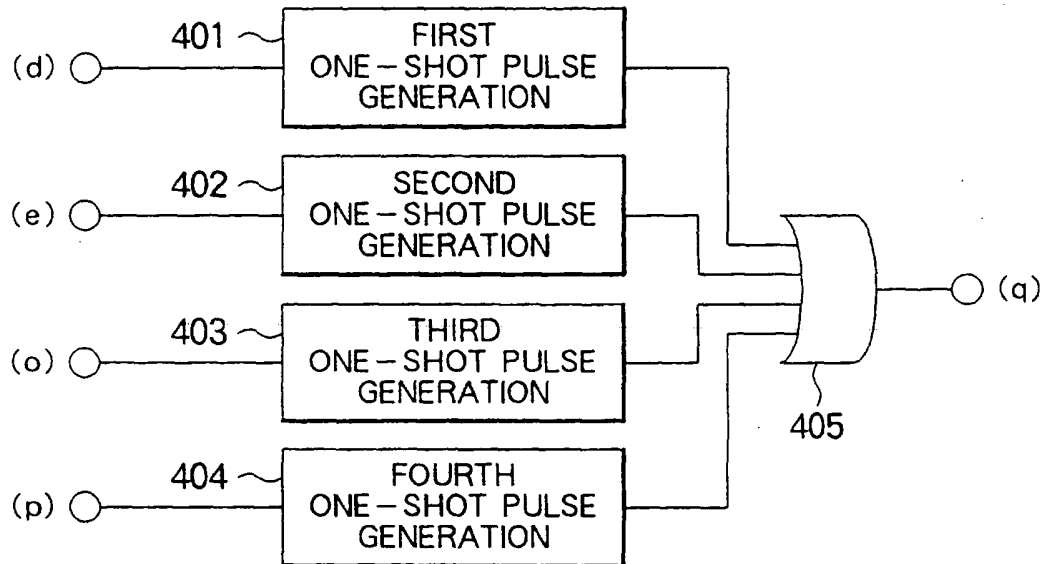


FIG. 13

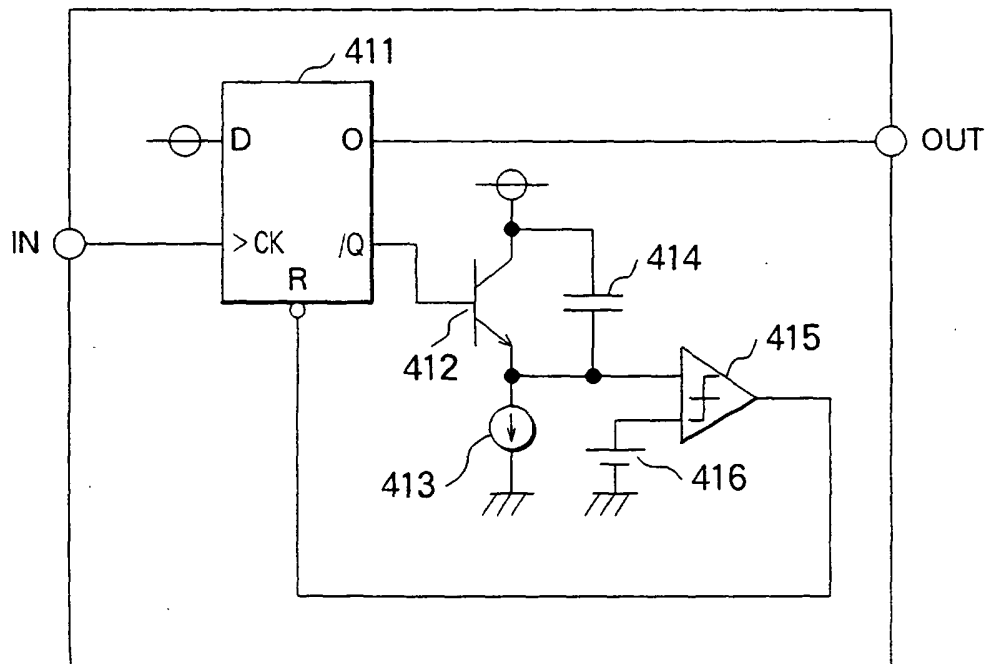


FIG. 14A

FIG. 14B

FIG. 14C

FIG. 14D

FIG. 14E

