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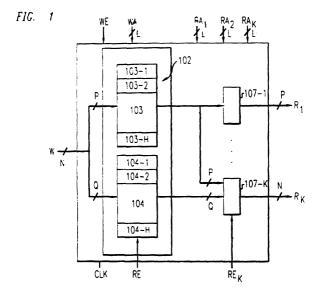
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(54) Multiported register file for updating the coefficients of a burst mode FIR filter

(57) Multiported register files used for storing coefficients in adaptive FIR are improved upon by implementing a split memory architecture (103,104) that has the ability to separately control the least significant bits and the most significant bits of coefficient values that are stored in the filter. When the filter is operated to use so-called "burst mode" updating, the updating circuitry of the filter can be disabled (by WE,RE) and only the most significant bits of the coefficients are read out from the multiported register file while the least significant bits

remain unchanged. This conserves power without sacrificing precision, since only certain ones of the bits of the coefficients are used in the multiplication of the sample. In addition, when only the most significant bits of the coefficients are being cycled through the register filter, any changing bits are prevented from being supplied to the updating circuit, so that the updating circuit performs no computation at all, rather than performing one that is discarded. Advantageously, using such improved multiported register files, adaptive FIR filters can be constructed which operate with lower power consumption.





EUROPEAN SEARCH REPORT

Application Number EP 98 30 9952

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