



(11) **EP 0 940 857 B2**

(12) **NEW EUROPEAN PATENT SPECIFICATION**
After opposition procedure

(45) Date of publication and mention
of the opposition decision:
24.10.2012 Bulletin 2012/43

(51) Int Cl.:
H01L 31/04 ^(2006.01) **H01L 31/0368** ^(2006.01)
H01L 31/0392 ^(2006.01)

(45) Mention of the grant of the patent:
25.11.2009 Bulletin 2009/48

(86) International application number:
PCT/JP1998/000556

(21) Application number: **98902190.2**

(87) International publication number:
WO 1999/010933 (04.03.1999 Gazette 1999/09)

(22) Date of filing: **12.02.1998**

(54) **THIN FILM PHOTOELECTRIC TRANSDUCER**
DÜNNSCHICHT PHOTOELEKTRISCHER WANDLER
TRANSDUCTEUR PHOTOELECTRIQUE A COUCHE MINCE

(84) Designated Contracting States:
DE GB

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(30) Priority: **21.08.1997 JP 24212697**

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(43) Date of publication of application:
08.09.1999 Bulletin 1999/36

(60) Divisional application:
09012398.5 / 2 136 410

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XP004111860 ISSN: 0927-0248**

EP 0 940 857 B2

Description

Technical Field

[0001] The present invention relates generally to improvement in conversion efficiency of a thin film photoelectric converter, and particularly to improvement in photoelectric conversion efficiency of a polycrystalline thin film photoelectric conversion layer.

Background Art

[0002] Important factors concerning photoelectric materials for a photoelectric converter include an effective range of sensible wavelengths, light absorption coefficient, carrier mobility, lifetime of minority carriers and so on, all of which are important parameters of physical properties for improved efficiency of photoelectric converter. In the thin film photoelectric converter, absorption coefficient is particularly important. When a photoelectric conversion layer is formed of a thin film, light absorption is insufficient in longer wavelength range where absorption coefficient is low. Thus, an amount of photoelectric conversion is limited by the thickness of the photoelectric conversion layer. Therefore, a thin film photoelectric converter having high sensitivity and a high absorption coefficient in longer wavelength range is needed.

[0003] Recently, thin film photoelectric materials have been developed which are sensitive to light of a wide range of wavelengths and typically used in thin film polycrystalline silicon solar cells. In a thin film photoelectric material, however, the longer the wavelength of light, the smaller the absorption coefficient of the photoelectric material, and light absorption of the thin film as a whole is limited by the film thickness. Therefore, effective photoelectric conversion is difficult to be achieved over an entire range of sensible light wavelengths.

[0004] In view of the foregoing, an object of the present invention is to provide a thin film photoelectric converter capable of generating a large photoelectric current by producing a light scattering structure for preventing incident light escaping from the photoelectric conversion layer.

[0005] JP 62045079 proposes to improve conversion efficiency by forming a large number of projections of a specified size on the surface where a thin film of silicon compound is in contact with a thin film electrode.

[0006] JP 7122764 proposes to enhance the confinement effect of light impinging upon a solar battery and to form a homogenous thin film by a method wherein the part where a thin film solar battery will be formed is roughened.

[0007] EP A 0591949 proposes techniques for manufacturing columnar-grained polycrystalline sheets by applying granular silicon to a setter material which supports the granular material. The setter material and granular silicon are subjected to a thermal profile which promotes columnar growth by melting the silicon from the top down-

wardly.

[0008] US 5282902 proposes a solar cell comprising a substrate having a surface, a first conductive layer disposed on the surface of said substrate, said first conductive layer having a roughened surface provided with irregularities at the surface thereof, a second conductive layer disposed on the roughened surface of said first conductive layer, said second conductive layer having a roughened surface provided with irregularities, a semiconductor active layer disposed on the roughened surface of said second conductive layer and an upper conductive layer disposed on said semiconductor active layer, wherein said second conductive layer is formed of a 10 nm to 150 nm thick highly reflective metal film.

Disclosure of the Invention

[0009] A thin film photoelectric converter of the present invention includes a substantially polycrystalline photoelectric conversion layer having first and second main surfaces and a metal thin film covering the second main surface; the polycrystalline photoelectric conversion layer being formed of a substantially polycrystalline silicon thin film with an average thickness in the range from 0.5 to 20 μm ; at least the first main surface of the polycrystalline photoelectric conversion layer having a textured surface structure, and the textured structure having fine unevenness of which level differences are smaller than half of the average thickness of the polycrystalline photoelectric conversion layer and substantially in the range from 0.05 to 3 μm ; and a transparent conductive or a transparent semiconductive buffer layer (3a) is interposed between said metal thin film (3) and said polycrystalline photoelectric conversion layer (4).

[0010] Preferably, crystallographic<110> direction of most crystal grains included in the polycrystalline photoelectric conversion layer is approximately parallel with a deviation angle less than 16° to the thickness direction of the polycrystalline photoelectric conversion layer.

[0011] Further, preferably the second main surface of the polycrystalline photoelectric conversion layer also has a textured surface structure and the textured structure has fine unevenness with level differences smaller than half of the thickness of polycrystalline photoelectric conversion layer and substantially in the range from 0.05 to 3 μm .

[0012] Still further, the polycrystalline photoelectric conversion layer is preferably formed of polycrystalline silicon of which crystallized volume fraction is 80% or larger, with hydrogen content of at least 0.1 atomic % and at most 30 atomic %.

[0013] Still further, at least a surface of the metal thin film facing the second main surface of the polycrystalline photoelectric conversion layer is preferably formed of a material selected from the group consisting of Ag, Au, Cu, Al and Pt, or an alloy including the same.

[0014] Still further, preferably, a transparent conductive or a transparent semiconductive buffer layer has a

thickness in the range from 0.05 to 0.15 μm .

[0015] Still further, the thin film photoelectric converter may include an amorphous photoelectric conversion layer of a substantially amorphous silicon thin film deposited on the first main surface of the polycrystalline photoelectric conversion layer.

Brief Description of Drawings

[0016]

Fig. 1 is a schematic sectional view of a thin film photoelectric converter.

Fig. 2 is a transmission electron micrograph showing in section a crystal structure of an underlying conductive layer 2 as shown in Fig. 1.

Fig. 3 is a graph showing distribution of unevenness in a textured surface structure of an underlying conductive layer as shown in Fig. 2.

Fig. 4 is a graph of optical absorption characteristic of a thin film photoelectric converter as shown in Fig. 1.

Fig. 5 is a graph of external quantum efficiency of a thin film photoelectric converter as shown in Fig. 1.

Fig. 6 is a graph referenced for deriving effective optical path length of a photoelectric conversion layer in a thin film photoelectric converter as shown in Fig. 1.

Fig. 7 is a schematic sectional view of a thin film photoelectric converter according to an embodiment of the present invention.

Fig. 8 is a transmission electron micrograph showing in section a thin film structure in a thin film photoelectric converter as shown in Fig. 7.

Fig. 9 is a graph illustrating effect of a buffer layer between a polycrystalline photoelectric conversion layer and a metal thin film on spectral sensitivity in each of some thin film photoelectric converters as shown in Fig. 7.

Fig. 10 is a schematic sectional view of a thin film photoelectric converter according to a further embodiment of the present invention.

Best Mode for Carrying Out the Invention

[0017] A thin film photoelectric converter is schematically shown in section in Fig. 1. The thin film photoelectric converter includes, on a glass substrate 1, an underlying conductive layer 2, a reflecting metal layer 3, a polycrystalline photoelectric conversion layer 4, and a transparent conductive layer 5 sequentially formed in this order.

[0018] As underlying conductive layer 2, for example, an n^+ type polycrystalline silicon layer with a high concentration of doped phosphorus can be deposited on glass substrate 1 by thermal CVD at a temperature of 500°C or higher. Here, through suitable adjustment of thermal CVD conditions (e.g. temperature, pressure, flow rate of reactive gas and so on), a free surface 2S of un-

derlying conductive layer 2 can be formed to have a textured structure with fine unevenness. The unevenness may have level differences in the range from 0.05 to 3 μm , for example.

[0019] Reflecting metal layer 3 can be formed by sputtering, for example, by using Ag target and utilizing RF (radio frequency) discharge in Ar gas at a pressure in the range from 0.1 to 50 mTorr. As for the target, materials other than Ag, such as Au, Cu, Al or Pt, or an alloy including at least one thereof can be used. Reflecting metal layer 3 thus formed has a free surface 3S having fine unevenness corresponding to the interface 2S with underlying conductive layer 2.

[0020] Substantially polycrystalline photoelectric conversion layer 4 can be formed by plasma CVD. Here, the term "substantially polycrystalline" means not only a completely polycrystalline material but also a polycrystalline material with small amorphous parts. Photoelectric conversion layer 4 can be formed, for example, of polycrystalline silicon of which crystallized volume fraction is 80% or higher. Polycrystalline photoelectric conversion layer 4 includes an n -type layer 4n, an i -type layer 4i and a p -type layer 4p deposited in this order. Plasma CVD can be performed, for example, at a pressure in the range from 0.01 to 5 Torr and at a temperature in the range from 50 to 550°C. Here, n -type layer 4n can be formed by plasma CVD using a mixed gas including, for example, phosphine, silane and hydrogen. Then, i -type layer 4i, which is a substantially intrinsic semiconductor, is deposited through plasma CVD using a mixed gas of hydrogen and a silane gas free from any conductivity type determining impurity. Finally, p -type semiconductor layer 4p is deposited through plasma CVD using a mixed gas including diborane, silane and hydrogen.

[0021] In most of the grains in polycrystalline photoelectric conversion layer 4 thus formed, $\langle 110 \rangle$ direction is approximately parallel to the thickness direction of the photoelectric conversion layer with a deviation angle of at most about 15°.

[0022] Polycrystalline photoelectric conversion layer 4 is grown to an average thickness in the range from about 0.5 to 20 μm and has a textured surface structure with fine unevenness on its free surface 4S. The unevenness on 4S includes V-shaped depressions and pyramid-shaped protrusions with level differences smaller than half of the average thickness of photoelectric conversion layer 4 and in the range from about 0.05 to 3 μm .

[0023] Further on polycrystalline photoelectric conversion layer 4, a layer 5 of transparent conductive oxide (TCO) such as indium tin oxide (ITO) is formed as a transparent electrode.

[0024] In polycrystalline photoelectric conversion layer 4 as shown in Fig. 1, rays of light are refracted at the uneven surface 4S, obliquely enter the layer, and then are reflected in multiple directions between interface 3S and uneven surface 4S, whereby effective optical path length is increased and, with the thin film, a large amount of light absorption can be obtained. The control of density

and level differences of unevenness on interface 4S can be achieved through adjustment of plasma CVD conditions (temperature, pressure, gas flow rate, radio frequency power and so on) for polycrystalline photoelectric conversion layer 4, whereby making it possible to select a wavelength range of light preferentially scattered in photoelectric conversion layer 4. When light rays of longer wavelengths are preferentially scattered in polycrystalline photoelectric conversion layer 4, an amount of light absorption for light rays of longer wavelengths can be increased in particular.

[0025] When light enters a solid medium from atmosphere, a large scattering effect is caused by the strong interaction between light and the uneven surface structure having the level differences nearly equal to the wavelength. In a medium with a high refractive index n such as silicon, a wavelength of a propagated ray of light is reduced to $1/n$. Hence, to scatter the light reaching interface 3S or uneven surface 4S in photoelectric conversion layer 4 and to again confine the light in photoelectric conversion layer 4, a preferable size of unevenness is in the range corresponding to $1/n$ times the light wavelength in the atmosphere. More preferably, the level differences of unevenness on interface 3S and uneven surface 4S are in the range from 0.08 to 1 μm .

[0026] When the polycrystalline silicon thin film is 2 μm in thickness, for example, among incident light, light rays of wavelengths more than about 500nm will reach the back surface of the silicon thin film and are confined between the front and back surfaces by multiple reflection. Meanwhile, the wavelength of light which can be absorbed by silicon and thus substantially contribute to photoelectric conversion is about 1000nm at the longest. As the refractive index n of silicon film is about 3.5 for the light rays of the wavelengths in the range from 500 to 1000nm, a still more preferable size of unevenness on the textured surface for allowing a large light scattering effect is in the range from about 75 to 175% of $1/n$ times the wavelength, in other words, a size in the range from 0.1 to 0.5 μm is optimal.

[0027] In addition, a transparent conductive oxide layer can be formed as an underlying conductive layer 2. A photograph taken by a transmission electron microscope (TEM) is shown in Fig. 2, which shows a textured surface structure of SnO_2 doped with F and deposited on glass substrate 1 at 500°C by thermal CVD at atmospheric pressure. Uneven surface 2S of underlying conductive layer 2 can be clearly observed in Fig. 2.

[0028] A graph of Fig. 3 shows measurement results obtained by the atomic force microscope (AFM) regarding the distribution of unevenness on the textured surface structure of underlying conductive layer 2 as shown in Fig. 2. In this graph, the ordinate shows the depth (nm) measured with the highest point of the surface as a reference point (zero level), and the abscissa shows the relative frequency of existence of free surface at every level. The measurement was achieved by scanning a square area of 5000nm by 5000nm lengthwise and cross-

wise with a probe. It can be seen from the graph in Fig. 3 that the average level of uneven surface 2S is about 159nm deep and the deepest level is about 318nm deep from the referenced zero level.

[0029] An optical absorption characteristic of the thin film photoelectric converter as shown in Fig. 1 is plotted in a graph of Fig. 4. In this graph, the abscissa shows the wavelength (nm) of light and the ordinate shows absorption characteristic (1-R-T). Here, R represents diffuse reflectance and T represents transmittance of the photoelectric converter. Curve 4A shows the characteristics of the thin film photoelectric converter as shown in Fig. 1 and curve 4B shows the absorption characteristic of a thin film photoelectric converter similar to the one in Fig. 1 but not including a textured surface structure and reflecting metal layer 3. As clear from Fig. 4, it is understood that the thin film photoelectric converter having the light confinement structure as shown in Fig. 1 causes a large amount of light absorption in a near-infrared range.

[0030] Fig. 5 shows external quantum efficiency of the thin film photoelectric converter having the light confinement structure as shown in Fig. 1. In Fig. 5, the abscissa represents the light wavelength (nm) and the ordinate represents the external quantum efficiency. Curve 5A shows the external quantum efficiency of the thin film photoelectric converter as shown in Fig. 1 and curve 5B shows the efficiency of a photoelectric converter similar to the one in Fig. 1 but not including a textured structure and reflecting metal film 3. As clear from Fig. 5, compared with the device not having the light confinement structure, the thin film photoelectric converter having the light confinement structure as shown in Fig. 1 exhibits a high external quantum efficiency in a wide range of wavelength from 360 to 1200nm, and shows a particularly high external quantum efficiency of about 50% for light of long wavelength of 800nm.

[0031] Fig. 6 shows measurement results of an optical characteristic of a photoelectric converter including polycrystalline photoelectric conversion layer 4 of 3 μm thickness having a textured surface structure. In Fig. 6, the abscissa represents the absorption length (nm) which is the inverse of absorption coefficient and the ordinate represents the inverse of internal quantum efficiency. The polycrystalline photoelectric conversion layer whose characteristic is shown in this graph has an effective optical path length of 260 μm . This means that the effective optical path length of the polycrystalline photoelectric conversion layer is increased to 120 times the film thickness. Here, the effective optical path length can be obtained as the inverse of slope of the line representing the relation between the absorption length derived from absorption coefficient of monocrystalline silicon and the inverse of internal quantum efficiency of photoelectric converter in the wavelength range more than 1000nm.

[0032] As an example for the thin film photoelectric converter having the structure as shown in Fig 1, on a TCO layer 2, an Ag thin film 3 of 300nm thickness; a polycrystalline photoelectric conversion layer 4 of 2 μm

thickness including 0.5 atomic % hydrogen; and a transparent electrode 5 of an 80nm thick ITO layer were sequentially deposited. In the photoelectric converter thus formed, short-circuit current was 28mA, open-circuit voltage was 0.45V and conversion efficiency was 9%.

[0033] Here, in the thin film photoelectric converter having the structure as shown in Fig. 1, if polycrystalline photoelectric conversion layer 4 formed by plasma CVD contains hydrogen in the range of at least 0.1 atomic % and at most 30 atomic %, the open-circuit voltage can be 0.45V or higher, and if it contains hydrogen in the range of at least 1 atomic % and at most 15 atomic %, the open-circuit voltage can be 0.5V or higher. Hydrogen atoms in the polycrystalline photoelectric conversion layer are believed to be capable of reducing defects in the photoelectric conversion layer. The hydrogen atom content can be evaluated by a secondary ion mass spectroscopy method.

[0034] In Fig. 1, though the thin film photoelectric converter includes underlying conductive layer 2, the underlying conductive layer is not essential to the present invention. If a relatively thick (about 300 to 500nm) Ag layer 3 is directly deposited on glass substrate 1, the surface of the Ag layer 3 can be formed to have a textured surface with fine unevenness regardless of the absence of underlying layer 2. Such an Ag layer can be formed by vacuum evaporation at a substrate temperature of 200 to 300°C. In addition, considering the adhesion property between Ag layer 3 and glass substrate 1, it is preferable that a Ti thin film layer of about 50nm thickness be interposed between Ag layer 3 and glass substrate 1. Such a Ti layer can be formed, for example, by sputtering or evaporation.

[0035] A schematic sectional view of a thin film photoelectric converter of the present invention is shown in Fig. 7. The photoelectric converter of Fig. 7 is similar to the one in Fig. 1, but includes a transparent conductive buffer layer 3a between polycrystalline photoelectric conversion layer 4 and reflecting metal layer 3. Buffer layer 3a serves to reduce recombination of carriers and to enhance the effect of confinement of the light reflected from metal layer 3 in photoelectric conversion layer 4. For buffer layer 3a, at least one of transparent conductive materials ZnO, In_2O_3 , SnO_2 , and CdO or at least one of transparent semiconductor materials Fe_2O_3 , TiO, ZnSe and ZnS can be employed. As an example, a thin film photoelectric converter having a structure as shown in Fig. 7 was formed by sequentially depositing on a TCO layer 2, an Ag thin film 3 of 300nm thickness; a buffer layer 3a of a ZnO thin film of 80 nm thickness; a polycrystalline photoelectric conversion layer 4 of 2 μm thickness; and a transparent electrode 5 of an 80nm thick ITO layer. In this photoelectric converter, short-circuit current was 30mA, open-circuit voltage was 0.49V and conversion efficiency was 11%.

[0036] Fig. 8 shows an example of TEM photograph of the thin film photoelectric converter as shown in Fig. 7. A white segment in a bottom portion represents a

length of 200nm. In the TEM photograph of Fig. 8, not only a textured surface structure with fine unevenness but also columnar crystals extending in $\langle 110 \rangle$ direction in polycrystalline photoelectric conversion layer 4 can be observed. The $\langle 110 \rangle$ direction of the columnar crystals is approximately parallel to the thickness direction of the photoelectric conversion layer with a deviation angle of at most about 15°.

[0037] Fig. 9 is a graph showing an effect of the thickness of ZnO buffer layer 3a on spectral sensitivity of the thin film photoelectric converter as shown in Fig. 7. In this graph, the abscissa represents the wavelength (nm) of light and the ordinate represents the spectral sensitivity (A/W). Here, W represents energy of incident light in Watt, and A represents current supplied from the photoelectric converter in ampere. Incidentally, Ti was employed for underlying conductive layer 2. Curves 9A, 9B and 9C correspond to photoelectric converter having ZnO buffer layers 3a of 80nm, 1000nm and 0nm in thickness, respectively. Comparison of curve 9A and curve 9C clearly shows significant increase in spectral sensitivity in the photoelectric converter having 80nm thick ZnO buffer layer 3a, compared with that not having such a buffer layer. On the other hand, comparison of curves 9A and 9B clearly shows decrease in spectral sensitivity in the device having ZnO buffer layer 3a of an increased thickness of 1000nm compared with the device having the 80nm thick ZnO buffer layer. Therefore, the preferable range of the thickness of buffer layer 3a is from 0.005 to 0.15 μm .

[0038] Fig. 10 is a schematic sectional view of a thin film photoelectric converter according to a still further embodiment of the present invention. The photoelectric converter of Fig. 10, though similar to the one in Fig. 7, includes an amorphous photoelectric conversion layer 6 interposed between polycrystalline photoelectric conversion layer 4 and transparent electrode layer 5. In other words, the thin film photoelectric converter of Fig. 10 is a tandem type thin-film photoelectric converter having amorphous photoelectric conversion layer 6 formed on polycrystalline photoelectric conversion layer 4. Amorphous photoelectric conversion layer 6 includes an n-type amorphous silicon layer 6n, a substantially intrinsic amorphous silicon layer 6i and a p-type amorphous silicon layer 6p. Similarly as in the photoelectric converter of Fig. 7, a tandem thin film photoelectric converter was formed which included a reflecting metal layer 3 of a 300nm thick Ag thin film; a buffer layer 3a of an 80nm thick ZnO thin film; polycrystalline photoelectric conversion layer 4 of 2 μm thickness including 0.5 atomic % hydrogen; a 0.4 μm thick amorphous photoelectric conversion layer 6; and a transparent electrode 5 of an 80nm thick ITO layer.

[0039] In the tandem photoelectric converter, short-circuit current was 13.5mA, open-circuit voltage was 1.4V and conversion efficiency was 13.5%. Compared with the thin film photoelectric converter of Fig. 7, in the tandem thin film photoelectric converter of Fig. 10, the short-

circuit current decreases and the open-circuit voltage increases. In addition, as light rays of short wavelengths are efficiently absorbed by amorphous photoelectric conversion layer 6 while light rays of longer wavelengths can be absorbed by polycrystalline photoelectric conversion layer 4, the photoelectric conversion efficiency is greatly improved.

Industrial Applicability

[0040] As can be seen from the foregoing, the present invention can provide a thin film photoelectric converter with an improved light absorption coefficient especially in the longer wavelength range. With this thin film photoelectric converter, large short-circuit current, high open-circuit voltage and high photoelectric conversion efficiency can be obtained.

Claims

1. A thin film photoelectric converter comprising:

a photoelectric conversion layer (4) including first (4S) and second (3S) main surfaces, at least said first main surface including a textured surface structure; and
a metal thin film (3) covering said second main surface;

characterised in that:

said photoelectric conversion layer is a polycrystalline photoelectric conversion layer formed of a substantially polycrystalline silicon thin film and has an average thickness in the range from 0.5 to 20 μm , and the textured structure has fine unevenness with level differences smaller than half of said average thickness and substantially in the range from 0.05 to 3 μm ; and
a transparent conductive or a transparent semiconductive buffer layer (3a) is interposed between said metal thin film (3) and said polycrystalline photoelectric conversion layer (4).

2. The thin film photoelectric converter according to claim 1, wherein $\langle 110 \rangle$ direction of most of grains in said polycrystalline photoelectric conversion layer is approximately parallel to the direction of said thickness with a deviation angle at most 15° .

3. The thin film photoelectric converter according to claim 1, wherein said second main surface has a textured surface structure and the textured structure includes fine unevenness with level differences smaller than half of said average thickness and substantially in the range from 0.05 to 3 μm .

4. The thin film photoelectric converter according to claim 1, wherein said polycrystalline photoelectric conversion layer is polycrystalline silicon having crystallized volume fraction of at least 80% and hydrogen content of at least 0.1 atomic % and at most 30 atomic %.

5. The thin film photoelectric converter according to claim 1, wherein said polycrystalline photoelectric conversion layer has an average thickness in the range from 1 to 10 μm , and the textured surface structure of at least one of said first and second main surfaces has fine unevenness with level differences in the range from 0.08 to 1 μm .

6. The thin film photoelectric converter according to claim 5, wherein the textured surface structure of at least one of said first and second main surfaces has fine unevenness with level differences in the range from 0.1 to 0.5 μm .

7. The thin film photoelectric converter according to claim 1, wherein said metal thin film has a high index of reflection to light with wavelengths ranging from 500 to 1200 nm.

8. The thin film photoelectric converter according to claim 7, wherein at least a surface of said metal thin film facing said polycrystalline photoelectric conversion layer is formed of a material selected from the group consisting of Ag, Au, Cu, Al and Pt, or an alloy thereof.

9. The thin film photoelectric converter according to claim 1, wherein the transparent conductive or a transparent semiconductive buffer layer (3a) interposed between said metal thin film and said polycrystalline photoelectric conversion layer has a thickness in the range from 0.005 to 0.15 μm .

10. The thin film photoelectric converter according to claim 9, wherein said buffer layer includes a material selected from the group consisting of ZnO, In_2O_3 , SnO_2 , Fe_2O_3 , TiO, ZnS and ZnSe.

11. The thin film photoelectric converter according to claim 1, wherein said polycrystalline photoelectric conversion layer with said textured surface structure has an effective optical path length of at least 10 times said average thickness.

12. The thin film photoelectric converter according to claim 1, wherein said polycrystalline photoelectric conversion layer with said textured surface structure has external quantum efficiency of at least 50% for light with wavelength of 800nm.

13. The thin film photoelectric converter according to

claim 1 further comprising an amorphous photoelectric conversion layer (6) of a substantially amorphous silicon thin film deposited on said first main surface of said polycrystalline photoelectric conversion layer.

Patentansprüche

1. Fotoelektrischer Dünnschichtwandler, umfassend:

eine fotoelektrische Wändlerschicht (4) mit ersten (4S) und zweiten (3S) Hauptoberflächen, wobei zumindest die erste Hauptoberfläche eine texturierte Oberflächenstruktur umfasst; und einen dünnen Metallfilm (3), der die zweite Hauptoberfläche bedeckt,
dadurch gekennzeichnet, dass:

die fotoelektrische Wändlerschicht eine polykristalline fotoelektrische Wändlerschicht ist, die aus einem im wesentlichen polykristallinen Silicium-Dünnschicht gebildet ist und eine mittlere Dicke im Bereich von 0,5 bis 20 μm aufweist, und die texturierte Struktur feine Unebenheiten mit Höhenunterschieden aufweist, die kleiner sind als die Hälfte der mittleren Dicke und im wesentlichen im Bereich von 0,05 bis 3 μm liegen; und eine transparente leitende oder transparente halbleitende Pufferschicht (3a) zwischen dem dünnen Metallfilm (3) und der polykristallinen fotoelektrischen Wändlerschicht (4) zwischengelagert ist.

2. Fotoelektrischer Dünnschichtwandler gemäß Anspruch 1, worin die $\langle 110 \rangle$ -Richtung der meisten Körner in der polykristallinen fotoelektrischen Wändlerschicht näherungsweise parallel zur Richtung der Dicke mit einem Abweichungswinkel von höchstens 15° liegt.

3. Fotoelektrischer Dünnschichtwandler gemäß Anspruch 1, worin die zweite Hauptoberfläche eine texturierte Oberflächenstruktur aufweist und die texturierte Struktur feine Unebenheiten mit Höhenunterschieden umfasst, die kleiner sind als die Hälfte der mittleren Dicke und im wesentlichen im Bereich von 0,05 bis 3 μm liegen.

4. Fotoelektrischer Dünnschichtwandler gemäß Anspruch 1, worin die polykristalline fotoelektrische Wändlerschicht aus polykristallinem Silicium besteht, das einen kristallisierten Volumenanteil von mindestens 80 % und einen Wasserstoffgehalt von mindestens 0,1 Atom-% und höchstens 30 Atom-% aufweist.

5. Fotoelektrischer Dünnschichtwandler gemäß Anspruch

1, worin die polykristalline fotoelektrische Wändlerschicht eine mittlere Dicke im Bereich von 1 bis 10 μm aufweist und die texturierte Oberflächenstruktur von zumindest einer von der ersten und der zweiten Hauptoberfläche feine Unebenheiten mit Höhenunterschieden aufweist, die im Bereich von 0,08 bis 1 μm liegen.

6. Fotoelektrischer Dünnschichtwandler gemäß Anspruch 5, worin die texturierte Oberflächenstruktur von zumindest einer von der ersten und der zweiten Hauptoberfläche feine Unebenheiten mit Höhenunterschieden aufweist, die im Bereich von 0,1 bis 0,5 μm liegen.

7. Fotoelektrischer Dünnschichtwandler gemäß Anspruch 1, worin der dünne Metallfilm einen großen Reflexionsindex für Licht mit Wellenlängen im Bereich von 500 bis 1.200 nm aufweist.

8. Fotoelektrischer Dünnschichtwandler gemäß Anspruch 7, worin zumindest die Oberfläche des dünnen Metallfilms, die der polykristallinen fotoelektrischen Wändlerschicht zugewandt ist, aus einem Material gebildet ist, das aus der Gruppe, bestehend aus Ag, Au, Cu, Al und Pt oder einer Legierung hiervon, ausgewählt ist.

9. Fotoelektrischer Dünnschichtwandler gemäß Anspruch 1, worin eine transparente leitende oder transparente halbleitende Pufferschicht (3a), die zwischen dem dünnen Metallfilm und der polykristallinen fotoelektrischen Wändlerschicht zwischengelagert ist, eine Dicke im Bereich von 0,005 bis 0,15 μm aufweist.

10. Fotoelektrischer Dünnschichtwandler gemäß Anspruch 9, worin die Pufferschicht ein Material umfasst, das ausgewählt ist aus der Gruppe, bestehend aus ZnO , In_2O_3 , SnO_2 , Fe_2O_3 , TiO , ZnS und ZnSe .

11. Fotoelektrischer Dünnschichtwandler gemäß Anspruch 1, worin die polykristalline fotoelektrische Wändlerschicht mit texturierter Oberflächenstruktur eine effektive optische Weglänge von mindestens dem 10-fachen der mittleren Dicke aufweist.

12. Fotoelektrischer Dünnschichtwandler gemäß Anspruch 1, worin die polykristalline fotoelektrische Wändlerschicht mit der texturierten Oberflächenstruktur eine externe Quantenausbeute von mindestens 50 % für Licht mit einer Wellenlänge von 800 nm aufweist.

13. Fotoelektrischer Dünnschichtwandler gemäß Anspruch 1, ferner umfassend eine amorphe fotoelektrische Wändlerschicht (6) aus einem im wesentlichen amorphen Silicium-Dünnschicht, der auf der ersten Hauptoberfläche der polykristallinen fotoelektrischen Wändlerschicht angeordnet ist.

Revendications

1. Convertisseur photoélectrique à couche mince comprenant :

une couche de transformation photoélectrique (4) incluant des première (4S) et seconde (3S) surfaces principales, au moins ladite première surface principale incluant une structure de surface texturée ; et
une couche mince de métal (3) recouvrant ladite seconde surface principale ;
caractérisé en ce que :

ladite couche de transformation photoélectrique est une couche de transformation photoélectrique polycristalline formée d'un film mince de silicium sensiblement polycristallin et a une épaisseur moyenne dans la plage de 0,5 à 20 μm , et la structure texturée a une fine inégalité avec des différences de niveau plus petites que la moitié de ladite épaisseur moyenne et sensiblement dans la plage de 0,05 à 3 μm ; et
une couche tampon conductrice transparente ou semi-conductrice transparente (3a) est interposée entre ladite couche mince de métal (3) et ladite couche de transformation photoélectrique (4).

2. Convertisseur photoélectrique à couche mince selon la revendication 1, dans lequel la direction $\langle 110 \rangle$ de la plupart des grains dans ladite couche de transformation photoélectrique polycristalline est à peu près parallèle à la direction de ladite épaisseur avec un angle de déviation au plus de 15° .
3. Convertisseur photoélectrique à couche mince selon la revendication 1, dans lequel ladite seconde surface principale a une structure de surface texturée et la structure texturée inclut une fine inégalité avec des différences de niveau plus petites que la moitié de ladite épaisseur moyenne et sensiblement dans la plage de 0,05 à 3 μm .
4. Convertisseur photoélectrique à couche mince selon la revendication 1, dans lequel ladite couche de transformation photoélectrique polycristalline est du silicium polycristallin ayant une fraction de volume cristallisée d'au moins 80 % et une teneur en hydrogène d'au moins 0,1 % en atomes et d'au plus 30 % en atomes.
5. Convertisseur photoélectrique à couche mince selon la revendication 1, dans lequel ladite couche de transformation photoélectrique polycristalline a une épaisseur moyenne dans la plage de 1 à 10 μm , et la structure de surface texturée d'au moins une des-

dites première et seconde surfaces principales a une fine inégalité avec des différences de niveau dans la plage de 0,08 à 1 μm .

6. Convertisseur photoélectrique à couche mince selon la revendication 5, dans lequel la structure de surface texturée d'au moins une des dites première et seconde surfaces principales a une fine inégalité avec des différences de niveau dans la plage de 0,1 à 0,5 μm .
7. Convertisseur photoélectrique à couche mince selon la revendication 1, dans lequel ledit film mince de métal a un indice de réflexion élevé à une lumière ayant des longueurs d'onde comprises entre 500 et 1 200 nm.
8. Convertisseur photoélectrique à couche mince selon la revendication 7, dans lequel au moins une surface de ladite couche mince de métal faisant face à ladite couche de transformation photoélectrique polycristalline est formée d'une matière sélectionnée dans le groupe constitué par l'Ag, l'Au, le Cu, l'Al et le Pt, ou un alliage de ceux-ci.
9. Convertisseur photoélectrique à couche mince selon la revendication 1, dans lequel la couche tampon conductrice transparente ou semi-conductrice transparente (3a) interposée entre ladite couche mince de métal et ladite couche de transformation photoélectrique polycristalline a une épaisseur dans la plage de 0,005 à 0,15 μm .
10. Convertisseur photoélectrique à couche mince selon la revendication 9, dans lequel ladite couche tampon inclut une matière sélectionnée dans le groupe constitué par le ZnO, l' In_2O_3 , le SnO_2 , le Fe_2O_3 , le TiO, le ZnS et le ZnSe.
11. Convertisseur photoélectrique à couche mince selon la revendication 1, dans lequel ladite couche de transformation photoélectrique polycristalline avec ladite structure de surface texturée a une longueur de chemin optique efficace d'au moins 10 fois ladite épaisseur moyenne.
12. Convertisseur photoélectrique à couche mince selon la revendication 1, dans lequel ladite couche de transformation photoélectrique polycristalline avec ladite structure de surface texturée a un rendement quantique externe d'au moins 50% pour une lumière ayant une longueur d'onde de 800 nm.
13. Convertisseur photoélectrique à couche mince selon la revendication 1, comprenant en outre une couche de transformation photoélectrique amorphe (6) d'un film mince de silicium sensiblement amorphe déposé sur ladite première surface principale de ladite

couche de transformation photoélectrique polycristalline.

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FIG. 1

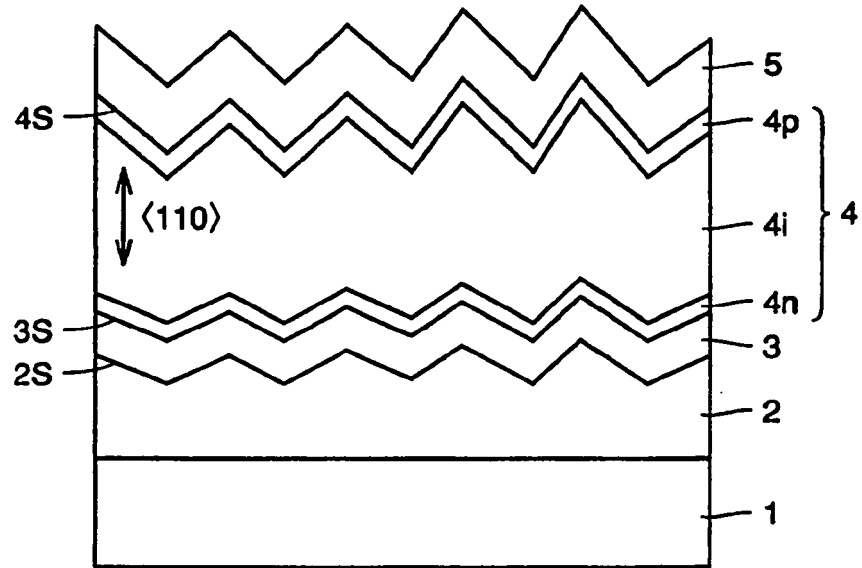


FIG. 7

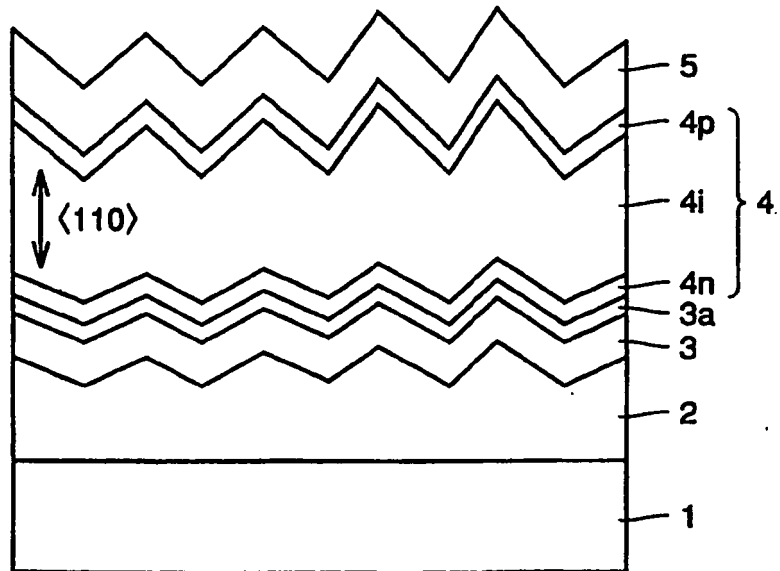


FIG. 2

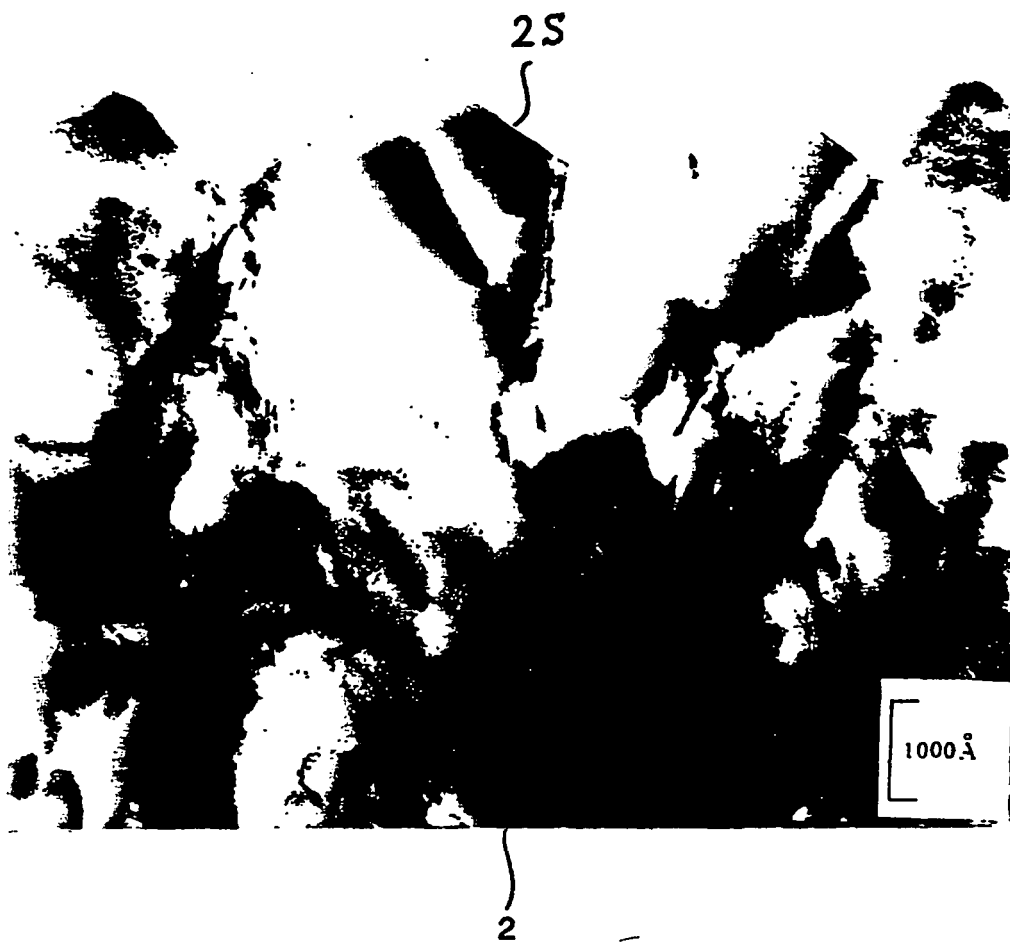


FIG.3

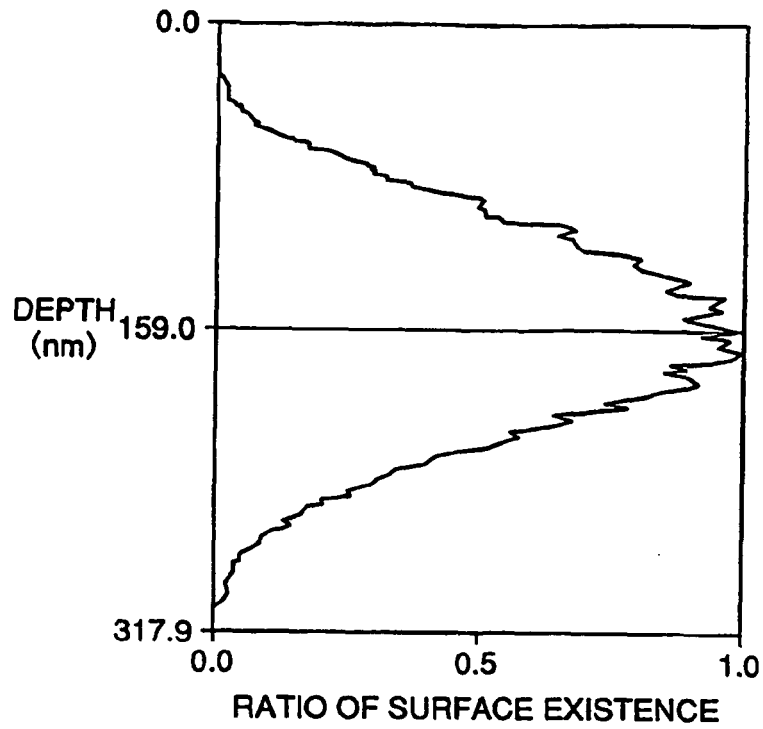


FIG.4

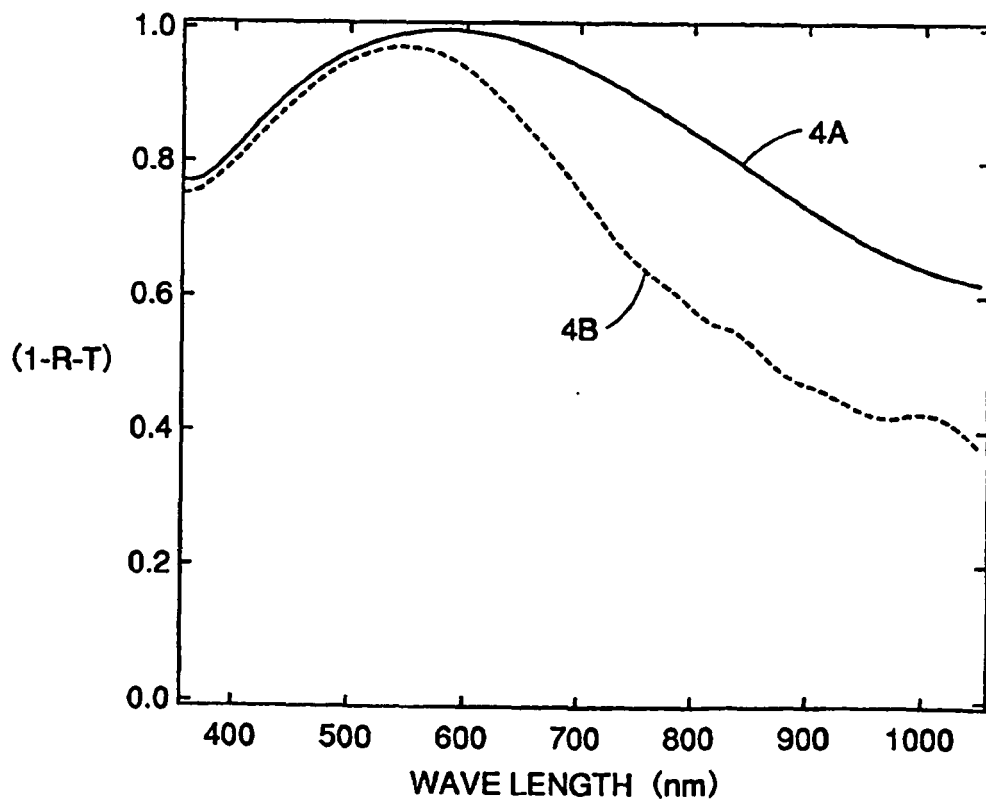


FIG.5

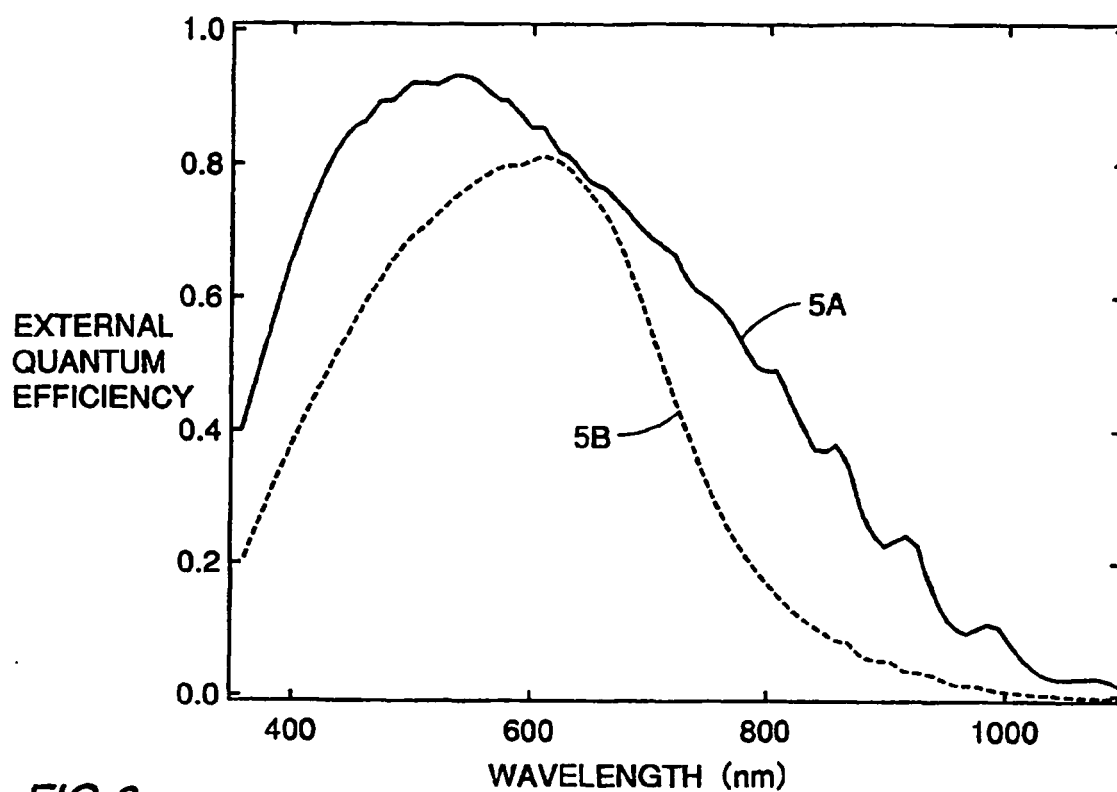


FIG.6

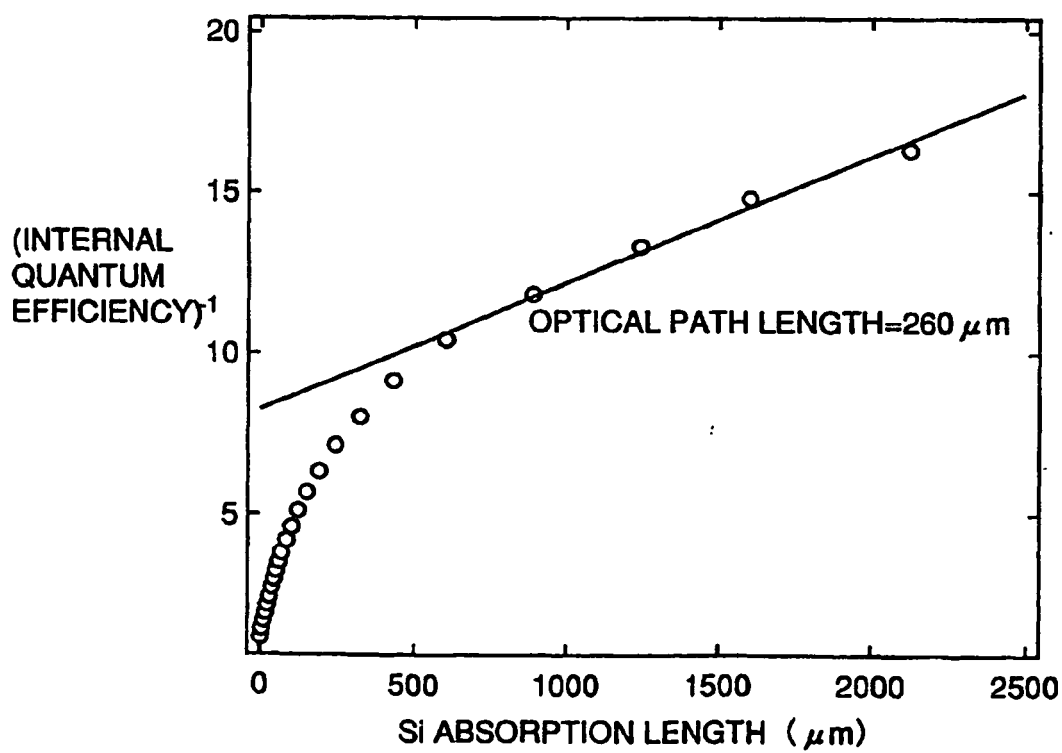


FIG. 8

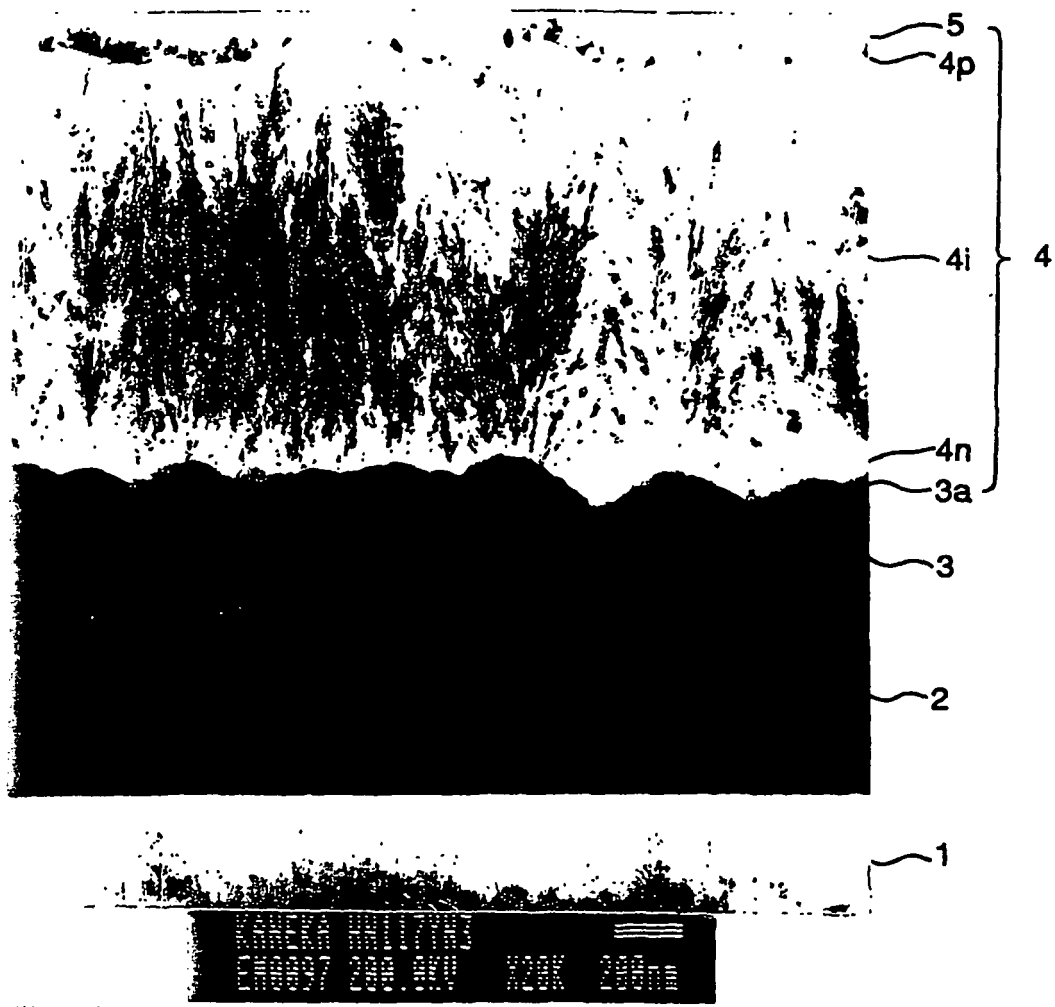


FIG.9

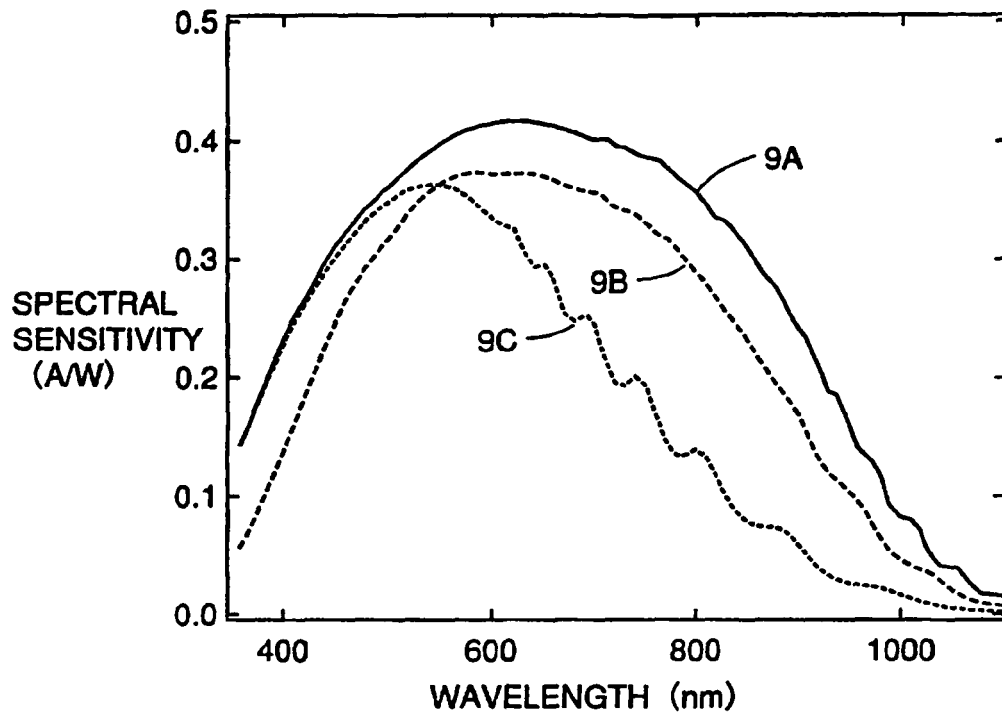
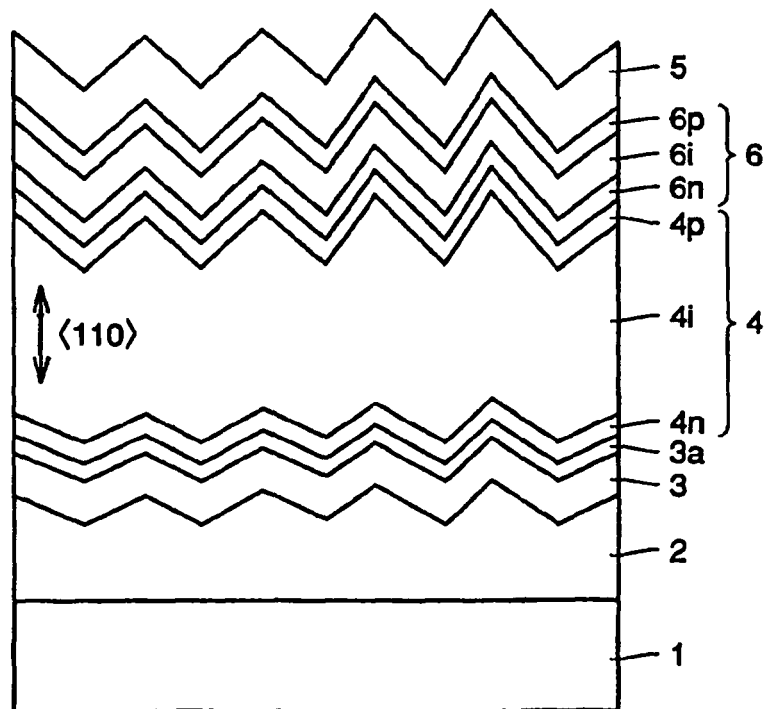


FIG.10



REFERENCES CITED IN THE DESCRIPTION

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