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- **Mark, Brian L. c/o NEC USA, Inc.
Princeton, New Jersey 08540 (US)**
- **Ramamurthy, Gopalakrishnan c/o NEC USA, Inc.
Princeton, New Jersey 08540 (US)**
- **Ishii, Alexander T. c/o NEC USA, Inc.
Princeton, New Jersey 08540 (US)**

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(71) Applicant: **NEC CORPORATION
Tokyo (JP)**

(72) Inventors:
• **Fan, Ruixue c/o NEC USA, Inc.
Princeton, New Jersey 08540 (US)**

(74) Representative:
**Baronetzky, Klaus, Dipl.-Ing. et al
Splanemann Reitzner
Baronetzky Westendorf
Patentanwälte
Rumfordstrasse 7
80469 München (DE)**

(54) **Time based scheduler architecture and method for ATM networks**

(57) A flexible and scalable architecture and method that implements dynamic rate control scheduling in an ATM switch. The scheduler shapes a large number of streams according to rate values computed dynamically based on switch congestion information. To handle a large range of bit rates, a plurality of timewheels are employed with different time granularities. The streams are assigned dynamically to the timewheels based on computed rate values. The shaper architecture and method supports priority levels for arbitrating among streams

which are simultaneously eligible to transmit.

Specifically, a scheduling timestamp is determined by the scheduler in consideration of a dynamic rate varied in dependency upon congestion information, a peak cell rate, and/or a sustainable cell rate, and a burst threshold while a shaping timestamp is also determined with reference to the scheduling timestamp determined by the above-mentioned manner. The scheduler may shape a stream in accordance with a rate determined by an ABR mechanism along with the dynamic rate.

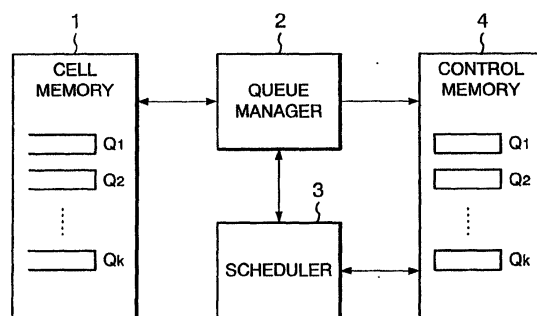


Fig.1



European Patent
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EUROPEAN SEARCH REPORT

Application Number
EP 99 10 5474

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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.5)
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A	OHBA Y: "QLWFQ: A QUEUE LENGTH BASED WEIGHTED FAIR QUEUEING ALGORITHM IN ATMNETWORKS" KOBE, APRIL 7 - 12, 1997, LOS ALAMITOS, CA: IEEE COMPUTER SOC, US, 7 April 1997 (1997-04-07), pages 566-575, XP000859121 ISBN: 0-8186-7782-1 * page 567, left-hand column *	1-33	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			H04L H04Q
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 29 September 2000	Examiner Veen, G
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on

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29-09-2000

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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82