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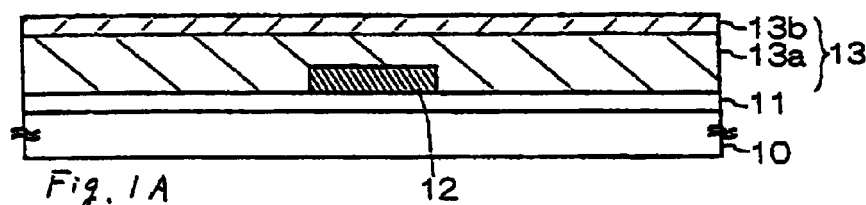
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(54) **Semiconductor device comprising organic resin and process for producing semiconductor device**

(57) In a TFT using a crystalline semiconductor film of a bottom gate type, a gate insulating film is flattened. On a substrate, an underlying film, a gate wiring and a gate insulating film are accumulated in this order. The gate insulating film comprises a flattening film comprising an insulating organic resin film, such as BCB, polyimide and acrylic, and an insulating inorganic film. Because the surface of the gate insulating film is flattened by the flattening film, a flat amorphous semiconductor film can be formed on the surface thereof.

Therefore, in the laser crystallization, since no difference in focal point of the laser light is formed among each position of the semiconductor film, crystallization can be uniformly conducted. Because the edge part of the gate wiring can be covered with the thick flattening film, implantation of an electron or a hole to the gate insulating film, and electrostatic breakage of the gate insulating film can be prevented.



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EUROPEAN SEARCH REPORT

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Place of search BERLIN		Date of completion of the search 27 March 2000	Examiner Polesello, P
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<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons</p> <p>& : member of the same patent family, corresponding document</p>			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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