



(11) **EP 1 020 921 B8**

(12) **CORRECTED EUROPEAN PATENT SPECIFICATION**

(15) Correction information:  
**Corrected version no 1 (W1 B1)**  
**Corrections, see**  
**Bibliography INID code(s) 73**

(51) Int Cl.:  
**H01L 29/10** <sup>(2006.01)</sup> **H01L 29/66** <sup>(2006.01)</sup>  
**H01L 29/78** <sup>(2006.01)</sup>

(48) Corrigendum issued on:  
**26.06.2013 Bulletin 2013/26**

(45) Date of publication and mention  
of the grant of the patent:  
**15.05.2013 Bulletin 2013/20**

(21) Application number: **99310127.8**

(22) Date of filing: **15.12.1999**

(54) **Mixed signal silicon MOS integrated circuit**

MOS Integrierte Schaltkreise aus Silizium für gemischte Signale

Circuit intégré MOS de silicium pour des signaux mixtes

(84) Designated Contracting States:  
**DE FR GB**

(30) Priority: **06.01.1999 US 226378**

(43) Date of publication of application:  
**19.07.2000 Bulletin 2000/29**

(73) Proprietor: **Lucent Technologies Inc.**  
**Murray Hill, NJ 07974-0636 (US)**

(72) Inventors:  
• **Eshraghi, Ali**  
**Princeton NJ 08540 (US)**  
• **Gopinathan, Venugopal**  
**Basking Ridge, NJ 07920 (US)**  
• **Khoury, John Michael**  
**New Providence, NJ 07974 (US)**  
• **Tarsia, Maurice**  
**Colonia, NJ 07067 (US)**  
• **Vuong, Thi-Hong-Ha**  
**Berkeley Heights, NJ 07922 (US)**

(74) Representative: **Williams, David John et al**  
**Page White & Farrer**  
**Bedford House**  
**John Street**  
**London**  
**WC1N 2BF (GB)**

(56) References cited:  
**US-A- 5 218 221 US-A- 5 466 957**  
**US-A- 5 532 508 US-A- 5 536 959**

- **PATENT ABSTRACTS OF JAPAN** vol. 018, no. 255 (E-1548), 16 May 1994 (1994-05-16) -& JP 06 037309 A (TOSHIBA CORP), 10 February 1994 (1994-02-10)
- **MOMIYAMA Y ET AL: "CHANNEL ENGINEERING OF 0.13μm NMOSFET FOR 1.0 CMOS USING GATE POLY-Si OXIDATION AND Laterally profiled, surface concentrated channel technologies" 1998 SYMPOSIUM ON VLSI TECHNOLOGY. DIGEST OF TECHNICAL PAPERS. HONOLULU, JUNE 9 - 11, 1998, SYMPOSIUM ON VLSI TECHNOLOGY, NEW YORK, NY: IEEE, US, vol. CONF. 18, 9 June 1998 (1998-06-09), pages 78-79, XP000802755 ISBN: 0-7803-4771-4**

Note: Within nine months of the publication of the mention of the grant of the European patent in the European Patent Bulletin, any person may give notice to the European Patent Office of opposition to that patent, in accordance with the Implementing Regulations. Notice of opposition shall not be deemed to have been filed until the opposition fee has been paid. (Art. 99(1) European Patent Convention).

**EP 1 020 921 B8**