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(54) System and method for suppressing conducted emissions by a cryptographic device

(57) System and method for performing cryptographic operations. At least one processor (20,22) performing cryptographic operations, memory (28) is coupled to the processor for use in performing the cryptographic operations; and a storage component (24,26) is coupled to the processor for storing and retrieving information calculated and used in the cryptographic operations. The processor, memory and storage component are securely enclosed (34) whereby direct access to the cryptographic operations is prevented. A first power source (30), which is external to the secure enclosure (34), is coupled to and supplies power to the processor, the memory and the storage component. A second power source (40), which is internal to the secure enclosure (34), is coupled to and supplies power to at least the processor. A switch is included for switching from the first power source (30) to the second power source (40) when the cryptographic operations are being performed and for switching from the second power source to the first power source when non-cryptographic operations are being performed.

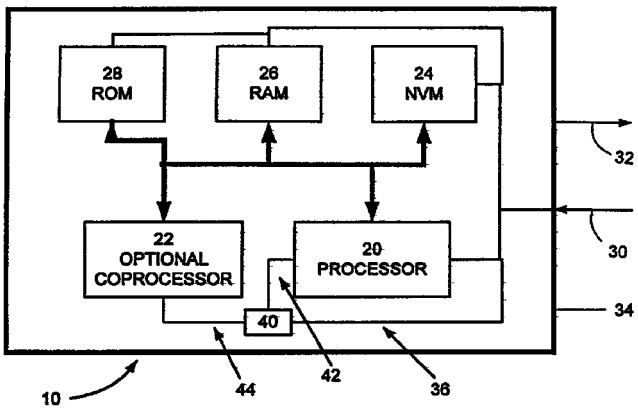

FIG. 3

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EUROPEAN SEARCH REPORT

Application Number
EP 99 12 5306

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			TECHNICAL FIELDS SEARCHED (Int.Cl.7) G06F H04L

The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
THE HAGUE	15 September 2000	Paraf, E

CATEGORY OF CITED DOCUMENTS

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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82